

# Resistance of Threshold Implementations against Statistical Ineffective Fault Attacks

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June 18, 2024

Journée de la Recherche

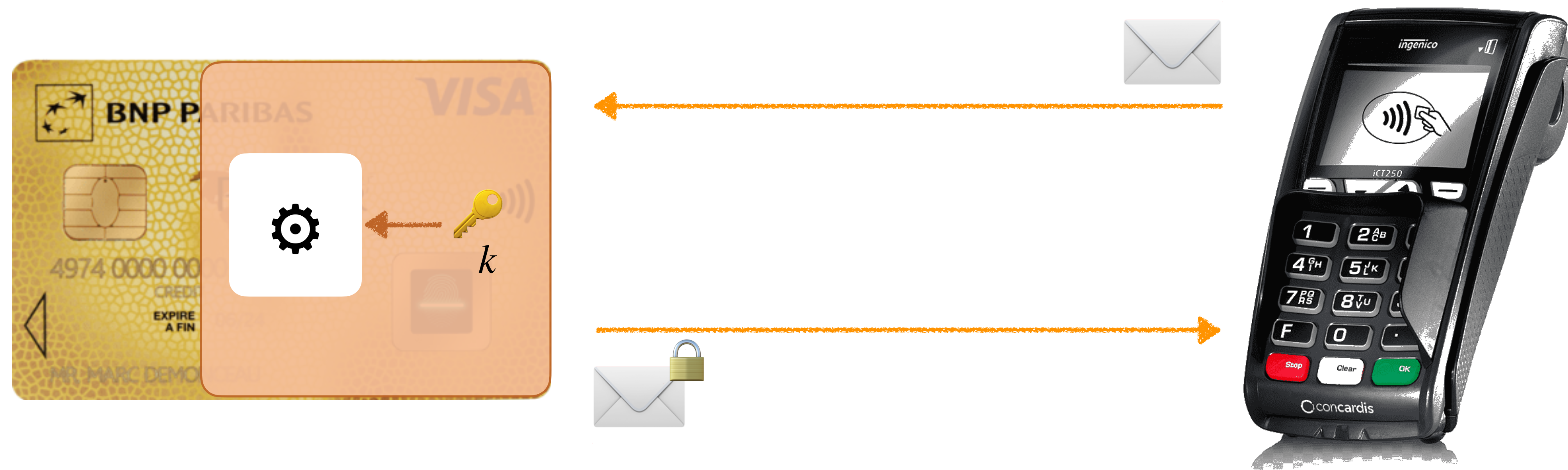
joint work with Vincent Grosso and Pierre-Louis Cayrel



! BIG CHALLENGE !

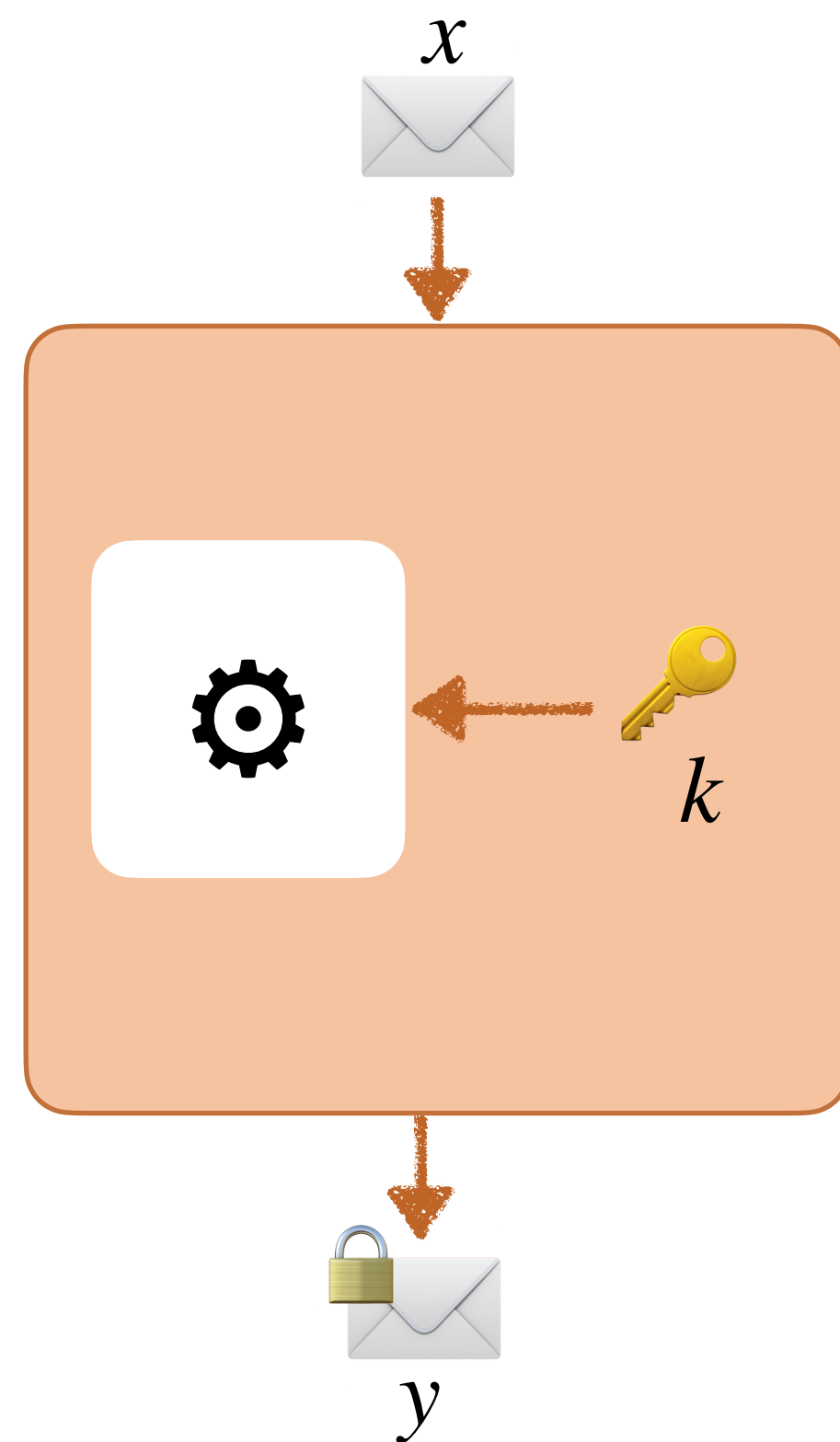
# Security of Data Transmission

# Payment Example

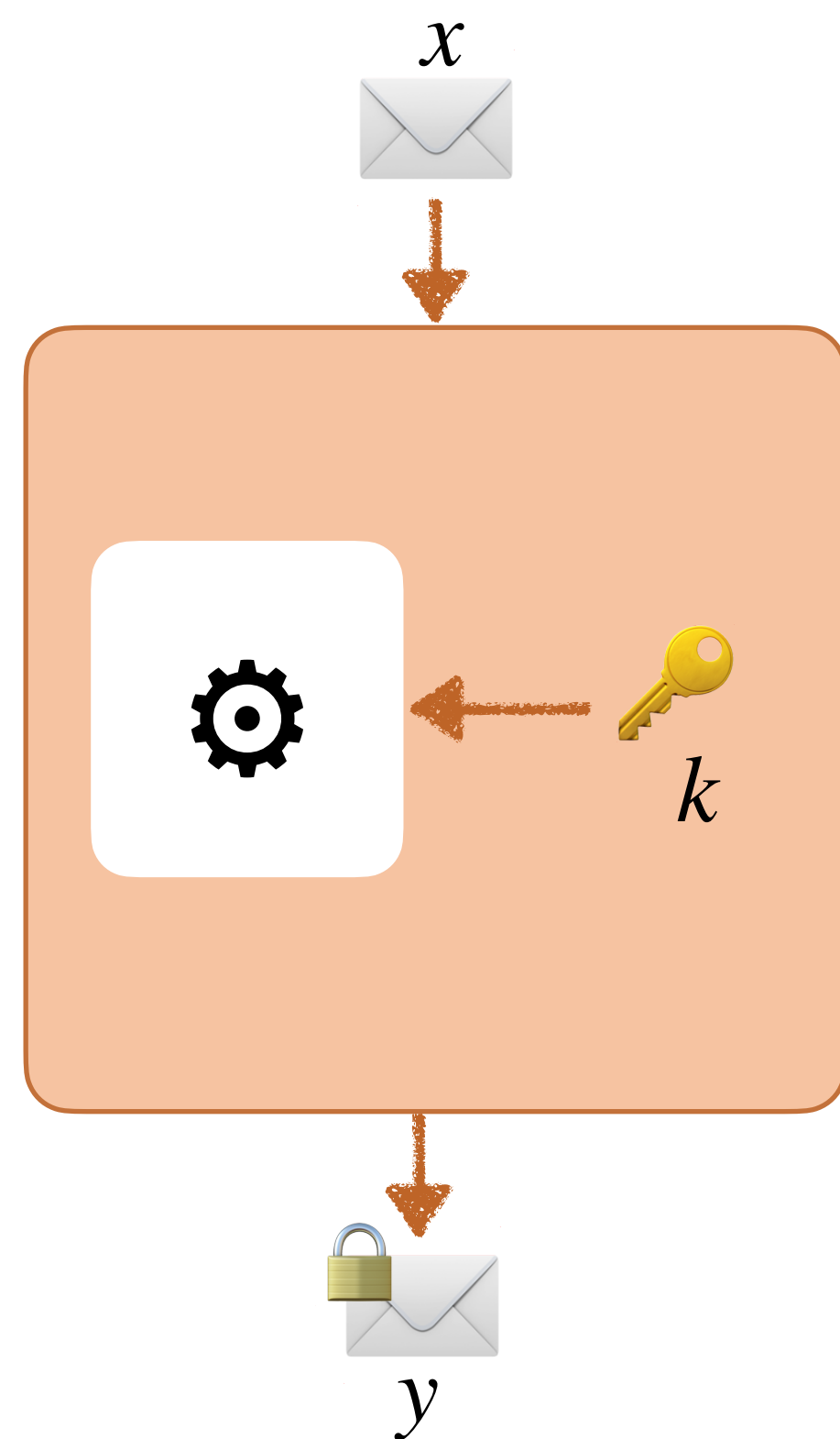


# Cryptography

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# Battle... here it comes!



Attacker

"I steal the key 🗝️"

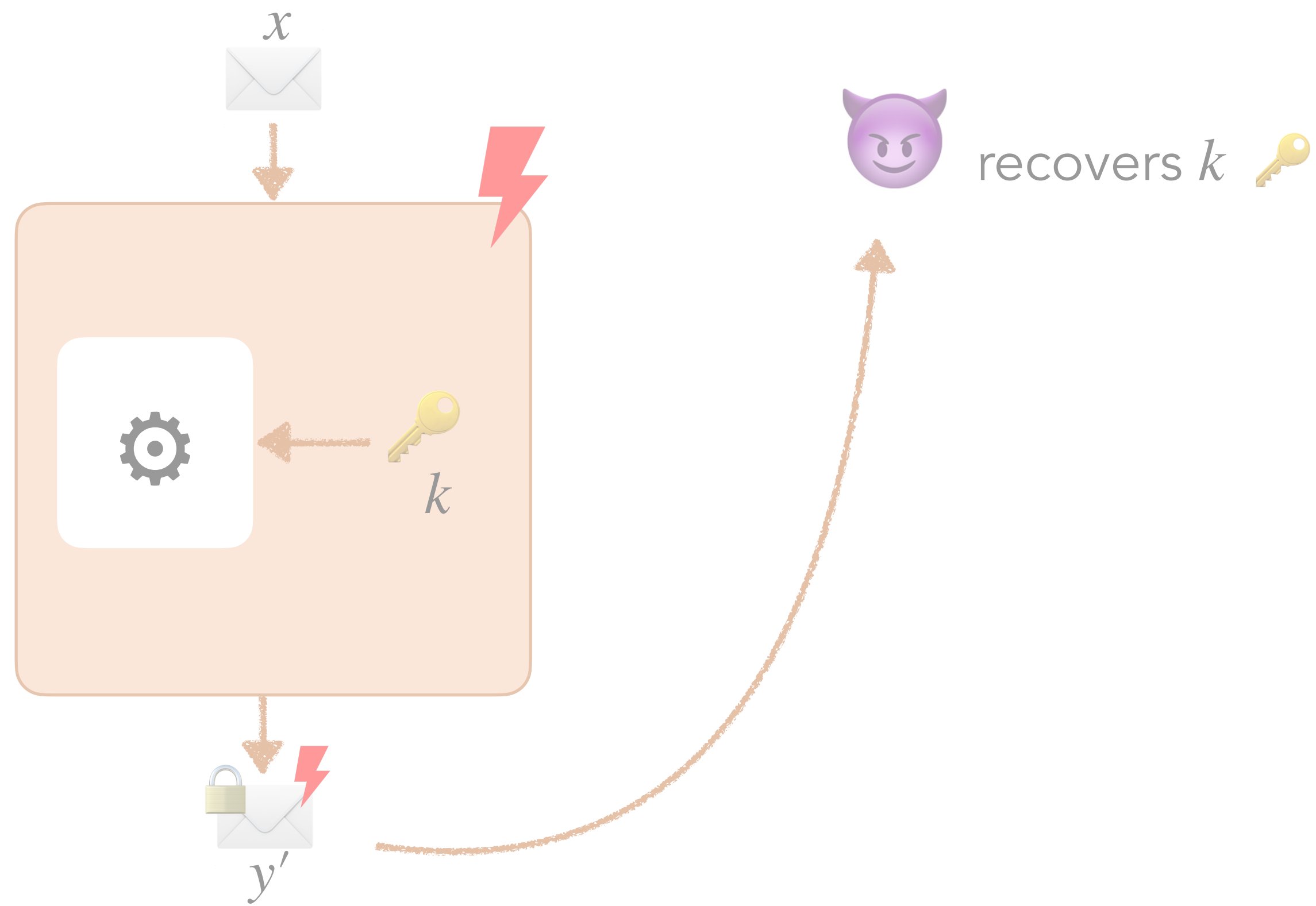


Designer

"I protect the key 🗝️"



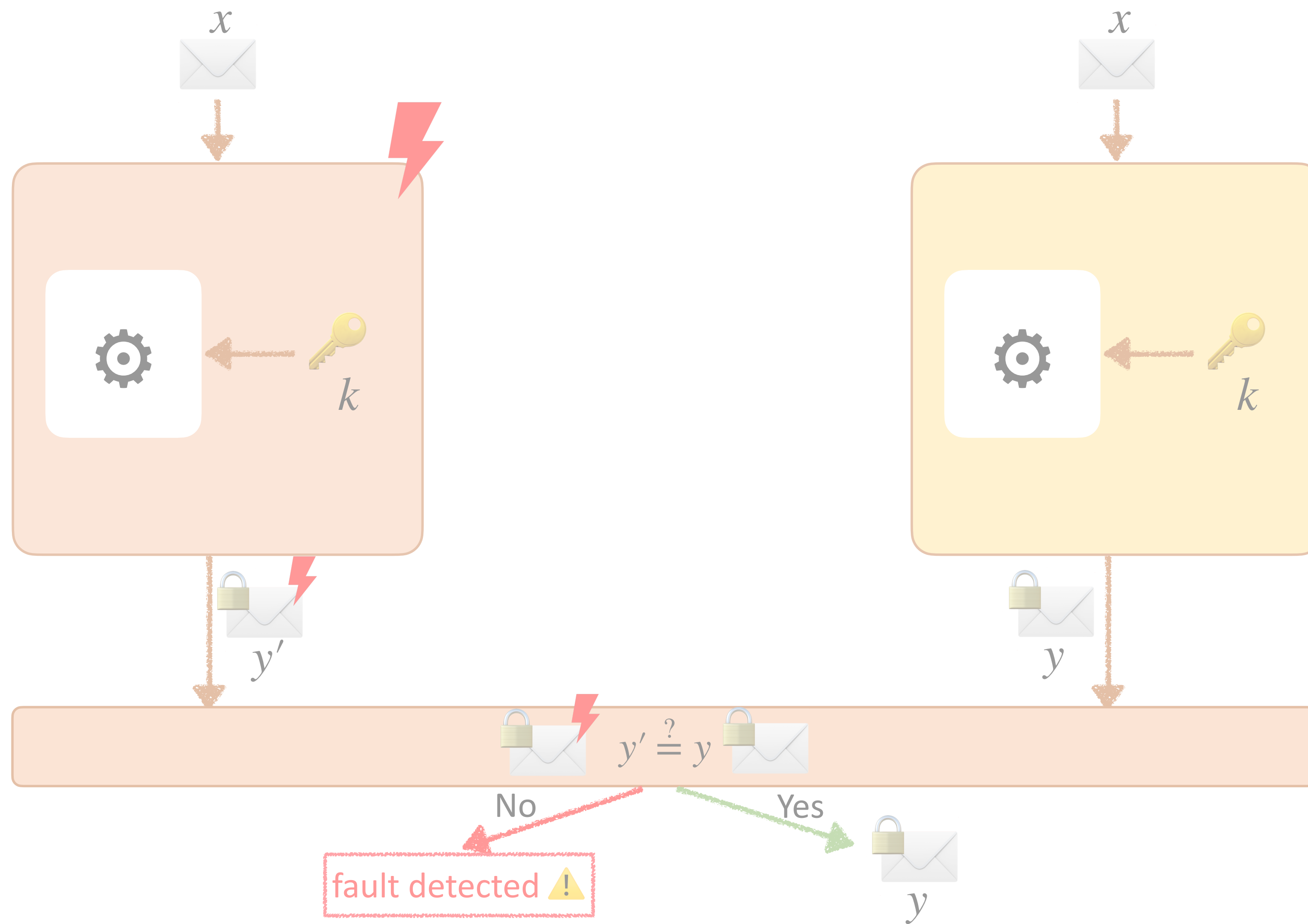
"I have fault attacks"





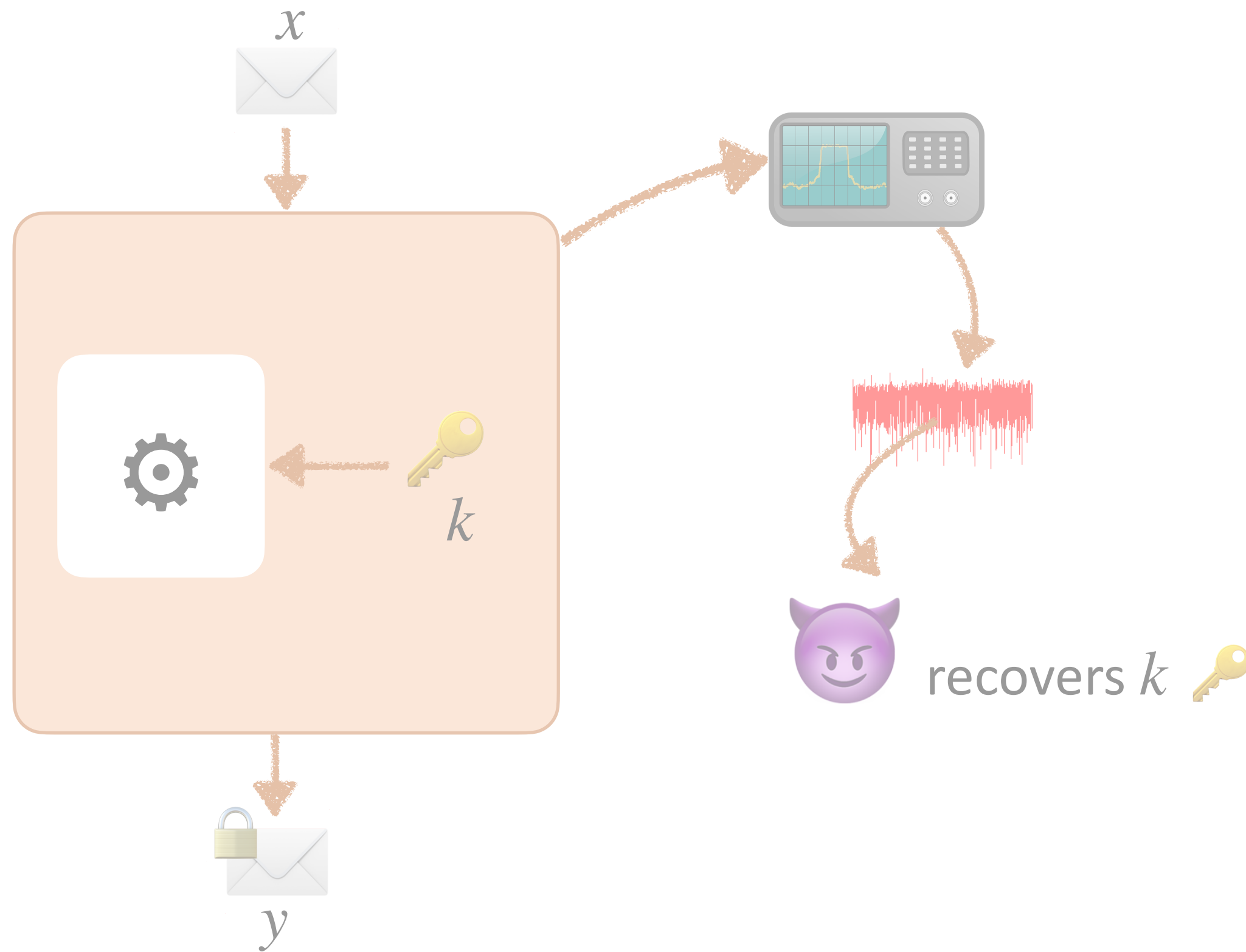
...

"I have countermeasure"

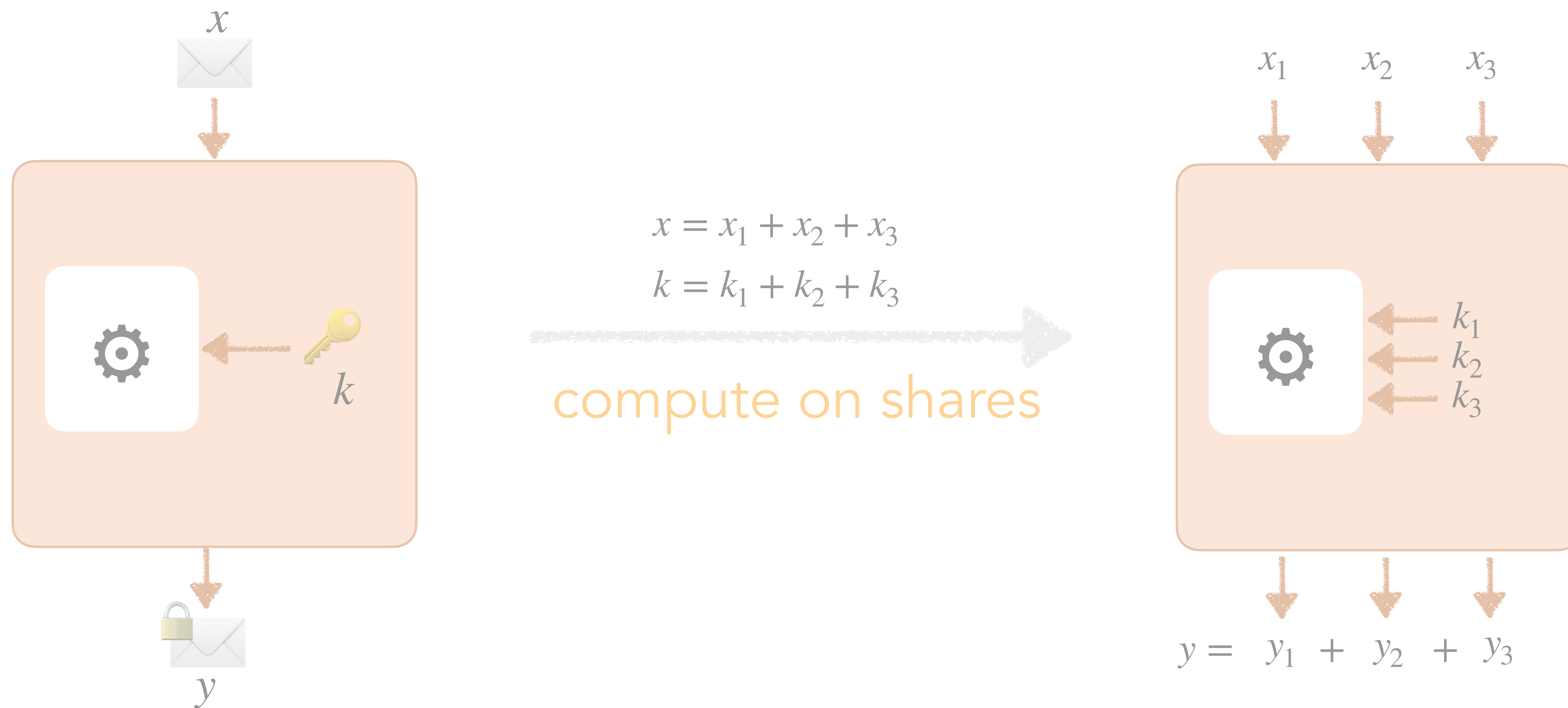




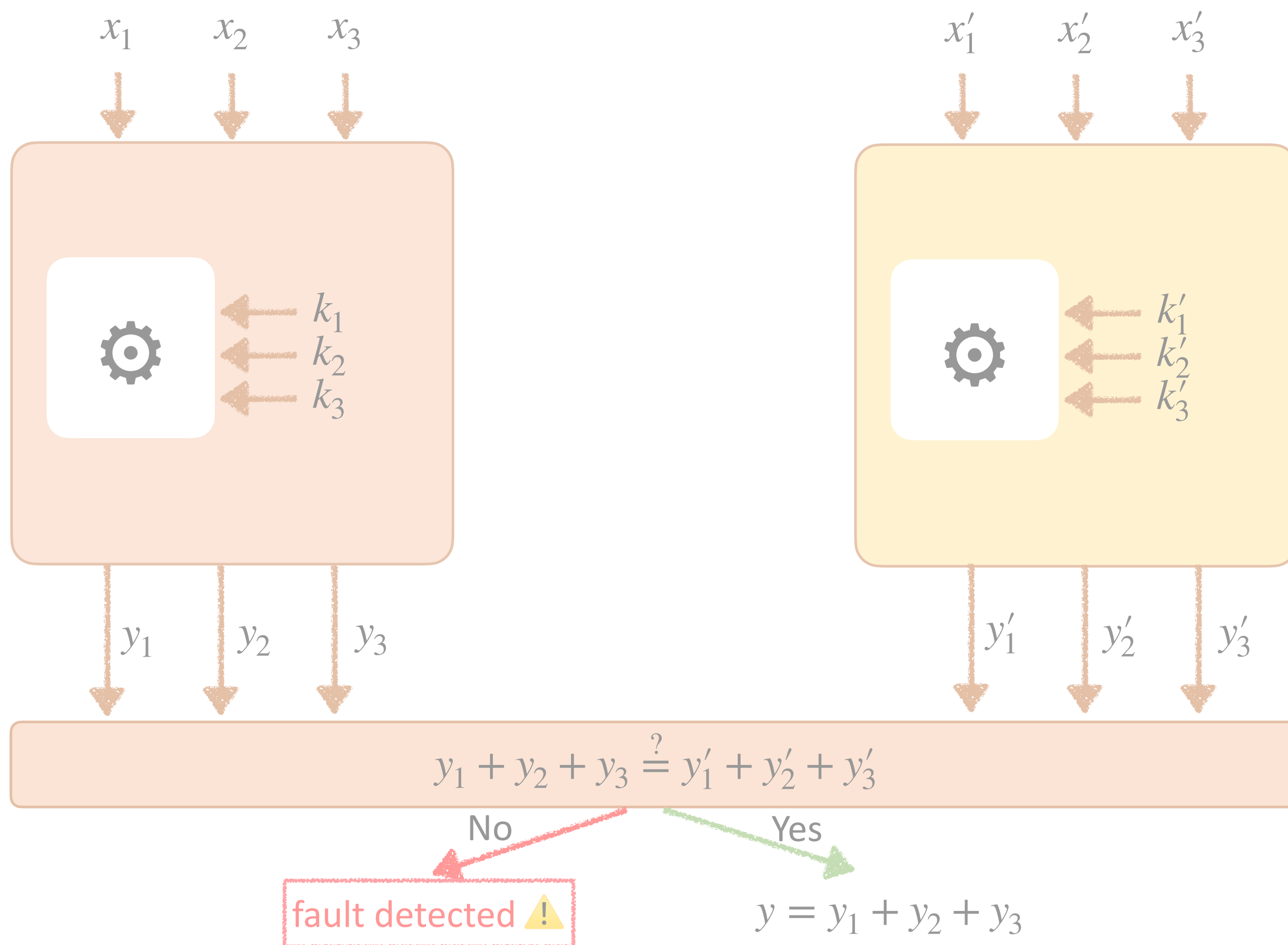
"I have side-channel attacks"







"This is combined countermeasure"

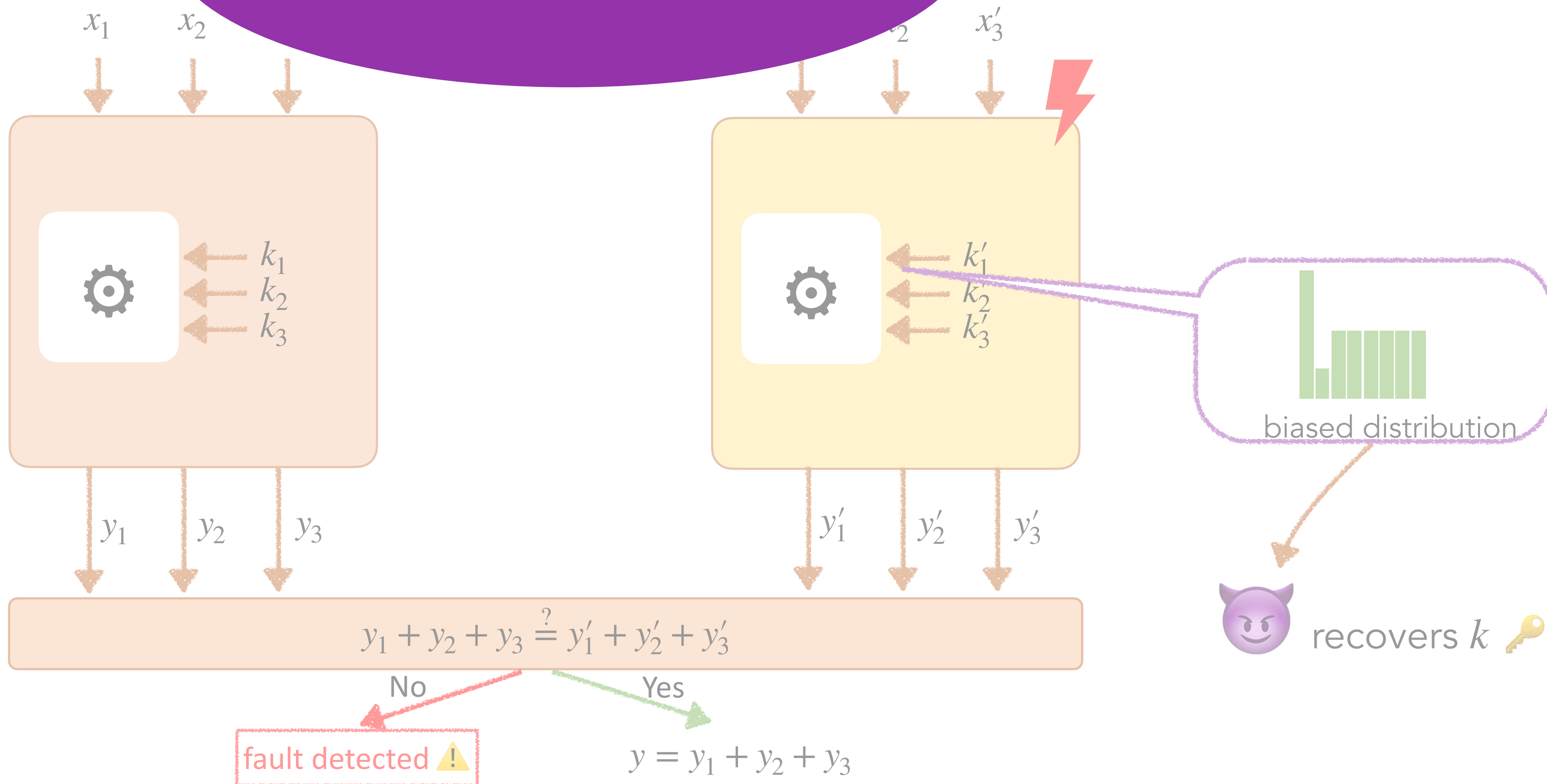




"I have stronger attack"

and countermeasure"

"This is state-of-the-art. Haha"





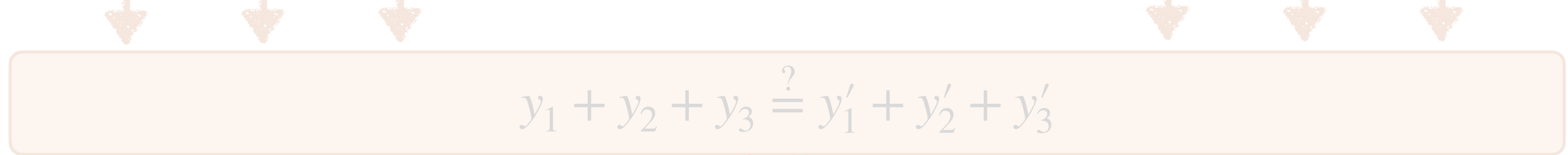
"I have str..."

"I know the reason"

"This is state-of-the-art. Haha"



"It's an implementation mistake"



recovery key  recovers  $k$  

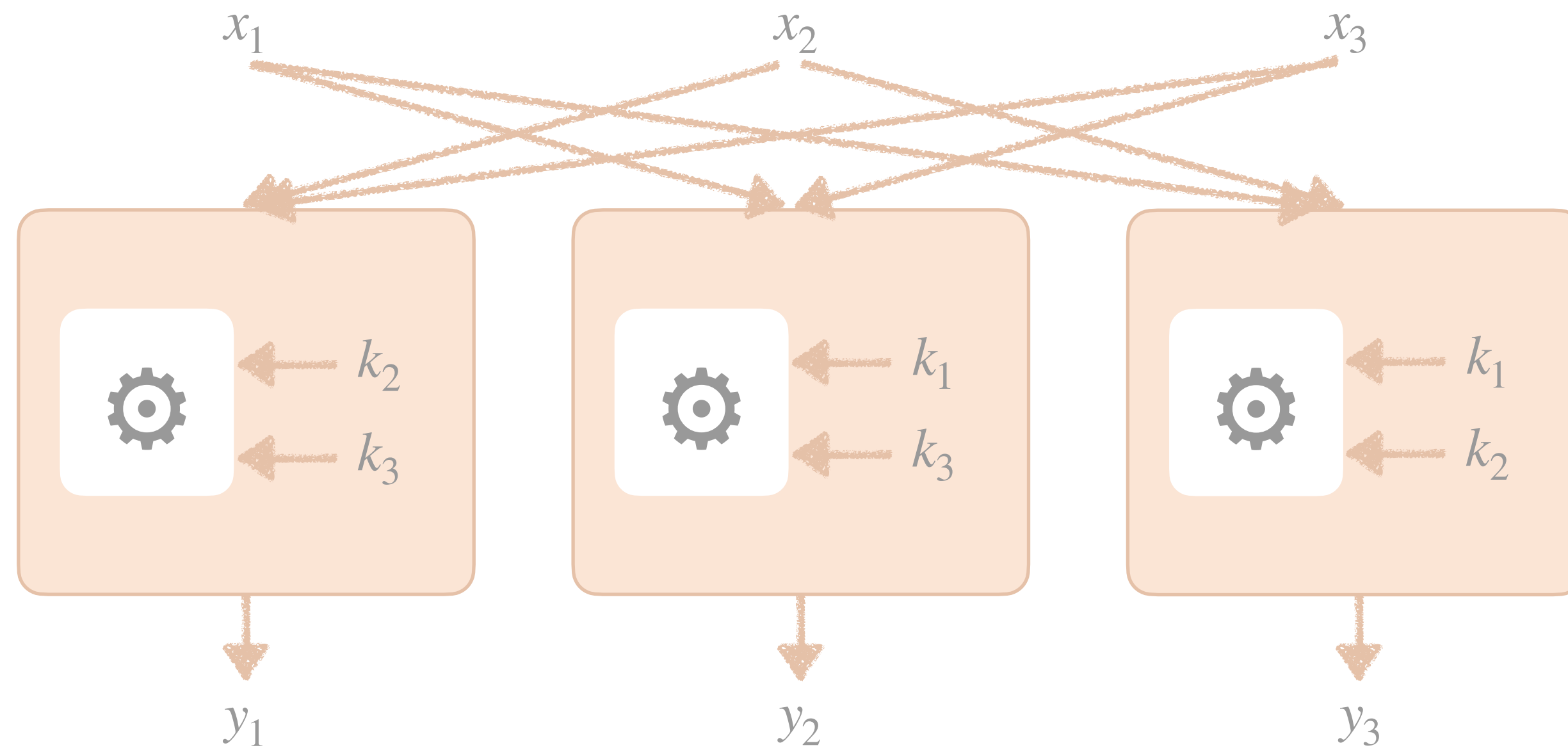


"I have str..."

"I know the reason"

"This is state-o..."

"I have countermeasure.  
Let's implement it properly!"





"I have str

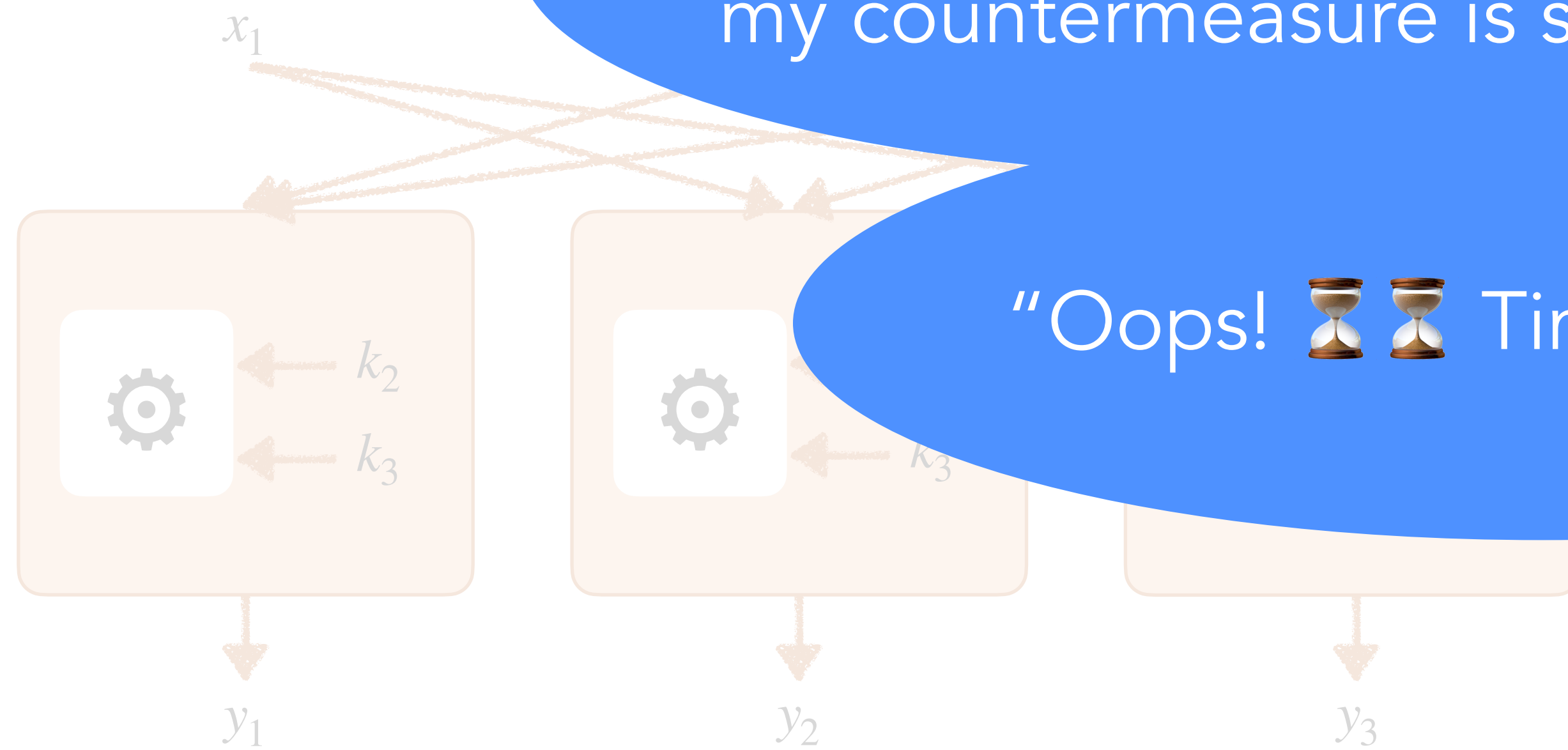
"This is state-o

"I know the reason"

"I have countermeasure.  
Let's implement it properly!"

"I can prove that  
my countermeasure is secure"

"Oops! 🕒🕒 Time's up! 🚨🚨"



"You want to know more?  
contact me 😊"

THANK YOU!



# Natural Resistance of Threshold Implementations against Statistical (Ineffective) Fault Attacks

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