Practical Second-Order CPA Attack on Ascon with Proper Selection Function

Viet-Sang Nguyen

joint work with Vincent Grosso and Pierre-Louis Cayrel

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UNIVERSITÉ DE LYON

UNIVERSITÉ JEAN MONNET SAINT-ÉTIENNE

Laboratoire Hubert Curien
UMR ▪ CNRS ▪ 5516 ▪ Saint-Étienne

anr

PROPHY ANR-22-CE39-0008-01

NIST

2018  Lightweight cryptography competition

NIST

2018  Lightweight cryptography competition

2023  Selected Ascon

NIST

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 Possible attacks

 Secure implementations

In this talk

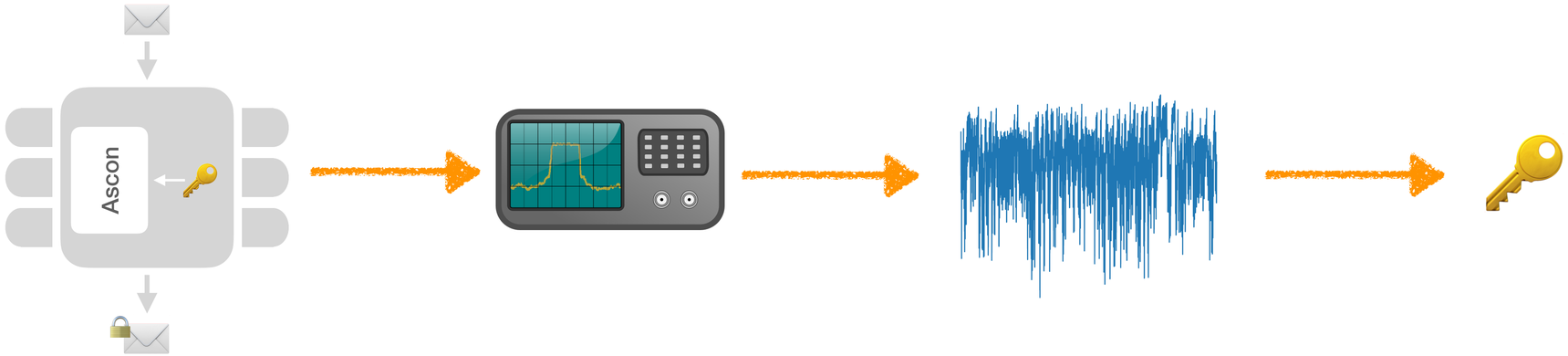
Possible attacks

In this talk

Possible attacks

Correlation Power Analysis (CPA) attack

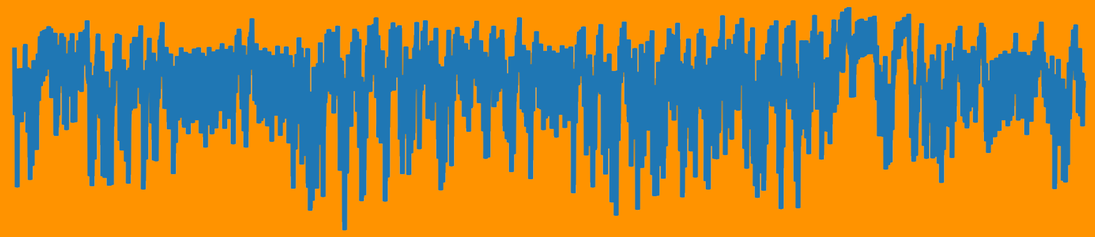
In this talk



Possible attacks

Correlation Power Analysis (CPA) attack

CPA attack



CPA attack

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- Choose attack point: intermediate variable v

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Selection function: $v = f(d, k)$

known non-constant data

part of the key

CPA attack

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Selection function: $v = f(d, k)$

known non-constant data — d

part of the key — k

Well-known CPA on AES: $v = \text{Sbox}(\text{plaintext}, \text{key})$

CPA attack

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- Choose leakage model for v

CPA attack

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- **Choose leakage model for v**

  This work: Hamming weight

CPA attack

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  This work: Hamming weight

  Hypothetical power consumption: $h = \text{HW}(v) = \text{HW}\left(f(d, k)\right)$

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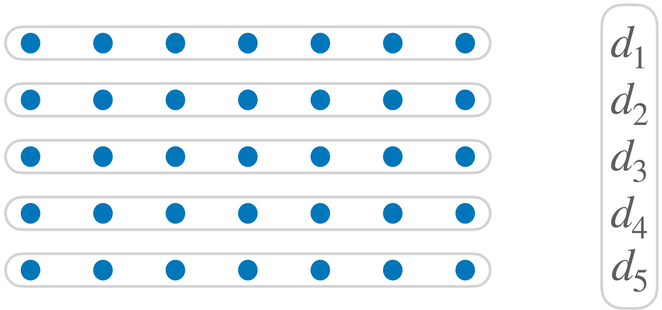

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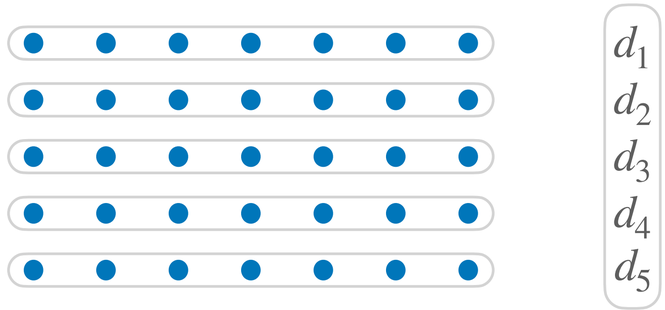

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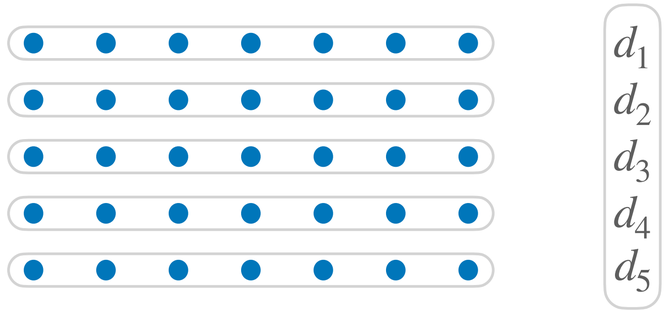

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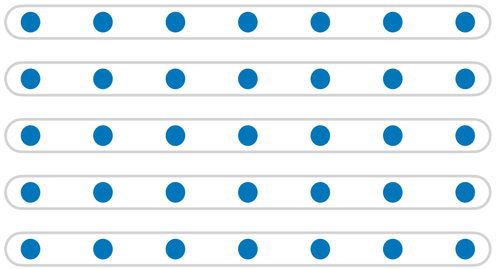

$h = \mathrm{HW}\left(f(d,k)\right)$

	k_1
d_1	h_{11}
d_2	h_{21}
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CPA attack

- Measure power consumption traces and record d
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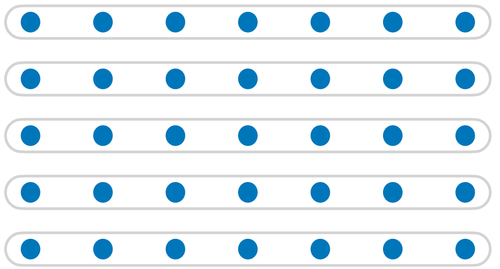

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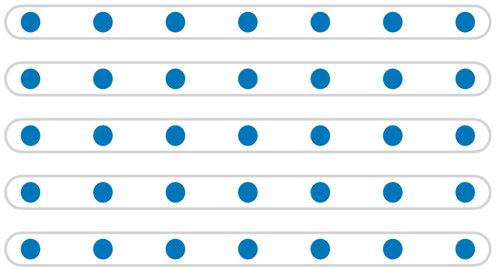

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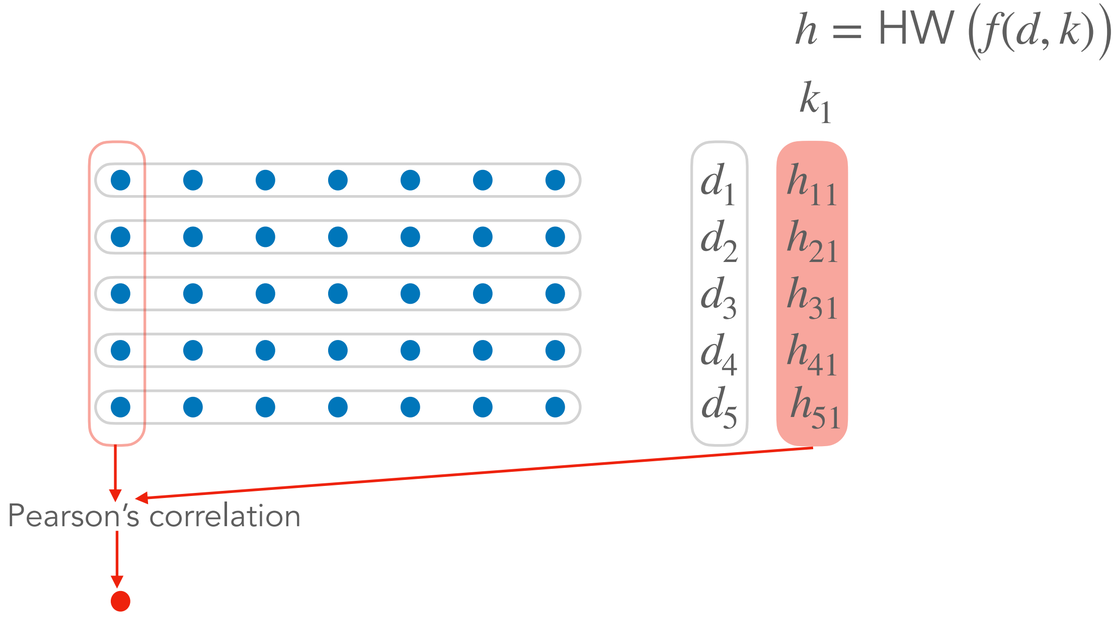

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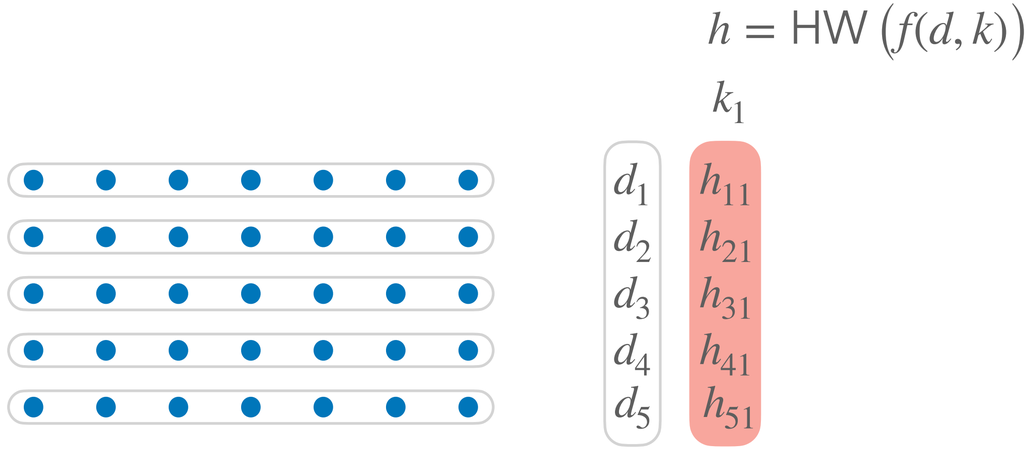

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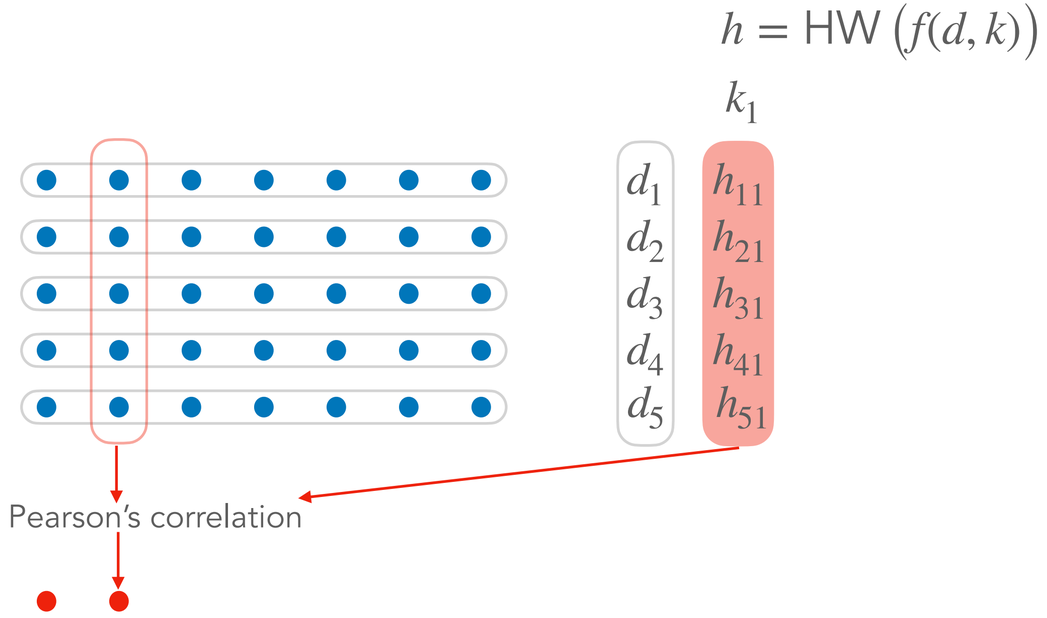

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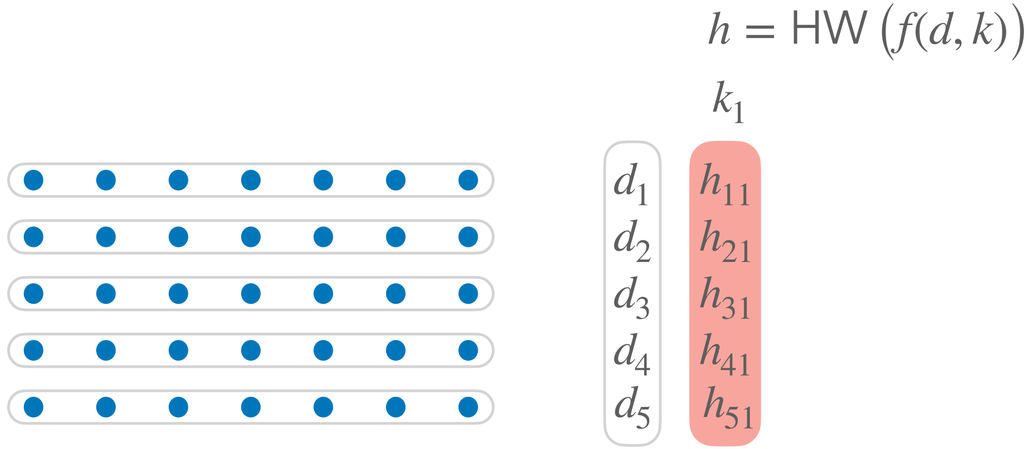

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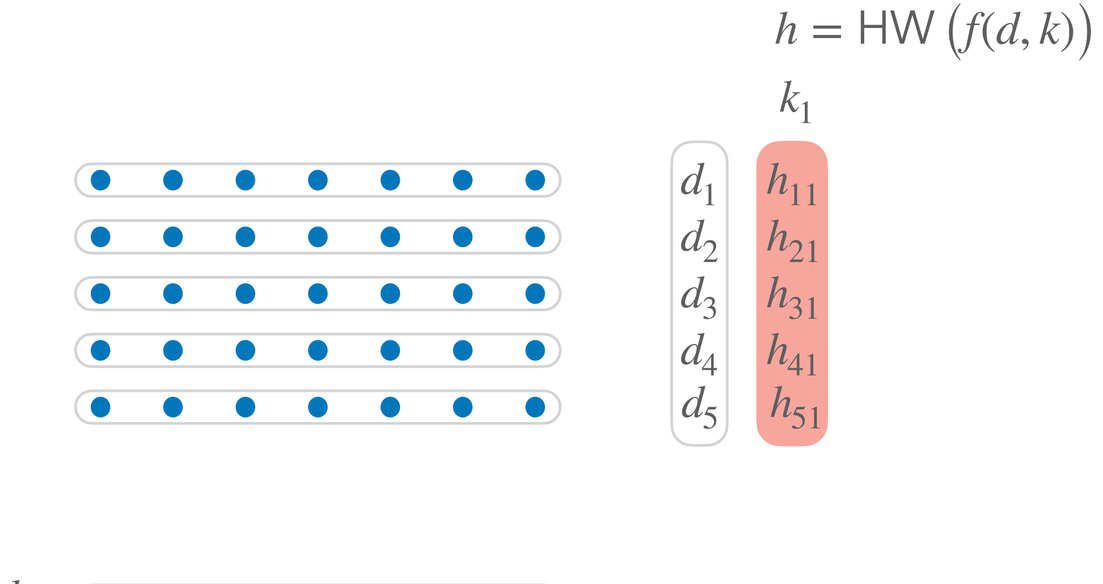

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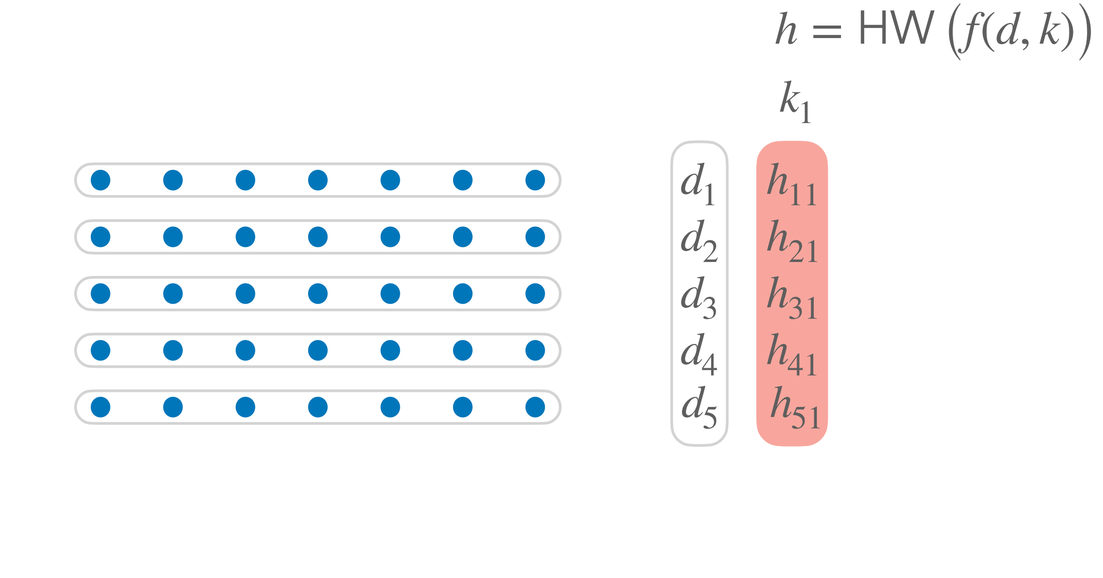

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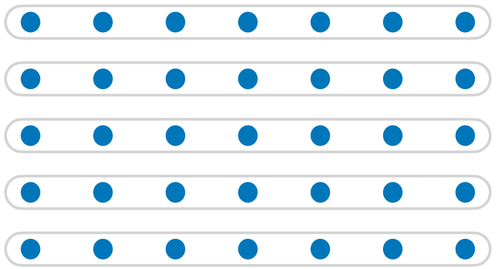

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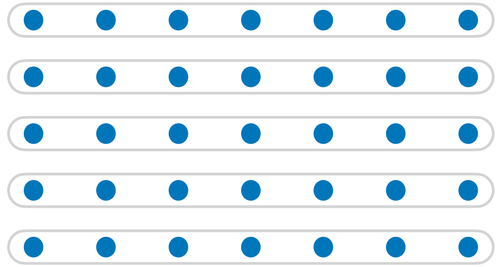

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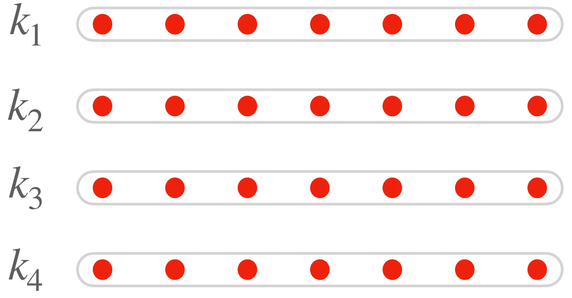

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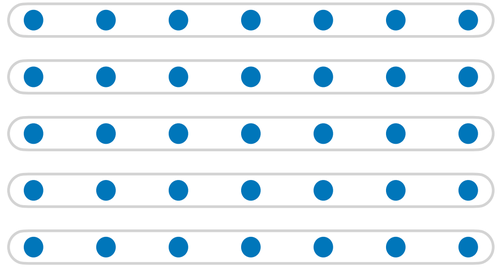

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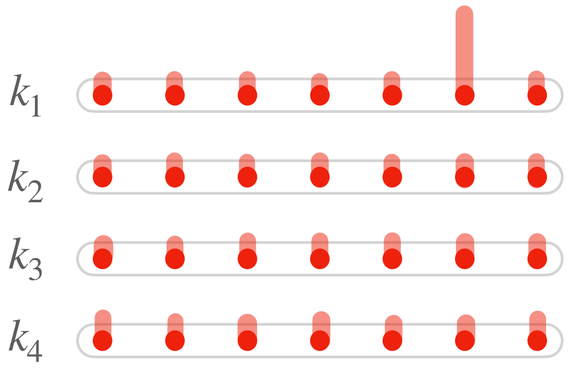

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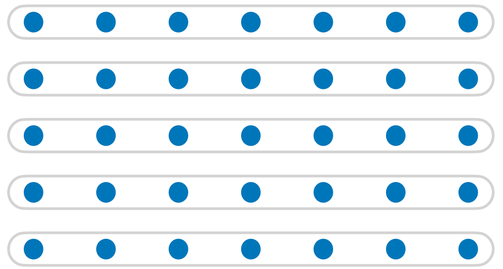

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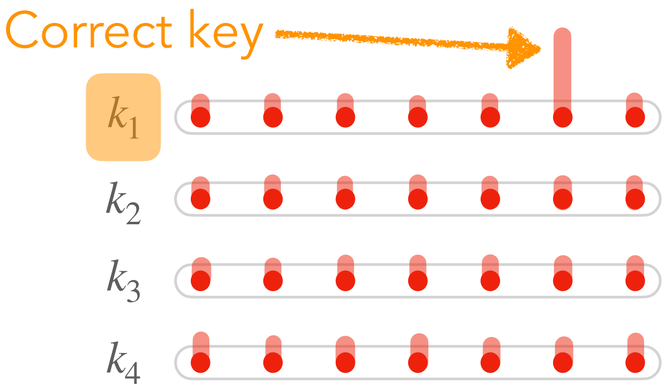
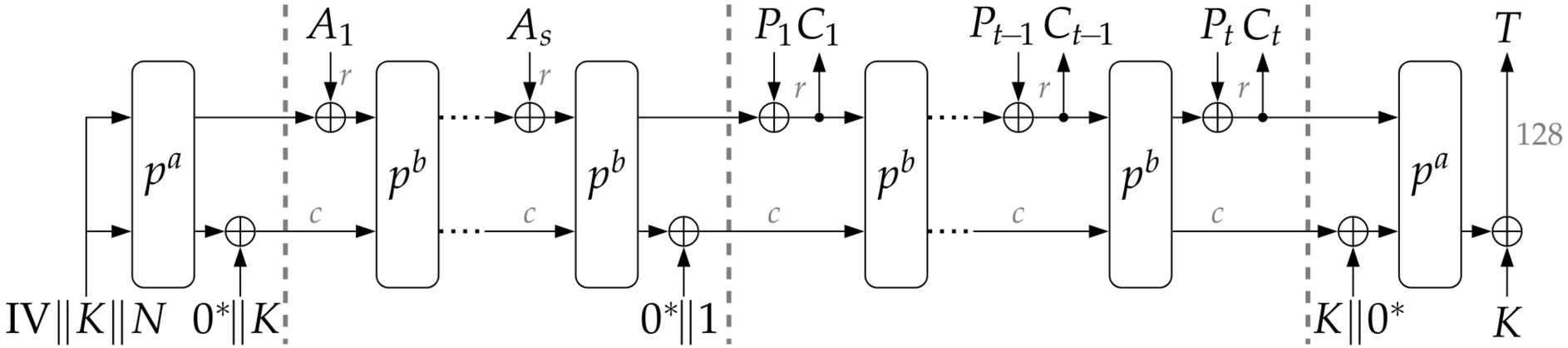
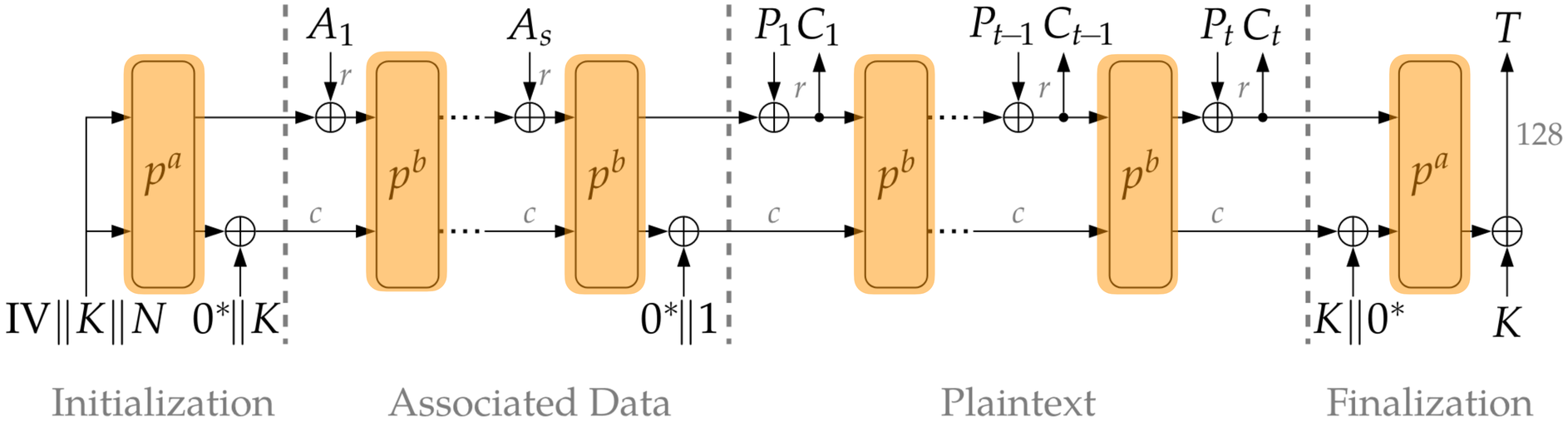


Ascon

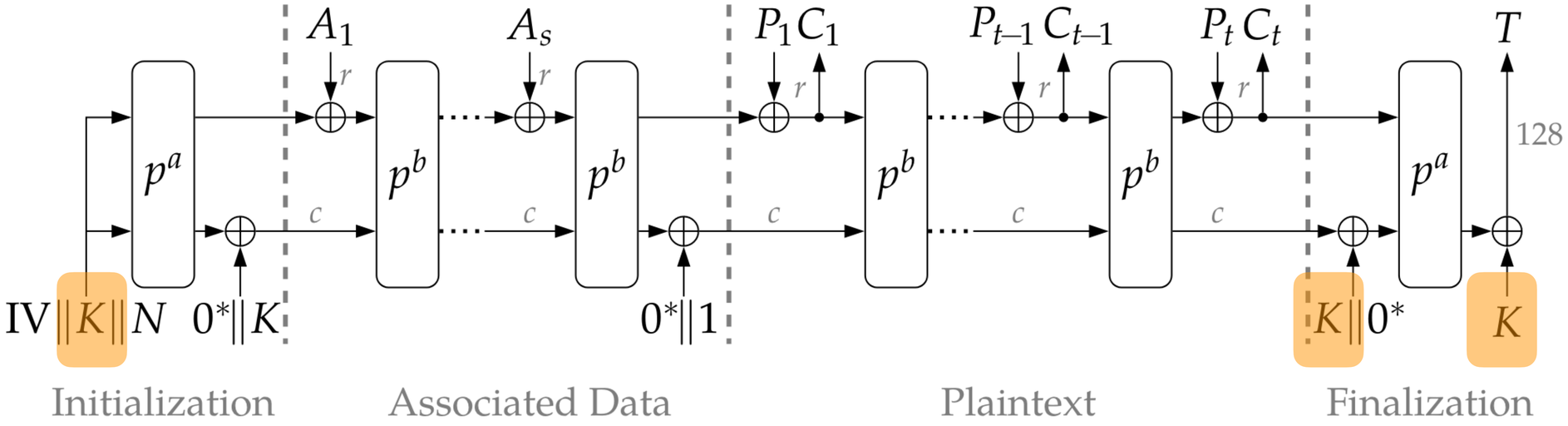

A_1
A_s
$P_1 C_1$
$P_{t-1} C_{t-1}$
$P_t C_t$
T
r
c
p^a
p^b
128
$\mathrm{IV} \| K \| N$
$0^* \| K$
$0^* \| 1$
$K \| 0^*$
K
Initialization
Associated Data
Plaintext
Finalization

Permutation blocks



- p^a: 12 rounds
- p^b: 6 rounds

Key (128 bits)


A_1
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K ‖ 0^*
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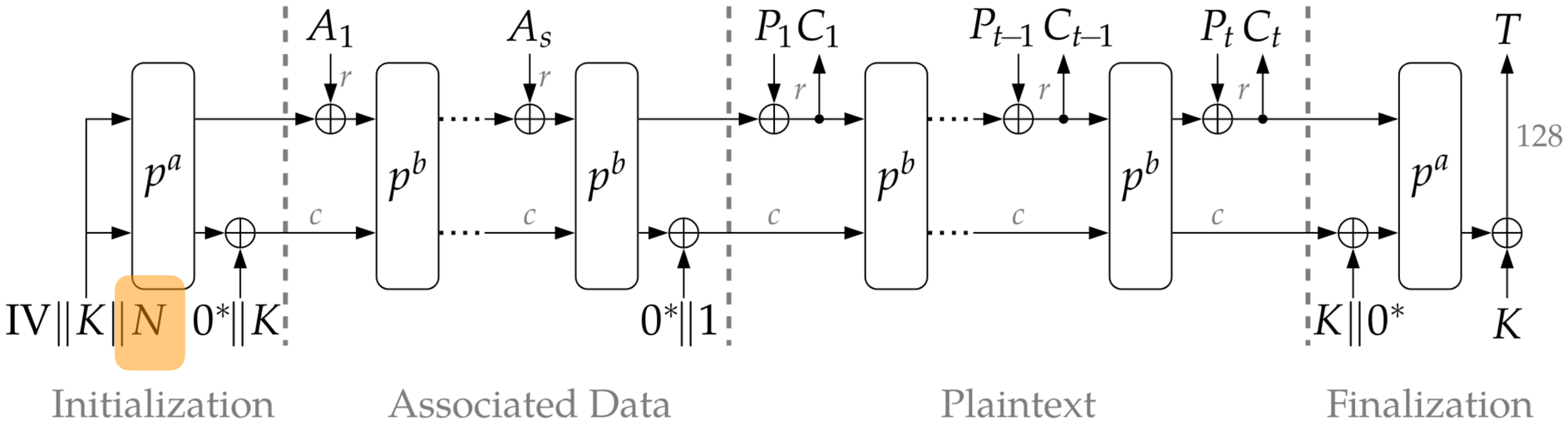
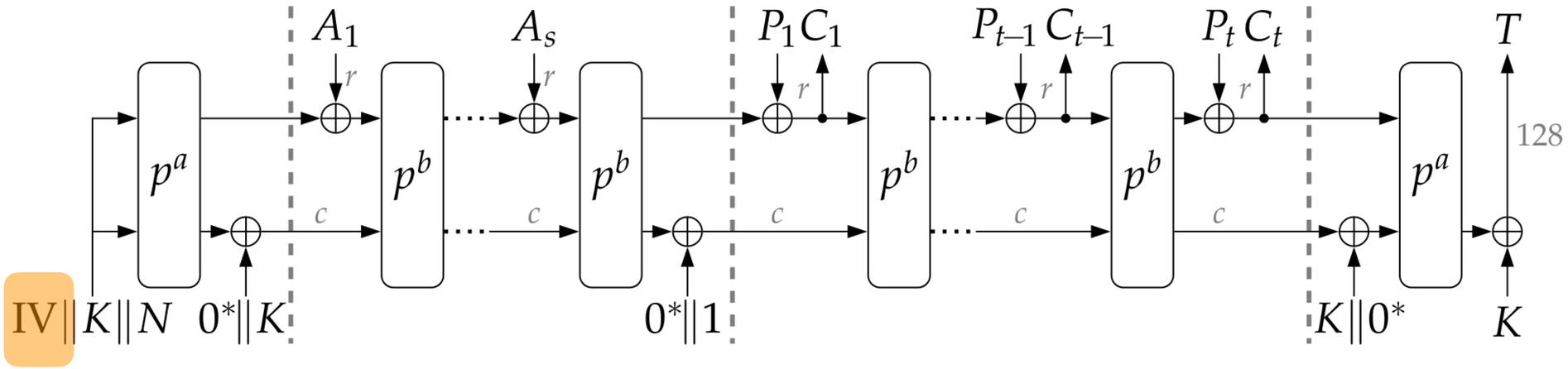
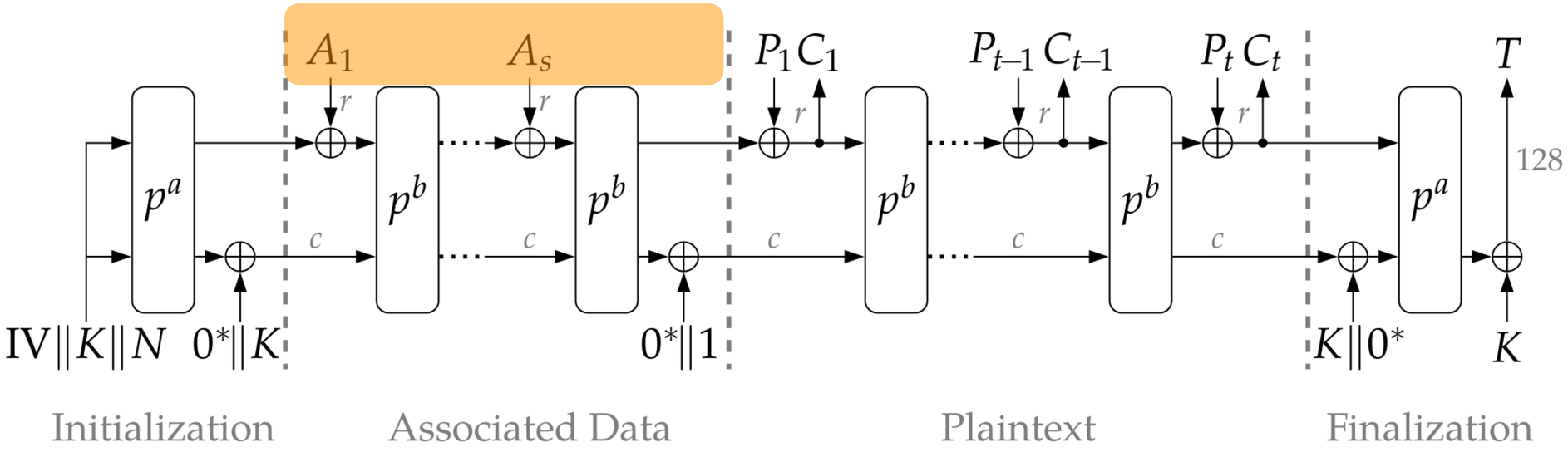
Nonce (128 bits)



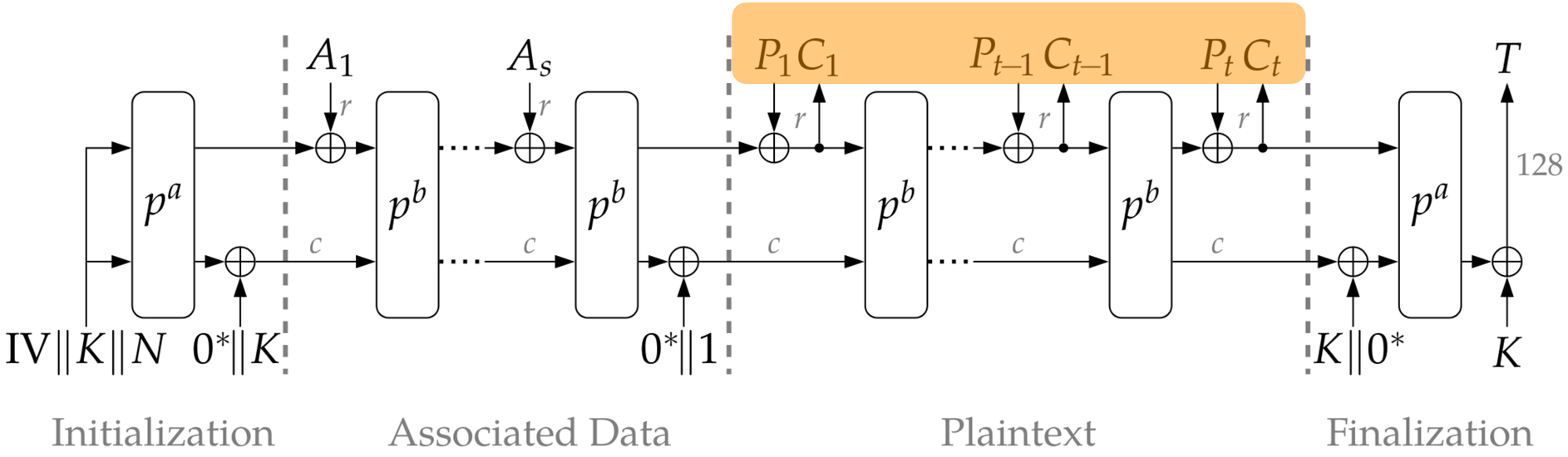
Initialization vector



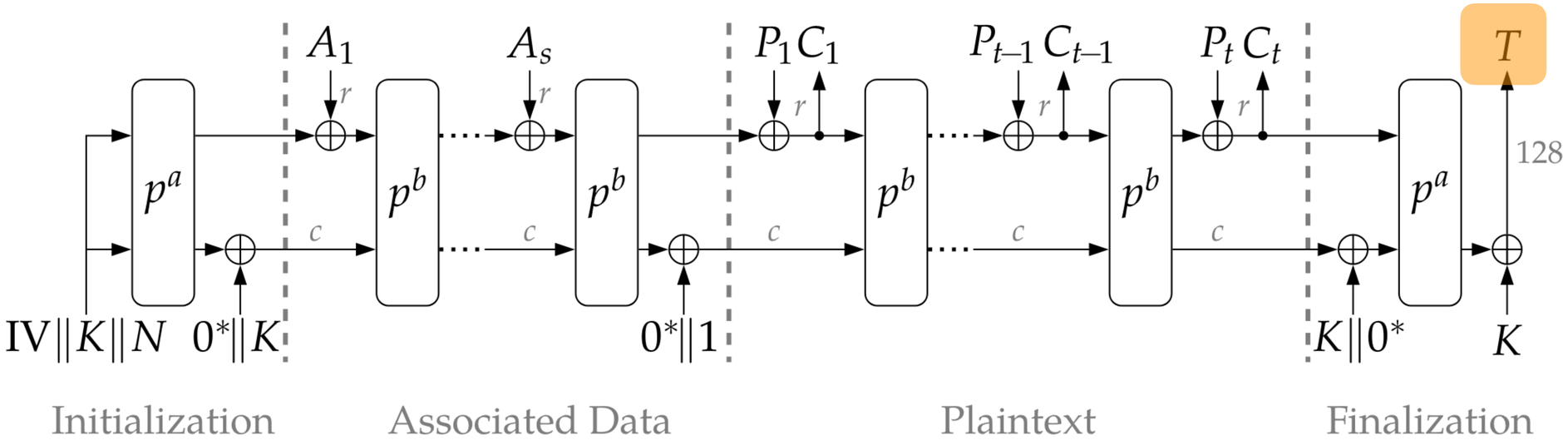
Associated data

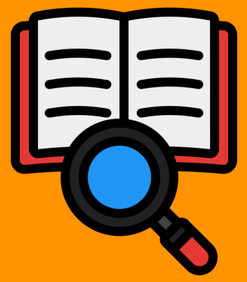


Plaintext / Ciphertext



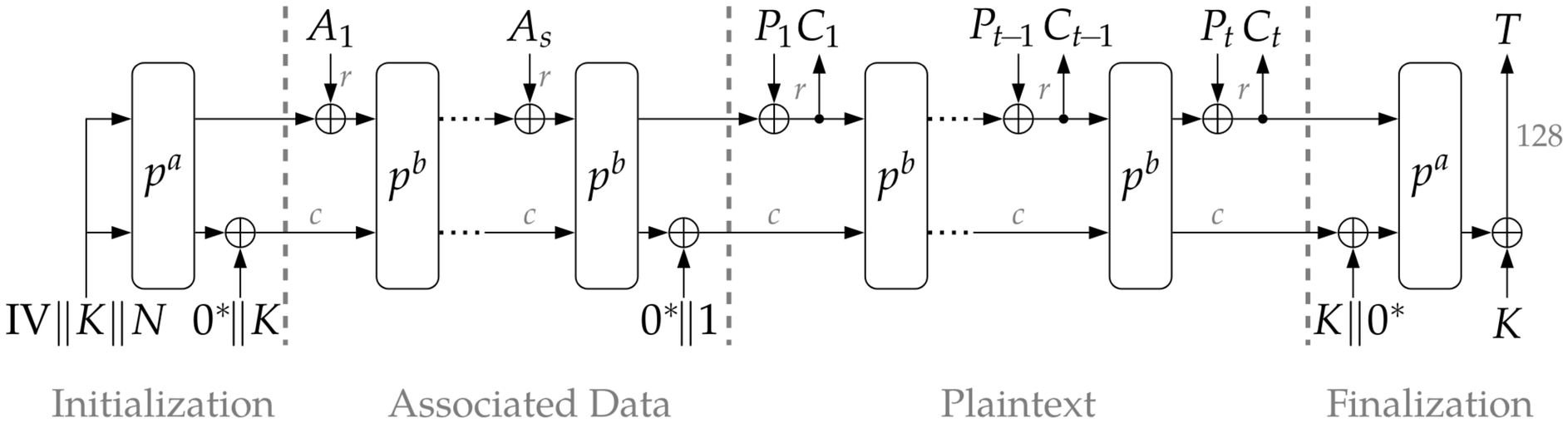
Verification Tag





Existing CPA attacks on Ascon

Selection function $v = f(d, k)$



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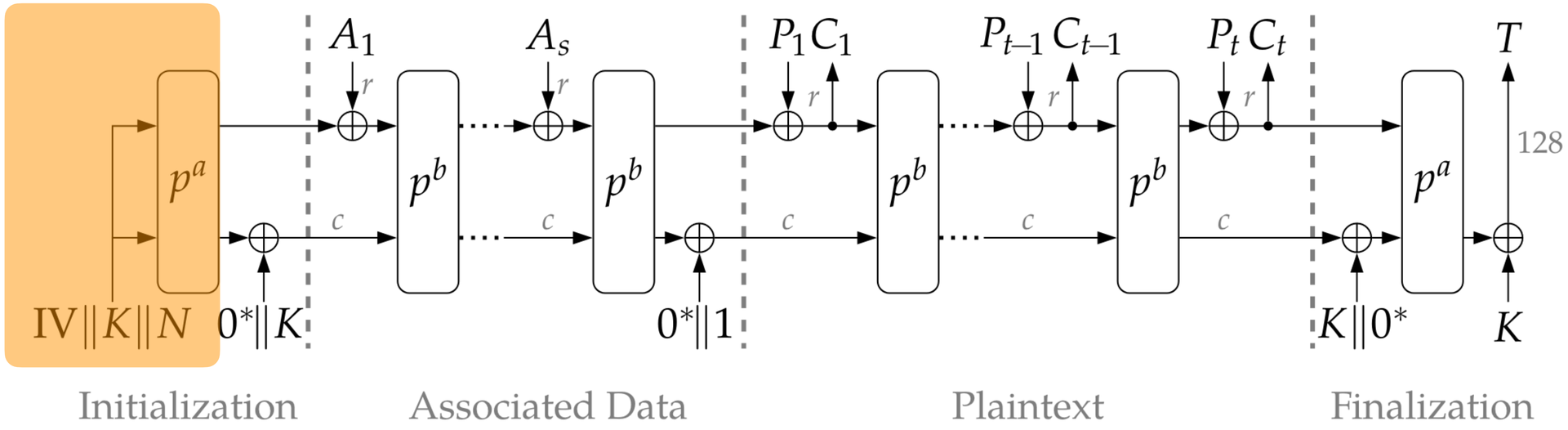

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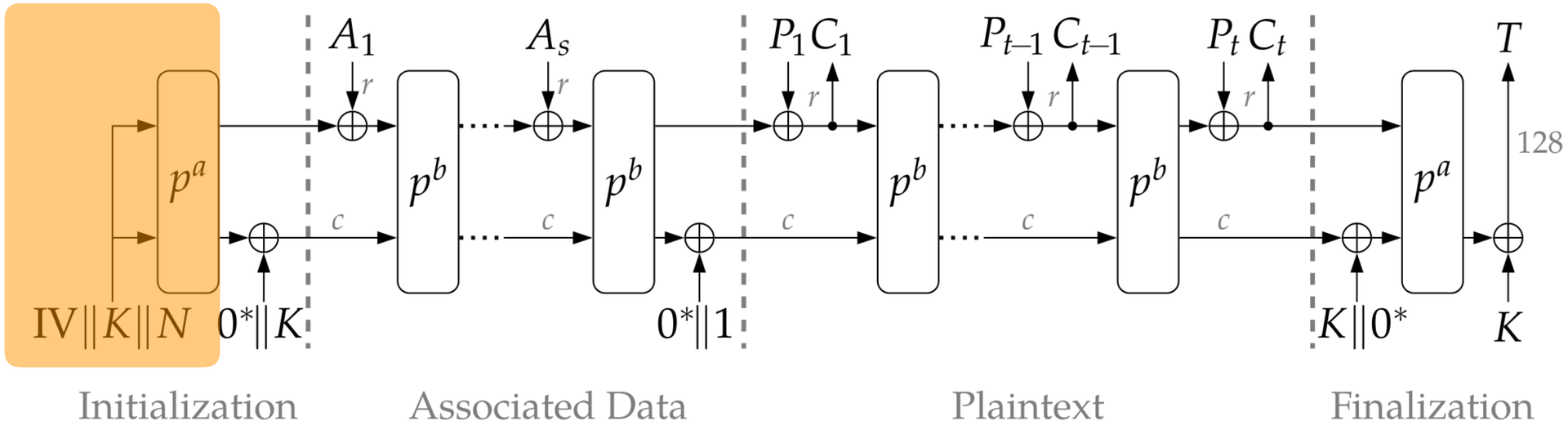


Choose $v = f(\text{nonce}, \text{key})$

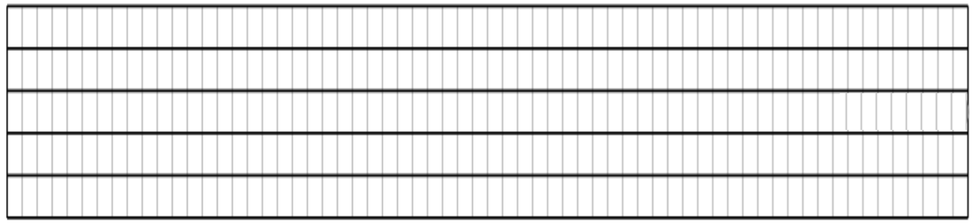
intermediate variable v in the *first round*

round computation

Round computation

Round computation

On 320-bit state = 5 x 64-bit words



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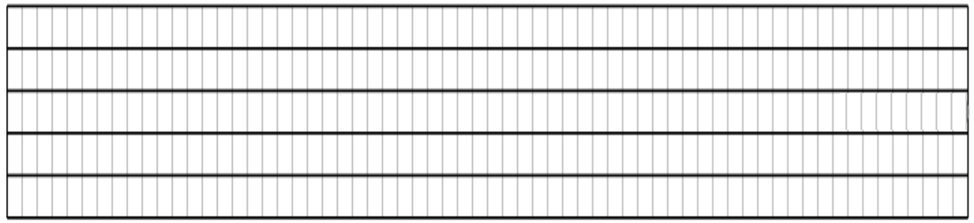

Input of the first round:

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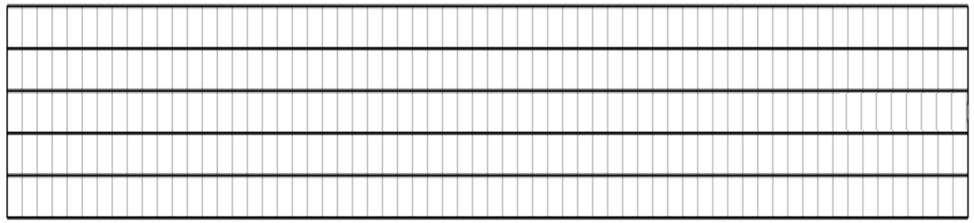

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- 128-bit nonce : $N = (n_0, n_1)$
- 64-bit init. vector : IV

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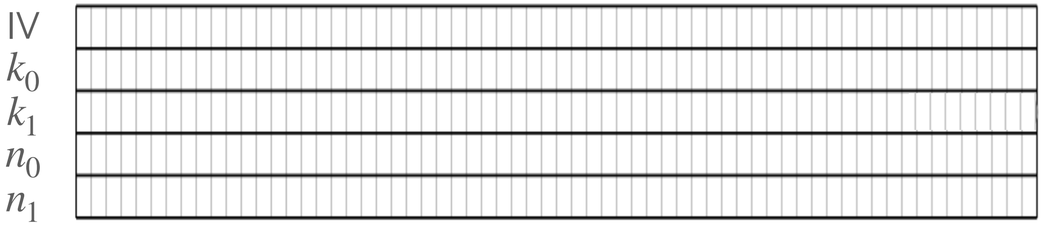

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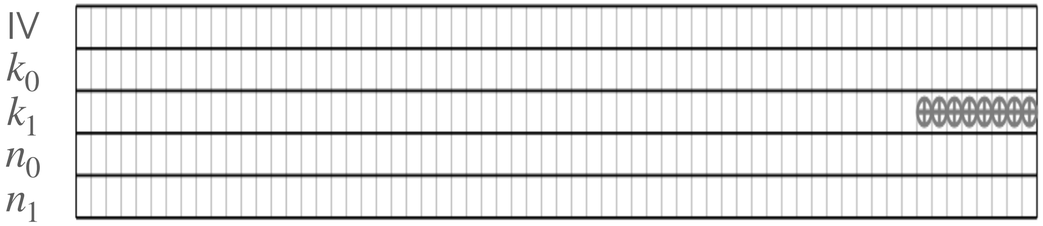

(1) Round constant addition

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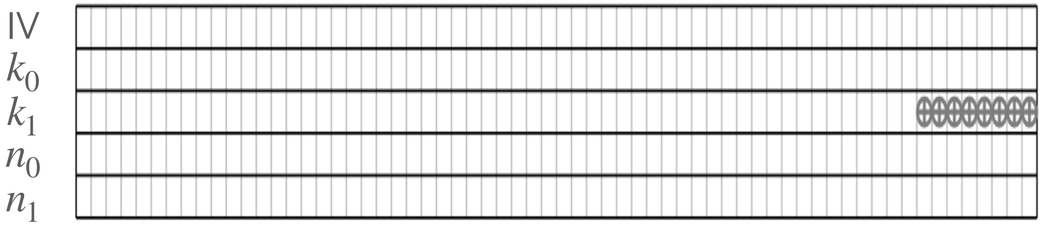

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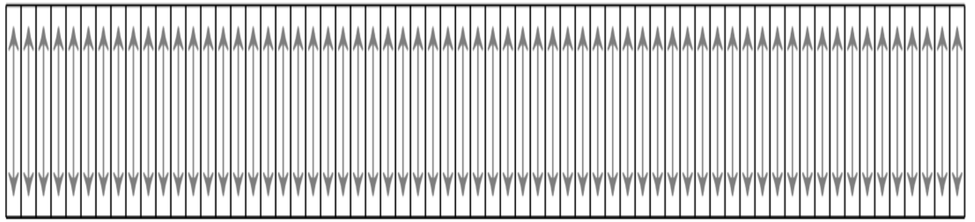


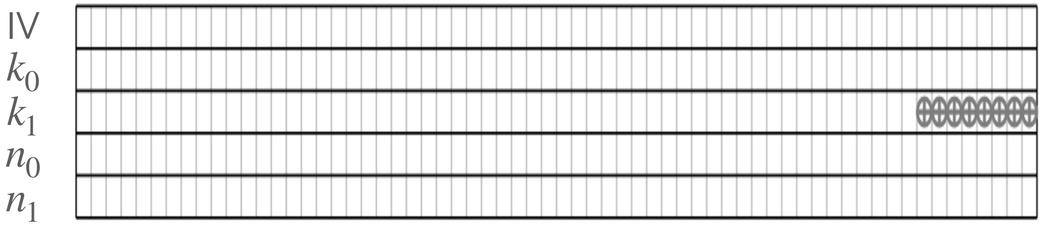
(2) Substitution *(vertical)*

Round computation

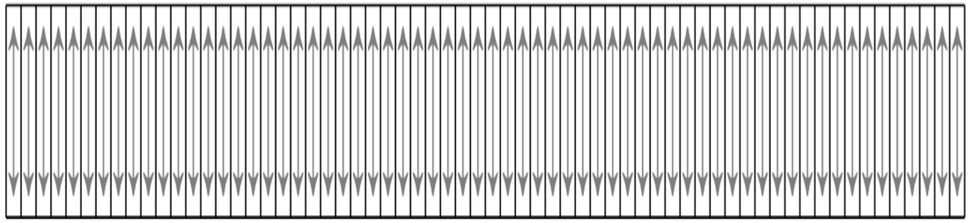
On 320-bit state = 5 x 64-bit words

Input of the first round:

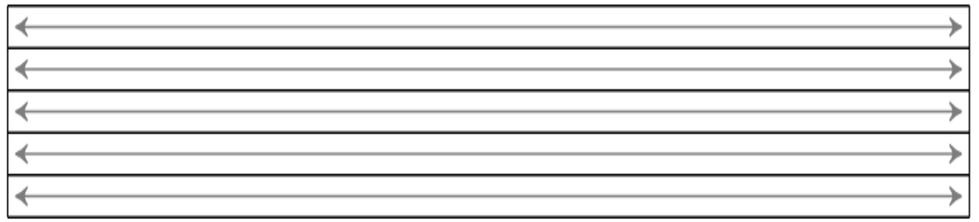
- 128-bit key : $K = (k_0, k_1)$
- 128-bit nonce : $N = (n_0, n_1)$
- 64-bit init. vector : IV



(1) Round constant addition

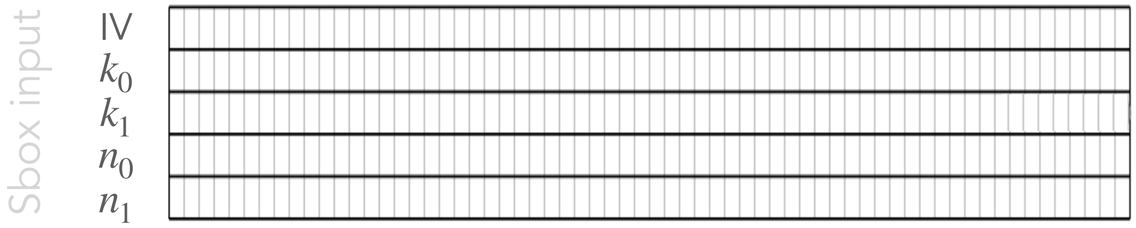


(2) Substitution *(vertical)*

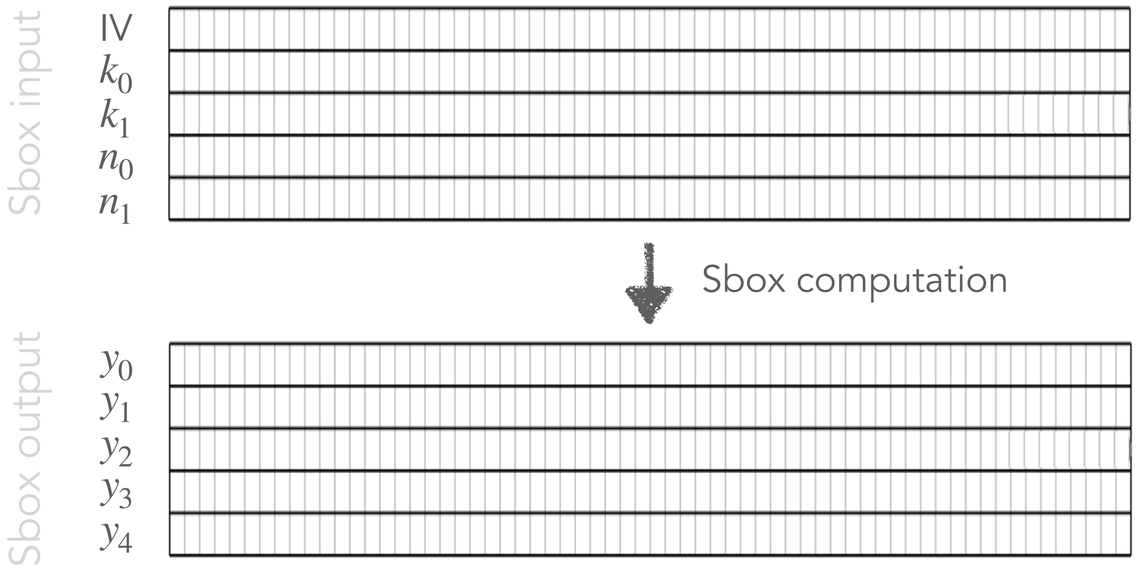


(3) Linear diffusion *(horizontal)*

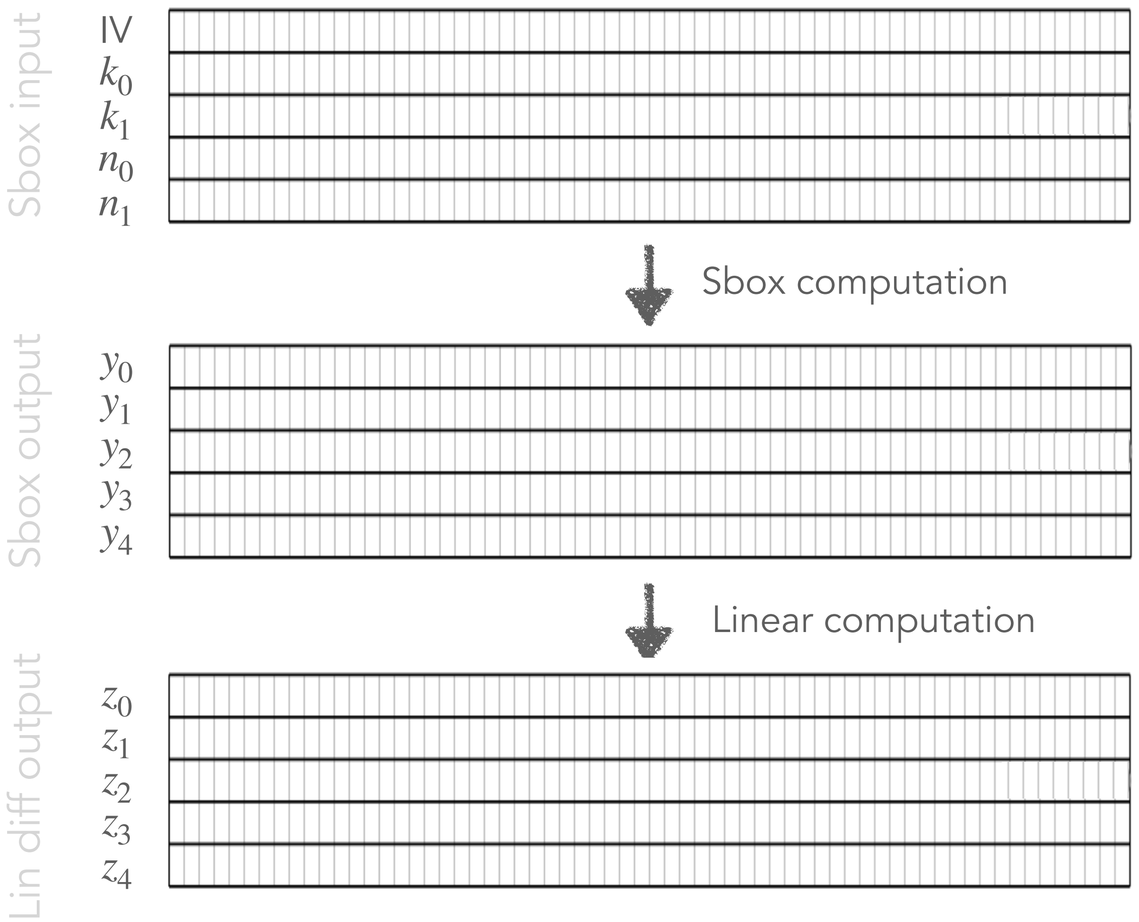
State changes



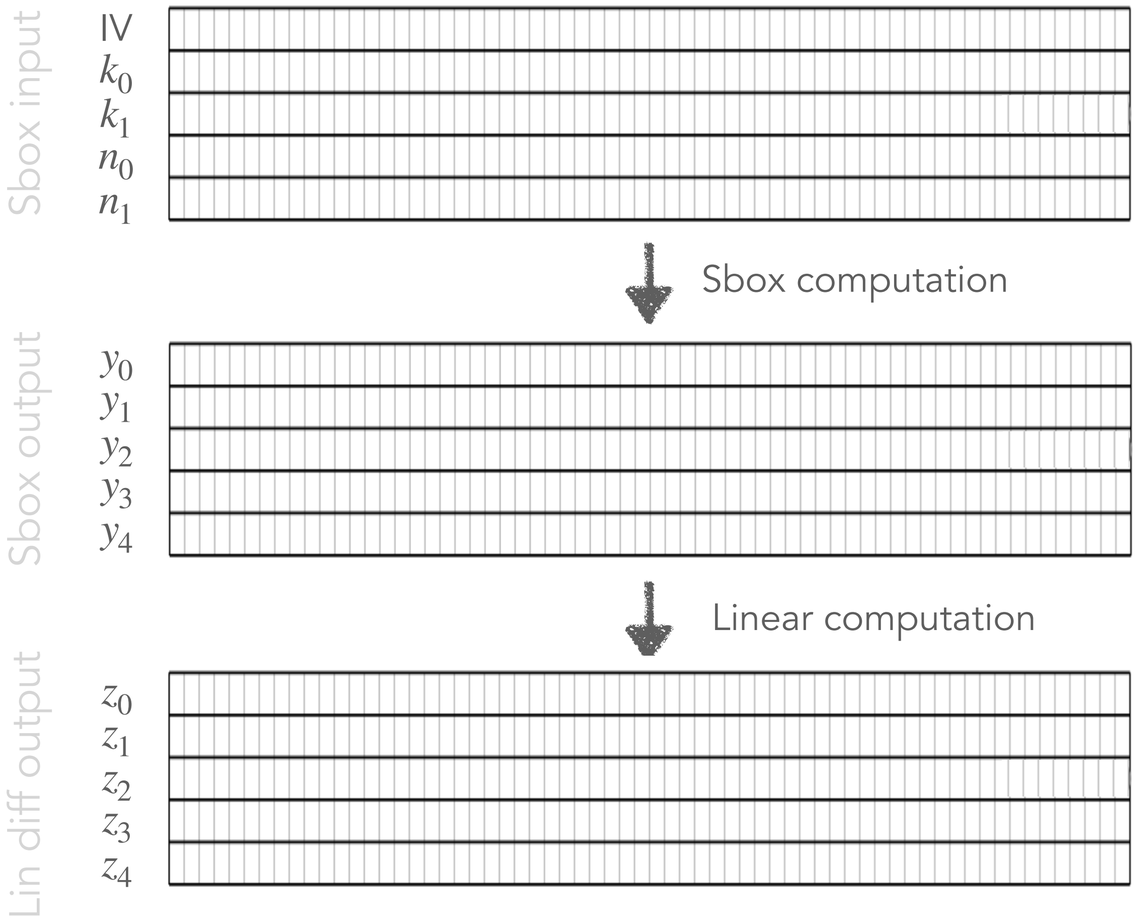
State changes



State changes



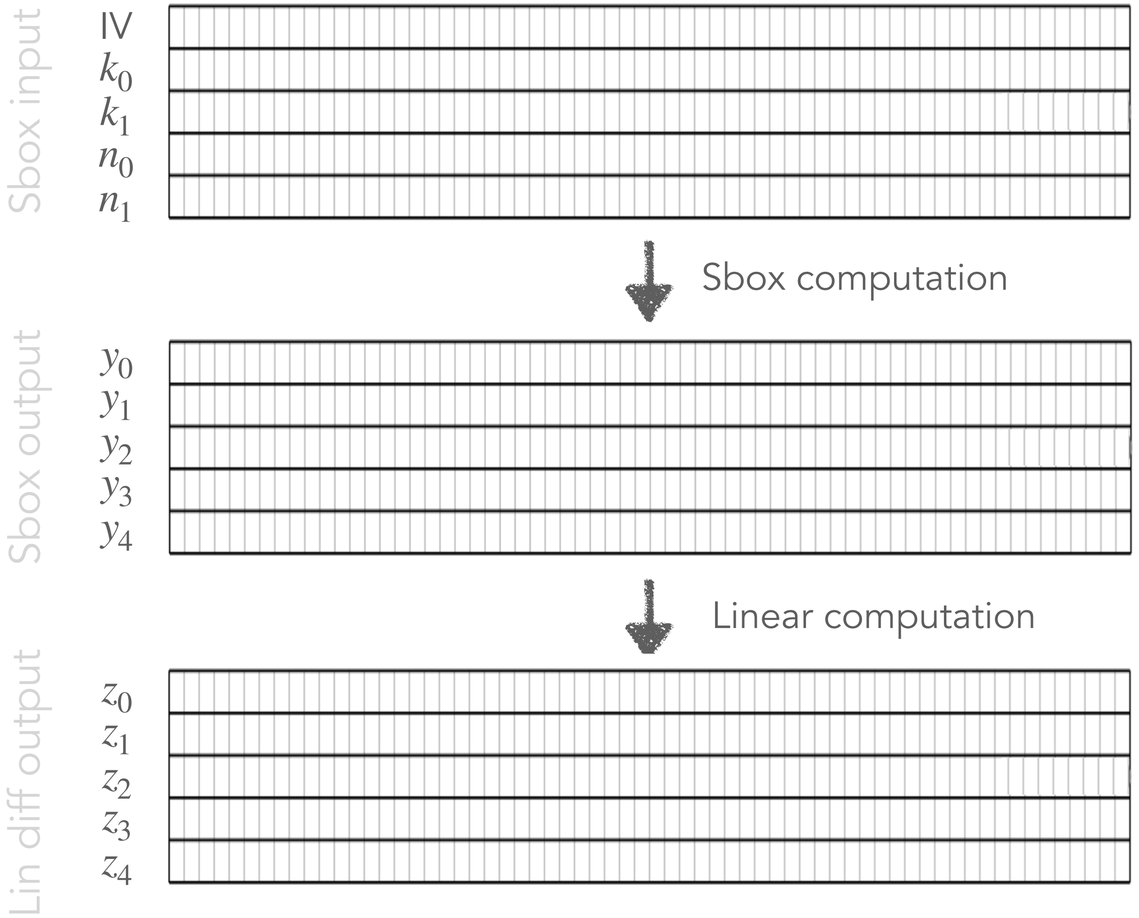
Previous CPA (1)



Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

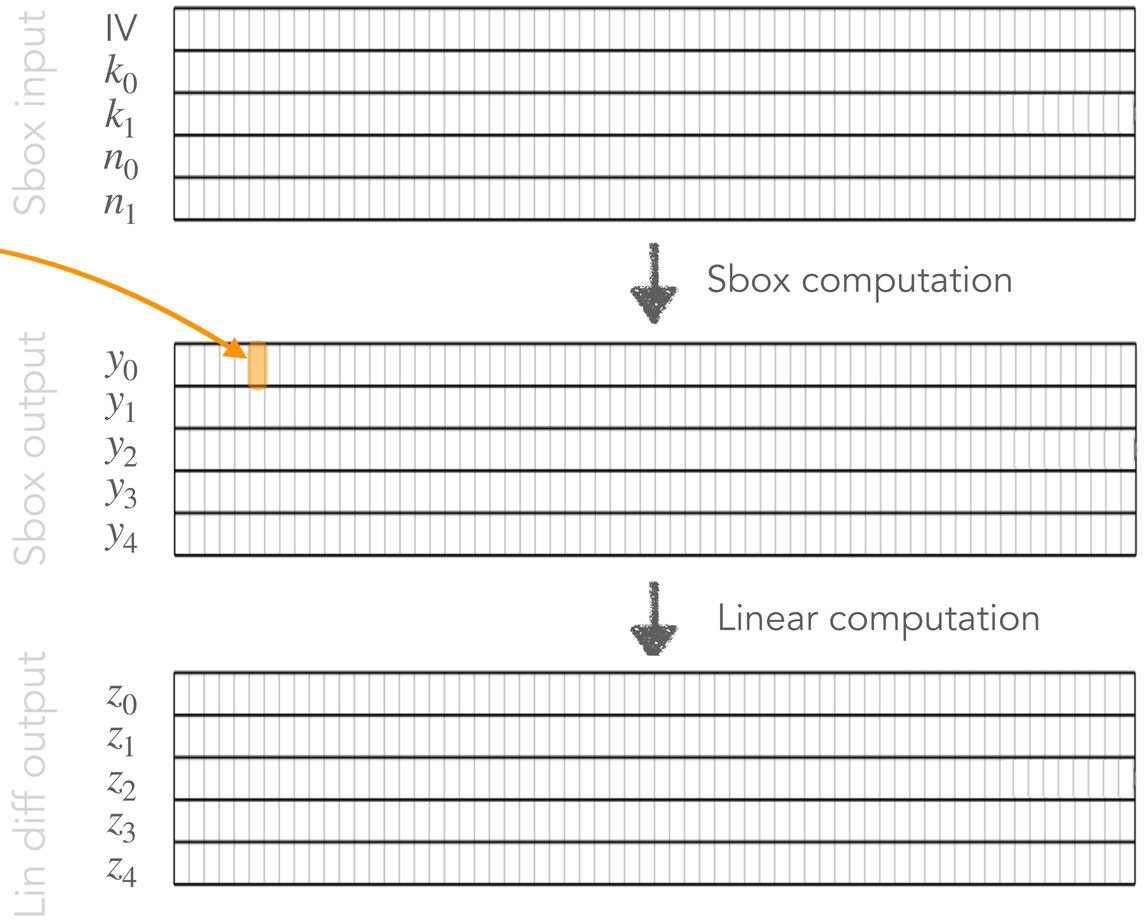


Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

- Intermediate variable: y_0^j



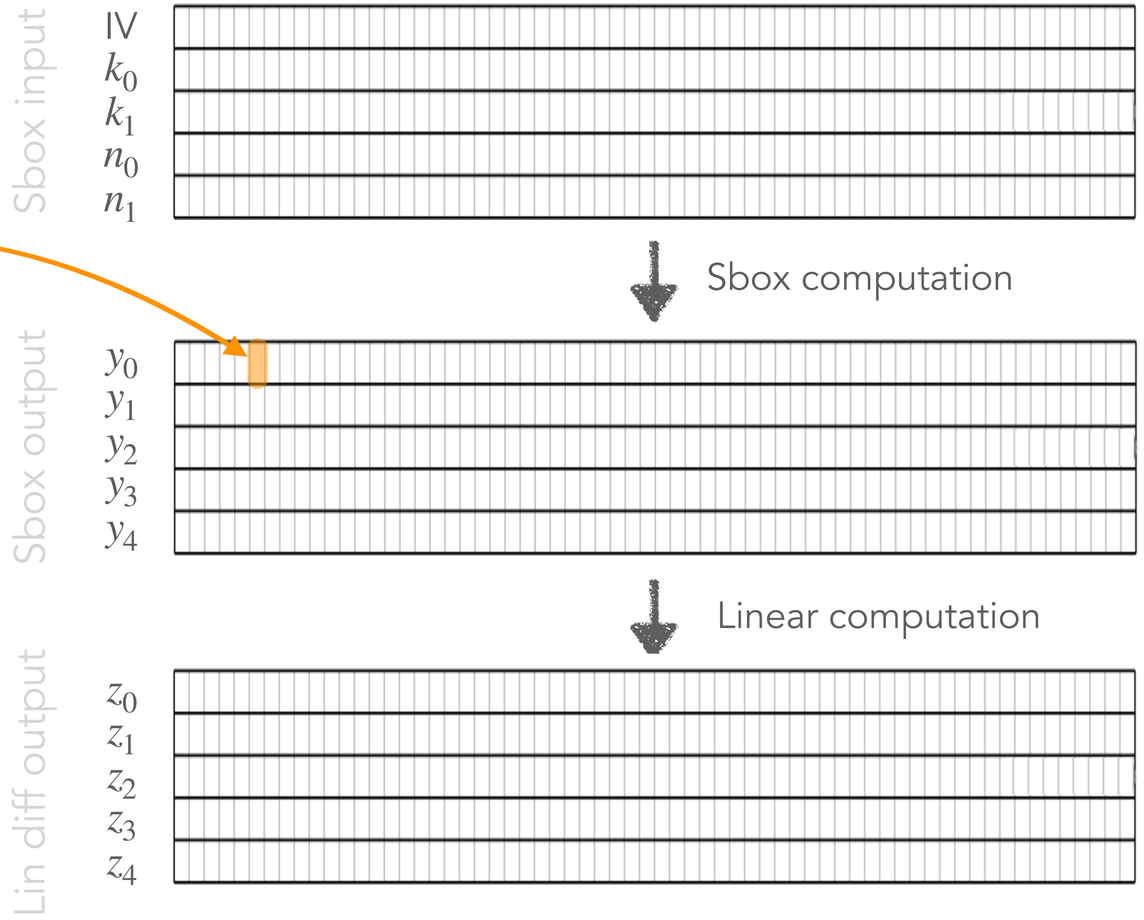
Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

- Intermediate variable: y_0^j

  → Failed (with 40K traces)



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Ramezanpour et al., 2020

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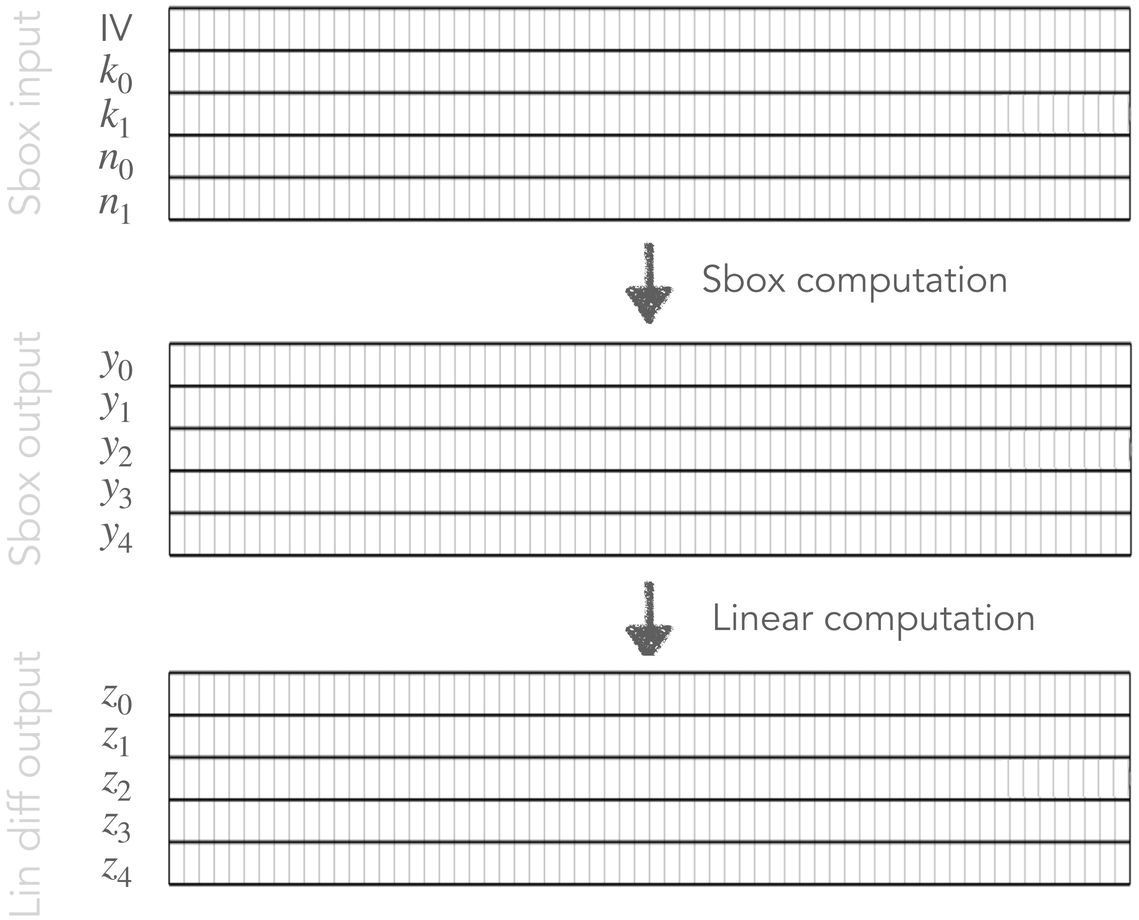


Previous CPA (1)

Ramezanpour et al., 2020

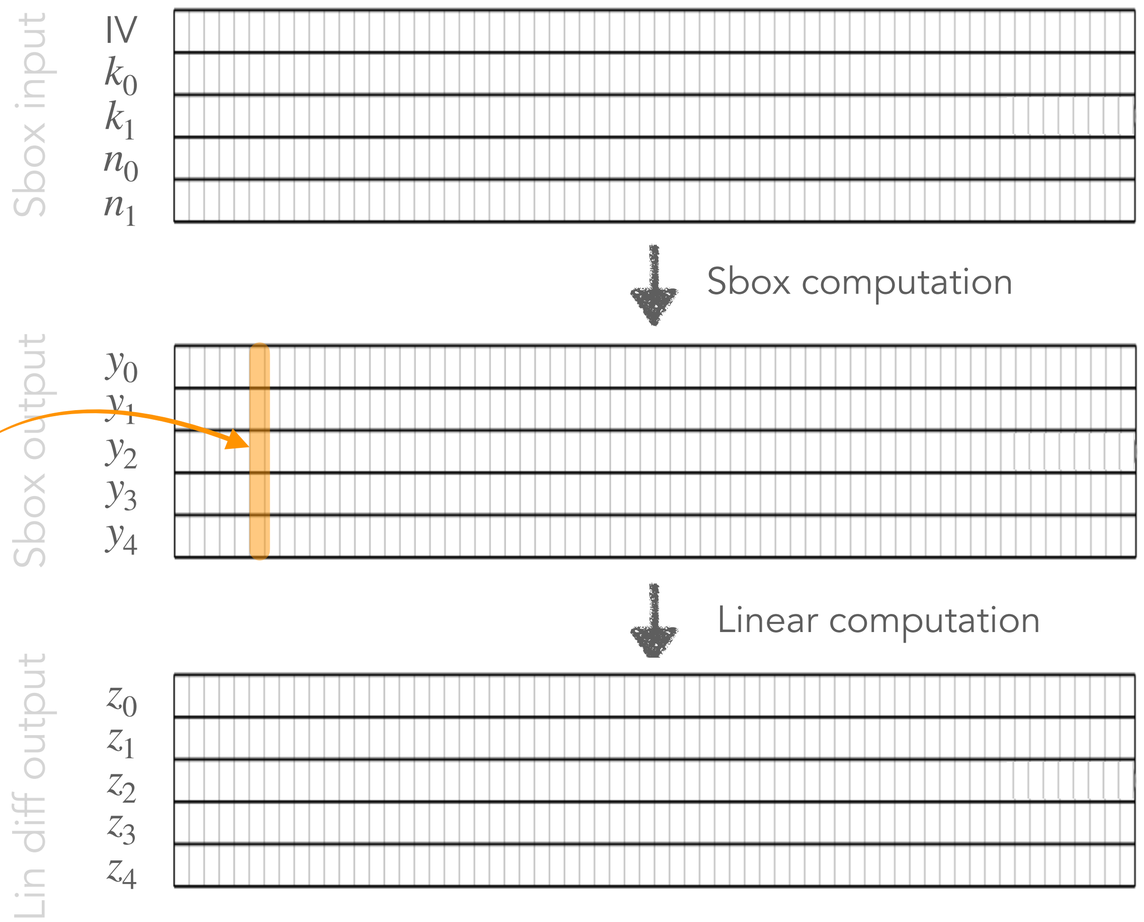
Choose Sbox output as attack point

- Intermediate variable: y_0^j

  → Failed (with 40K traces)

- Intermediate variable: $(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$

  HW of Sbox output



Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

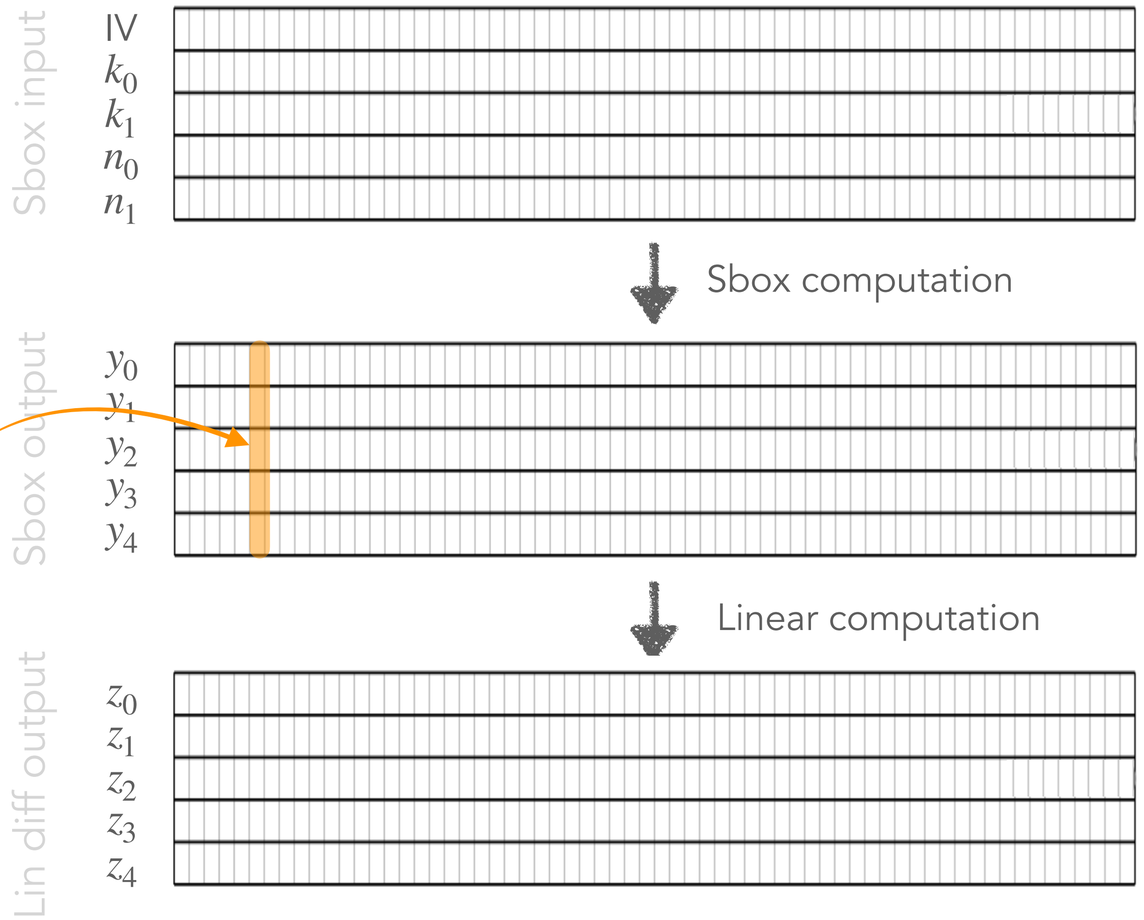
- Intermediate variable: y_0^j

  → Failed (with 40K traces)

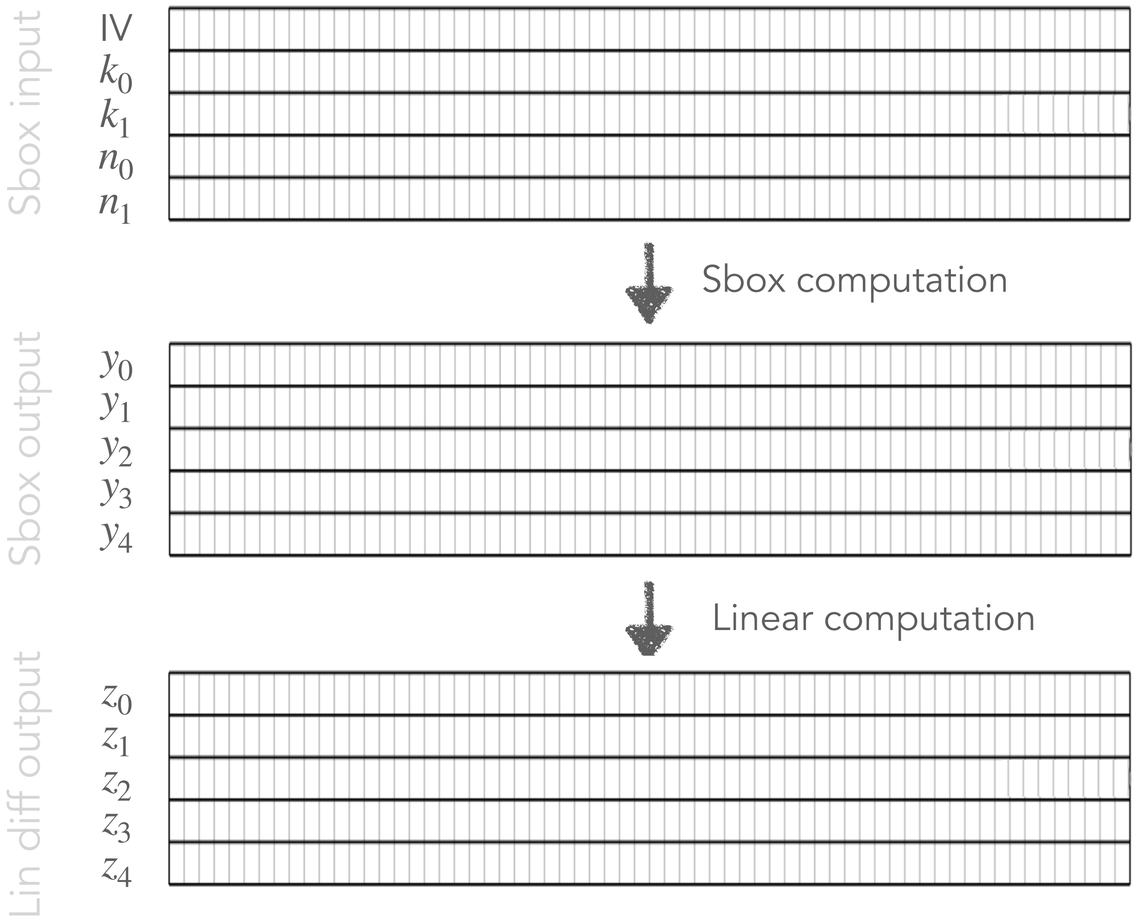
- Intermediate variable: $(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$

  HW of Sbox output

  → Failed (with 40K traces)



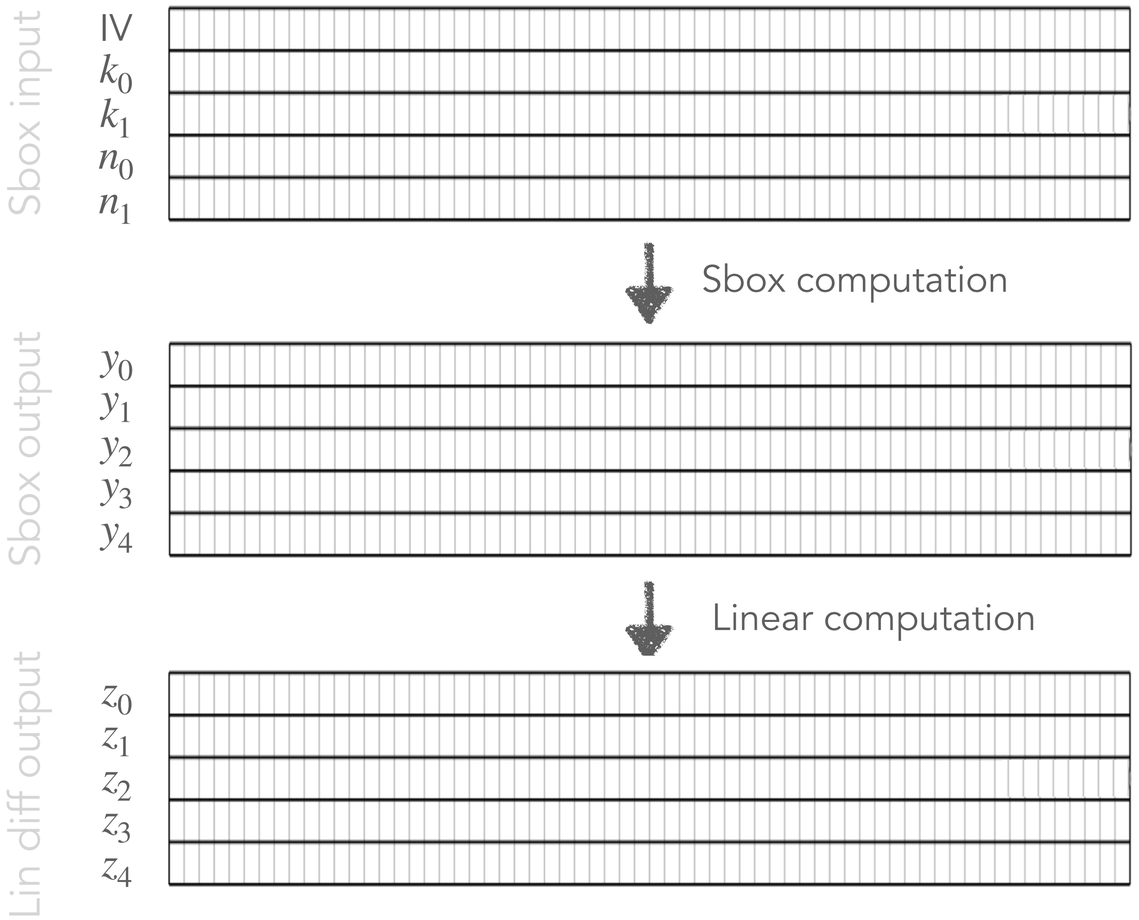
Previous CPA (2)



Previous CPA (2)

Samwel and Daemen, 2018

Choose linear diffusion output as attack point

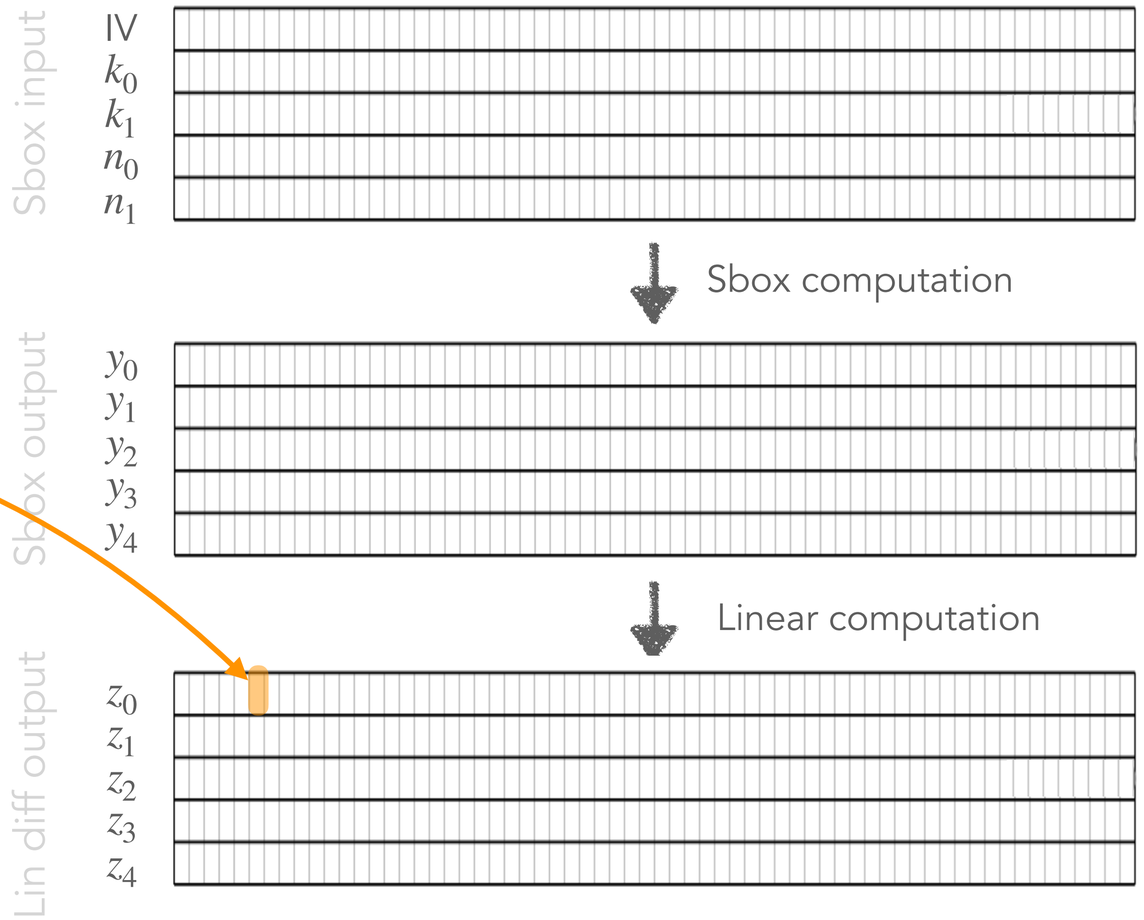


Previous CPA (2)

Samwel and Daemen, 2018

Choose linear diffusion output as attack point

Intermediate variable: z_0^j



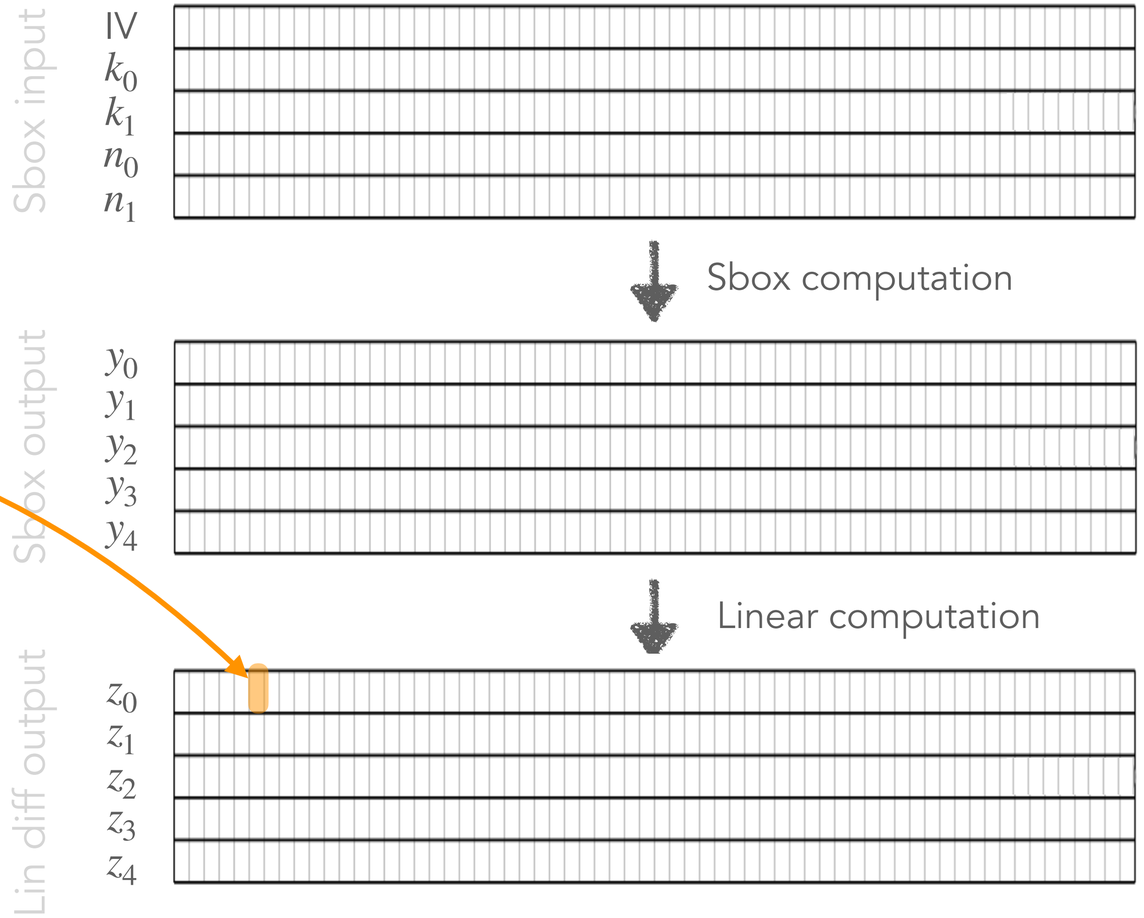
Previous CPA (2)

Samwel and Daemen, 2018

Choose linear diffusion output as attack point

Intermediate variable: z_0^j

Fine-tune computation: $z_0^j \rightarrow \tilde{z}_0^j$



Previous CPA (2)

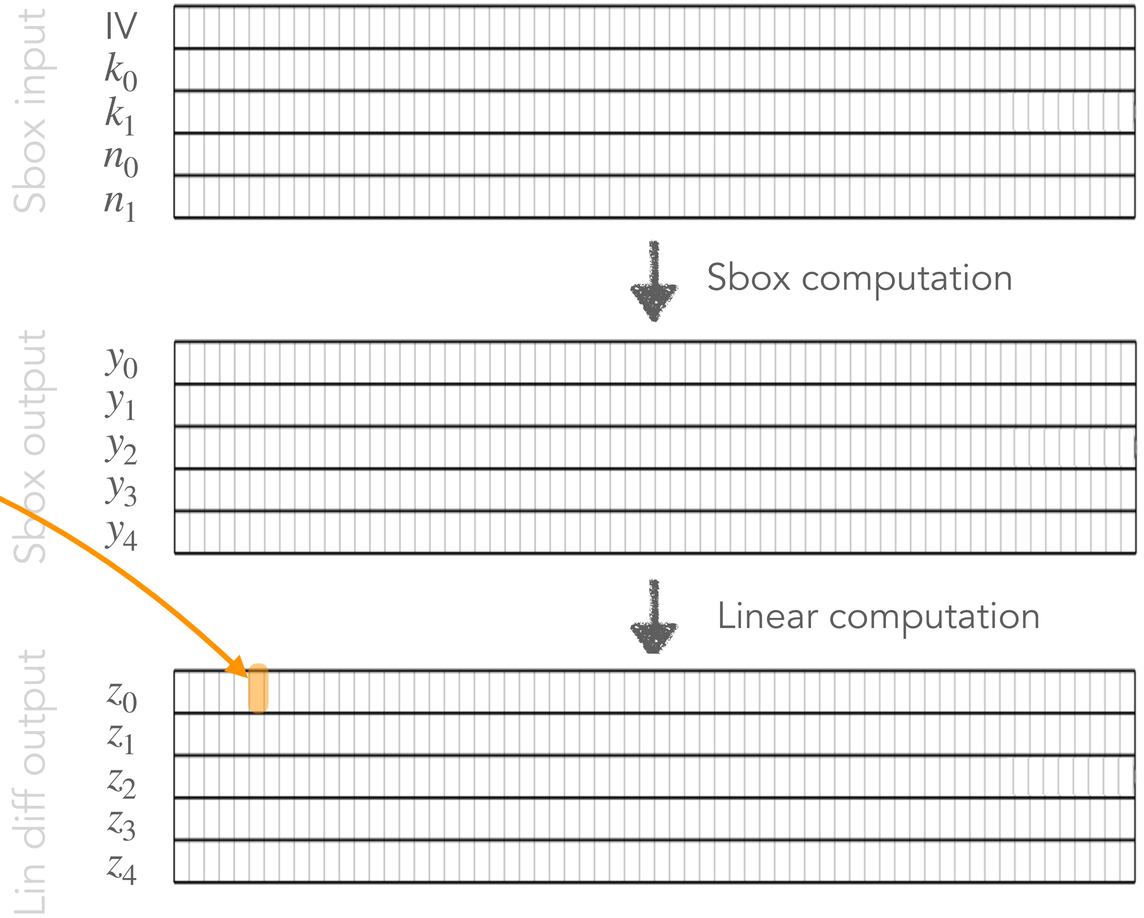
Samwel and Daemen, 2018

Choose linear diffusion output as attack point

Intermediate variable: z_0^j

Fine-tune computation: $z_0^j \rightarrow \tilde{z}_0^j$

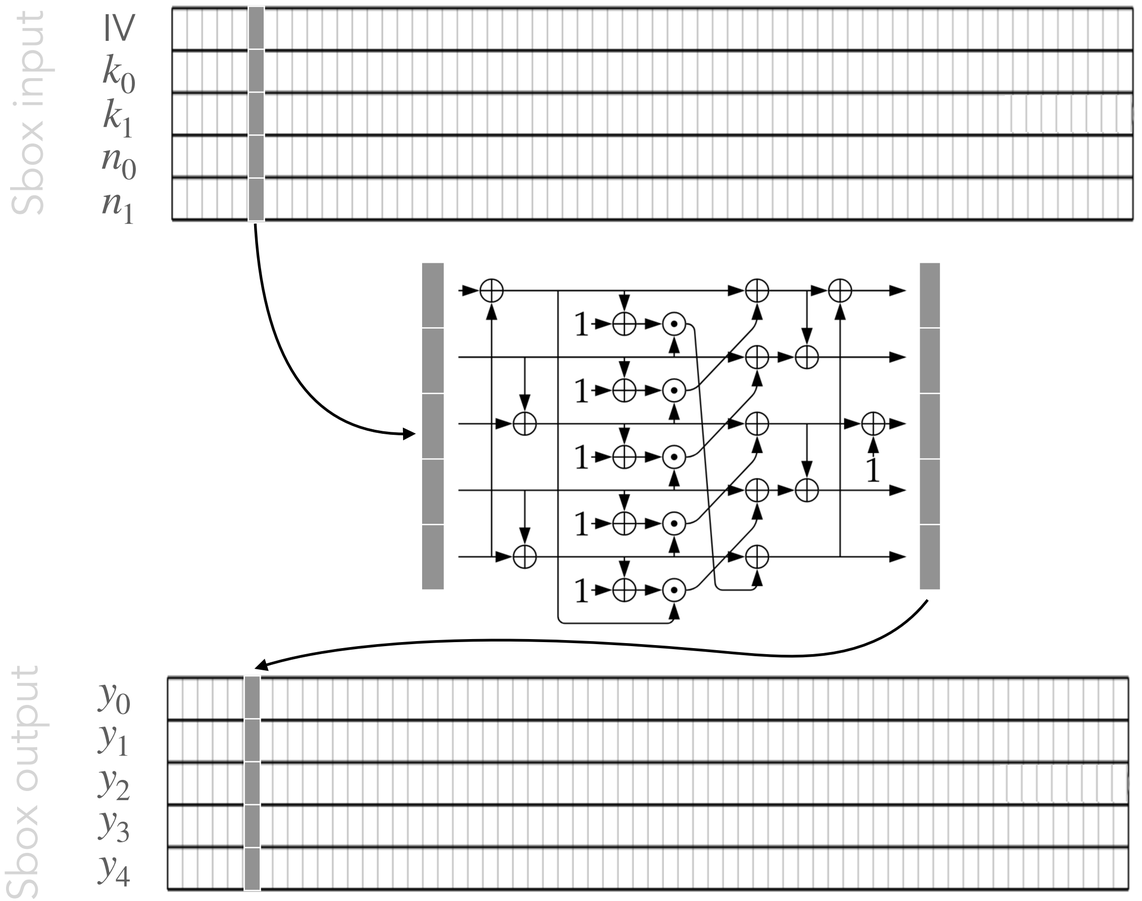
→ Succeeded



What are the reasons of this difference ?

(1) Sbox output as attack point

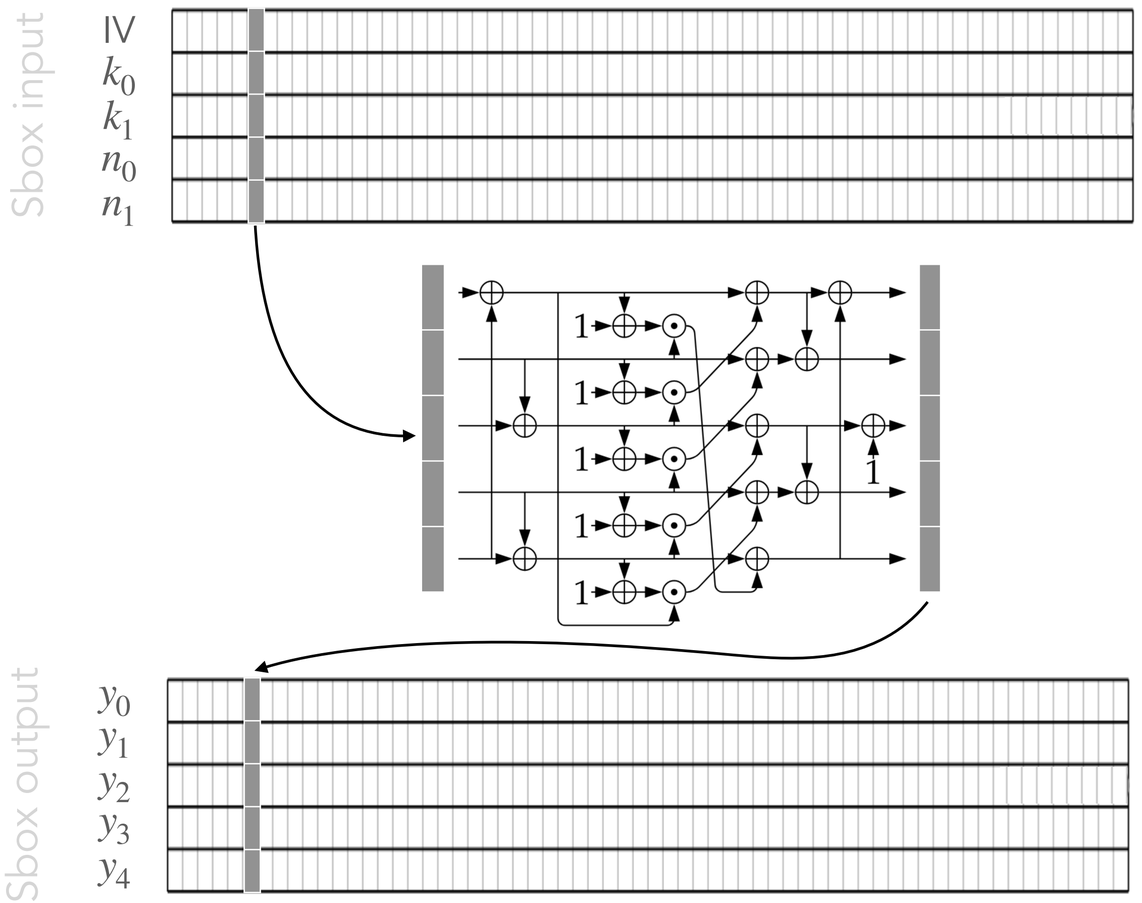
(1) Sbox output as attack point



(1) Sbox output as attack point

In an Sbox computation y_0^j:

- 2 bits of key : (k_0^j, k_1^j)
- 2 bits of nonce : (n_0^j, n_1^j)



(1) Sbox output as attack point

In an Sbox computation y_0^j:

- 2 bits of key : (k_0^j, k_1^j)
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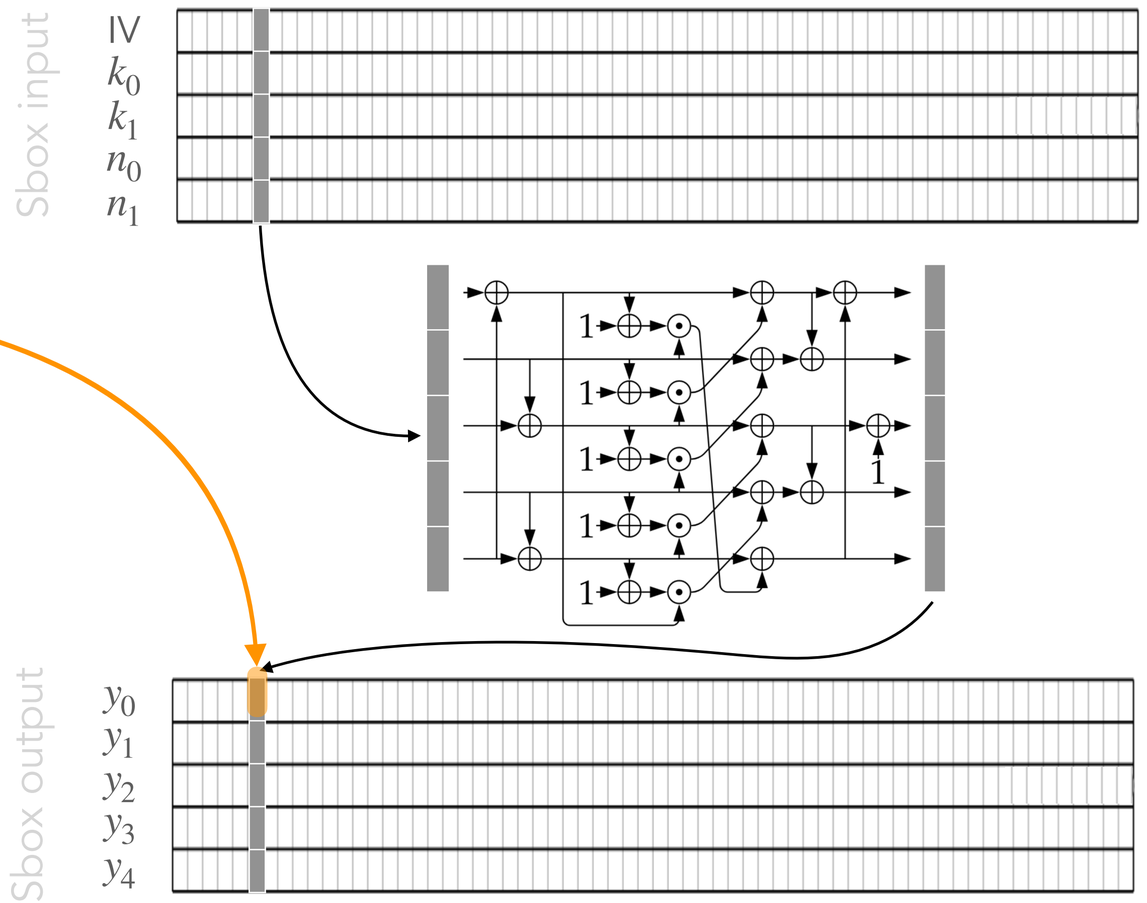
$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$



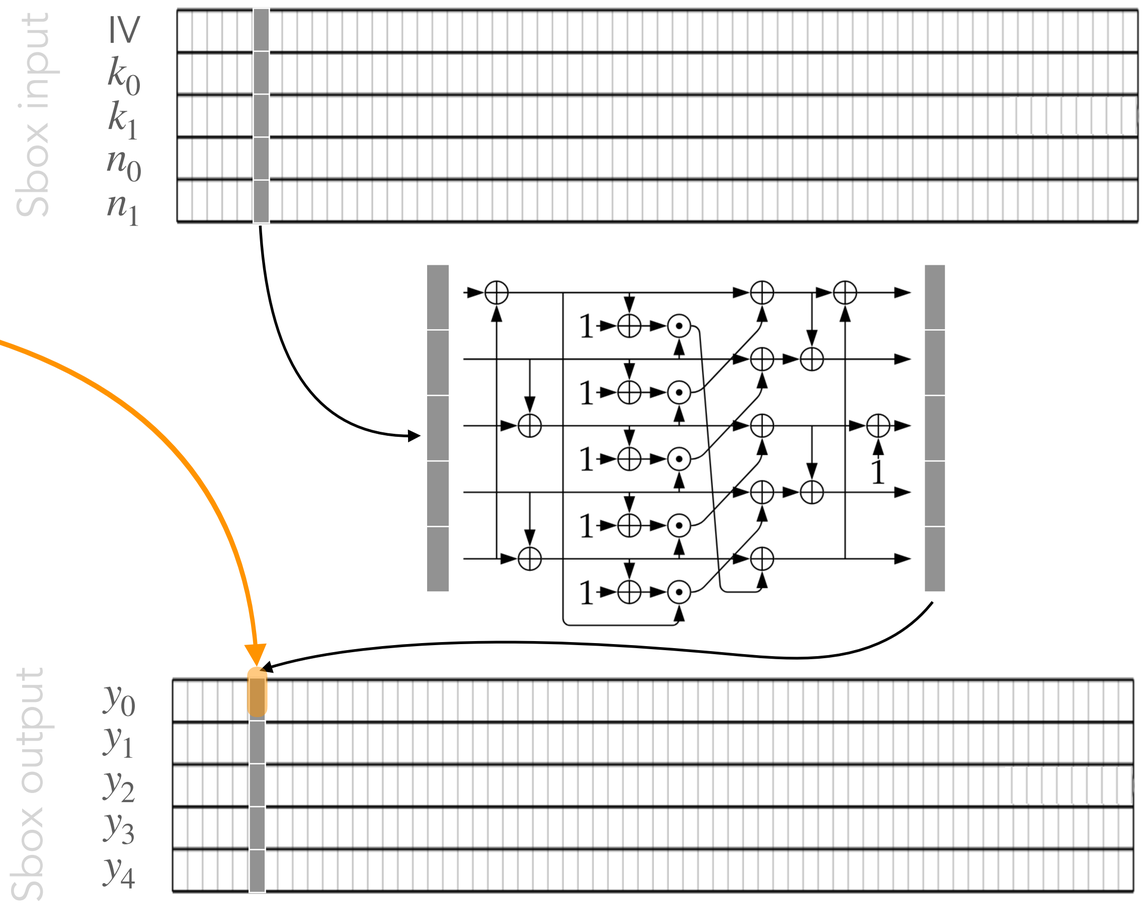
(1) Sbox output as attack point

In an Sbox computation y_0^j:

- 2 bits of key : (k_0^j, k_1^j)
- 2 bits of nonce : (n_0^j, n_1^j)

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

	(k_0^j, k_1^j)			
(n_0^j, n_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	0	1	1	1
(0,1)	0	1	0	0
(1,0)	1	0	0	0
(1,1)	1	0	1	1
Correlation	-1		1	



(1) Sbox output as attack point

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	(k_0^j, k_1^j)			
(n_0^j, n_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
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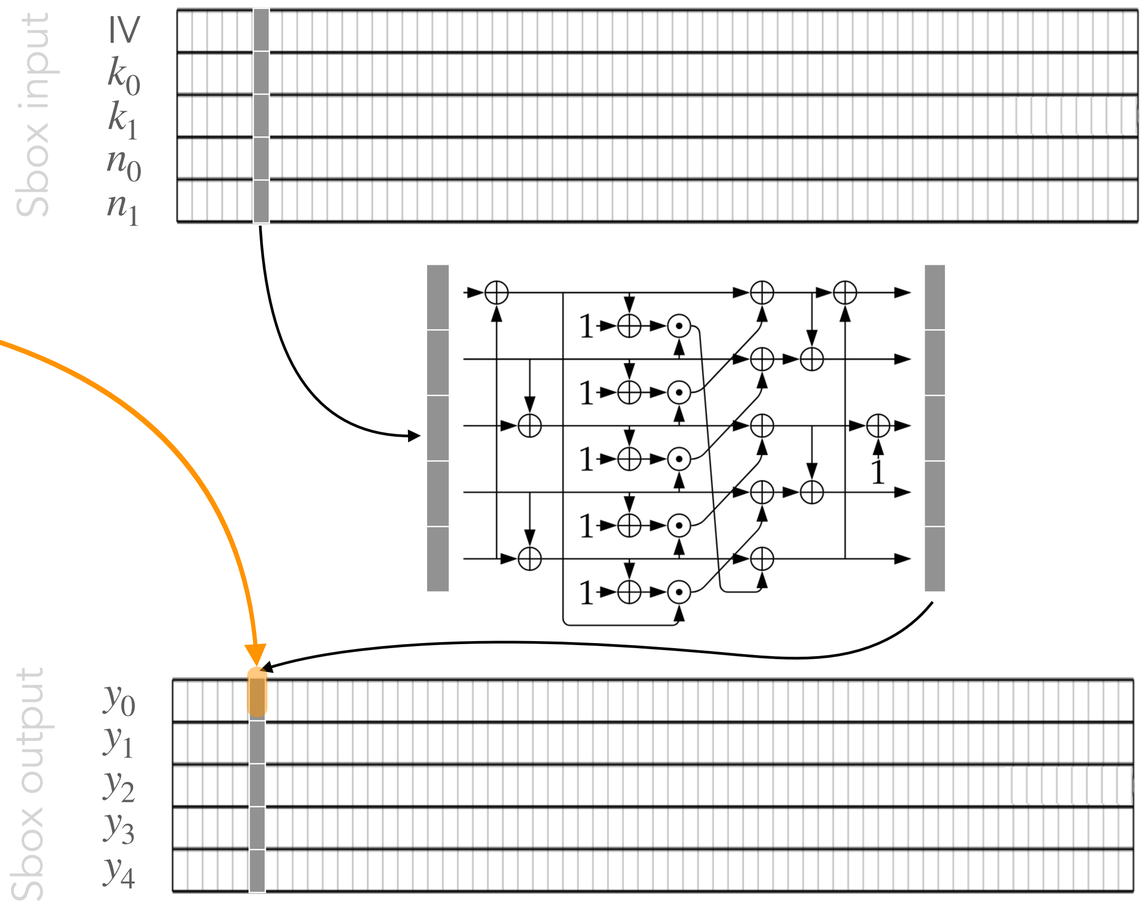
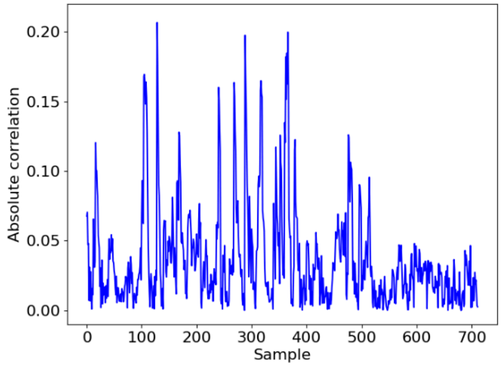
(1,0)	1	0	0	0
(1,1)	1	0	1	1
Correlation	-1		1	



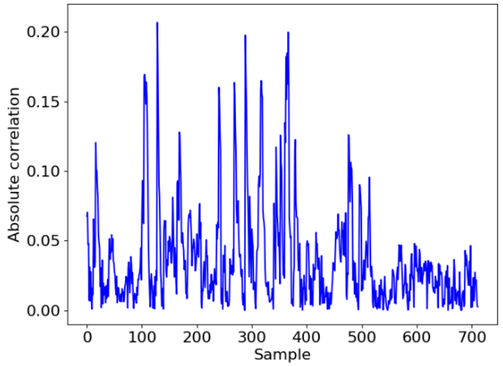
→ We cannot obtain unique (correct) key candidate

(1) Sbox output as attack point

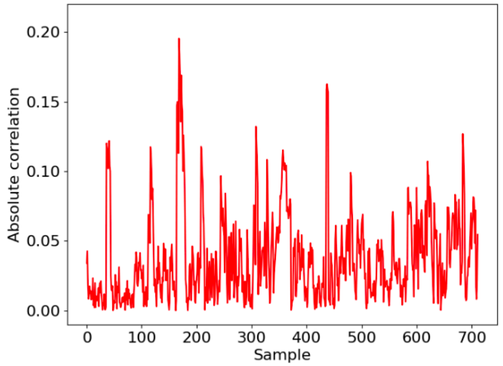
Correlation traces for all key candidates



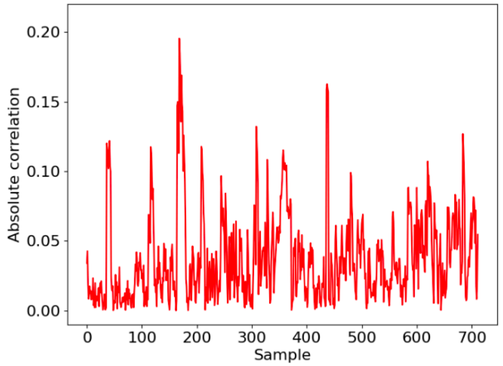
(a) $(k_0^j, k_1^j) = (0, 0)$



(b) $(k_0^j, k_1^j) = (0, 1)$



(c) $(k_0^j, k_1^j) = (1, 0)$



(d) $(k_0^j, k_1^j) = (1, 1)$

(1) Sbox output as attack point

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	1	-	-
(0,1)	1	1	-	-
(1,0)	-	-	1	1
(1,1)	-	-	1	1

y_0^j

(1) Sbox output as attack point

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	1	-	-
(0,1)	1	1	-	-
(1,0)	-	-	1	1
(1,1)	-	-	1	1

y_0^j

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	-	-	1
(0,1)	-	1	1	-
(1,0)	-	1	1	-
(1,1)	1	-	-	1

y_1^j

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	1	1	1
(0,1)	1	1	1	1
(1,0)	1	1	1	1
(1,1)	1	1	1	1

y_2^j and y_3^j

k_0^j	0	1
0	1	0
1	0	1

y_4^j

(1) Sbox output as attack point

Hamming weight of Sbox output: HW($y_0^j \mid y_1^j \mid y_2^j \mid y_3^j \mid y_4^j$)

(1) Sbox output as attack point

Hamming weight of Sbox output: HW$(y_0^j \,|\, y_1^j \,|\, y_2^j \,|\, y_3^j \,|\, y_4^j)$

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1.00	0.15	0.89	0.87
(0,1)	0.15	1.00	0.48	0.09
(1,0)	0.89	0.48	1.00	0.90
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(1) Sbox output as attack point

Hamming weight of Sbox output: HW($y_0^j \,|\, y_1^j \,|\, y_2^j \,|\, y_3^j \,|\, y_4^j$)

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→ Not effective for CPA attacks

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Hamming weight of Sbox output: HW$(y_0^j \,|\, y_1^j \,|\, y_2^j \,|\, y_3^j \,|\, y_4^j)$

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→ Not effective for CPA attacks

This may explain why Ramezanpour et al., 2020 failed

(2) Fine-tuned linear diffusion output as attack point

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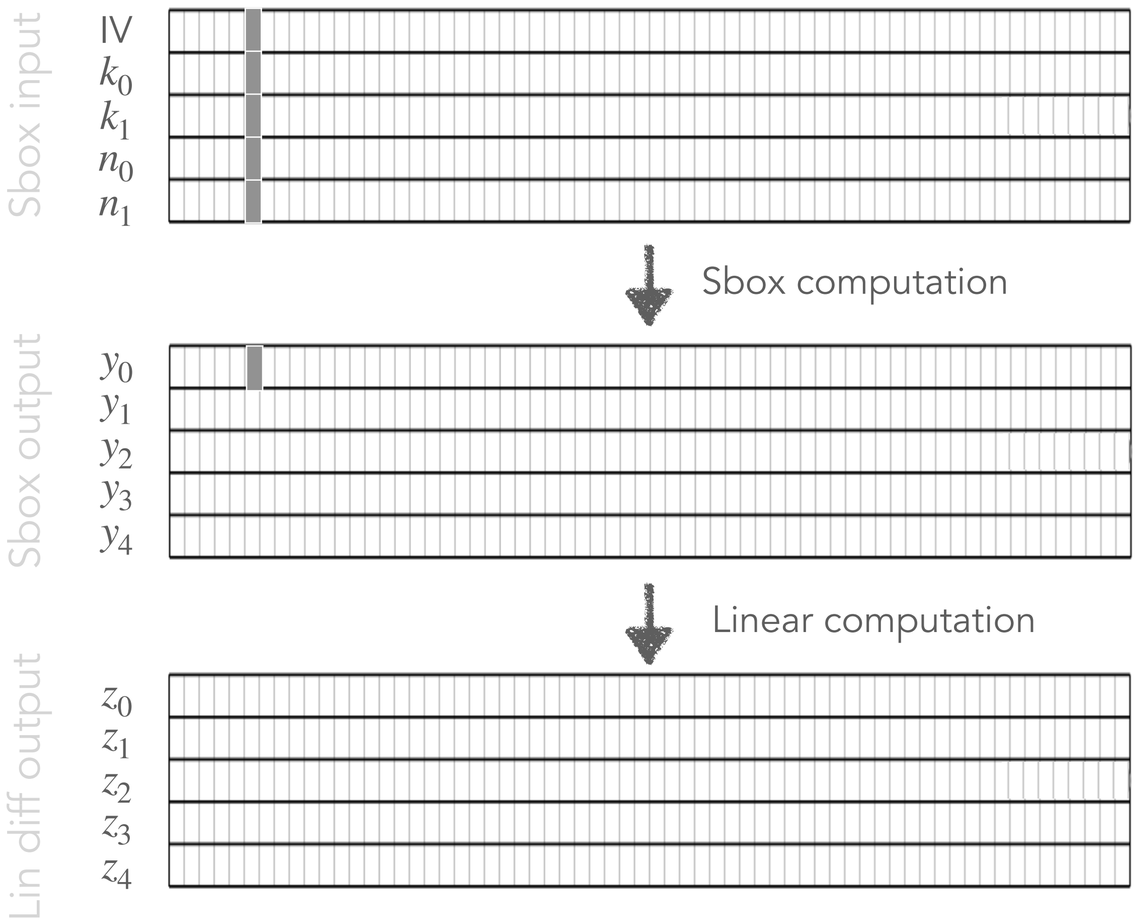
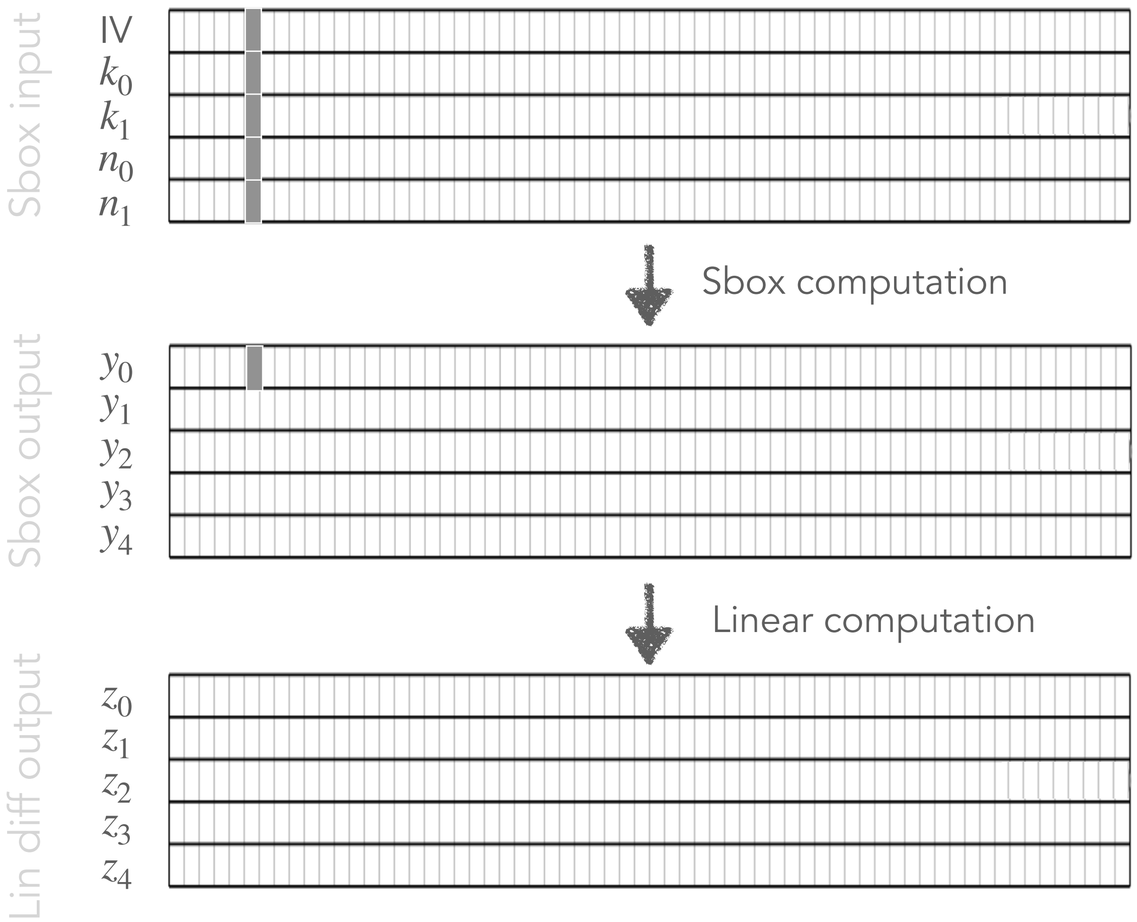


(2) Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$



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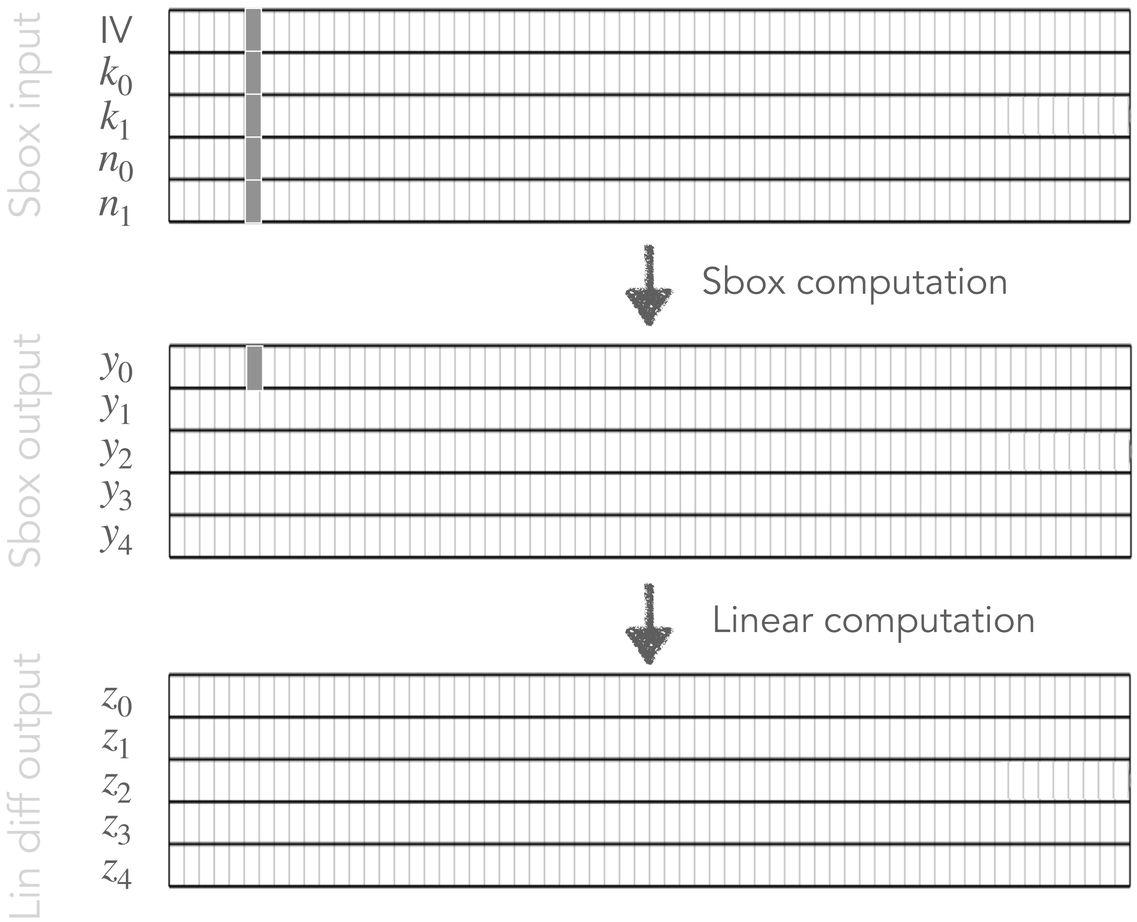

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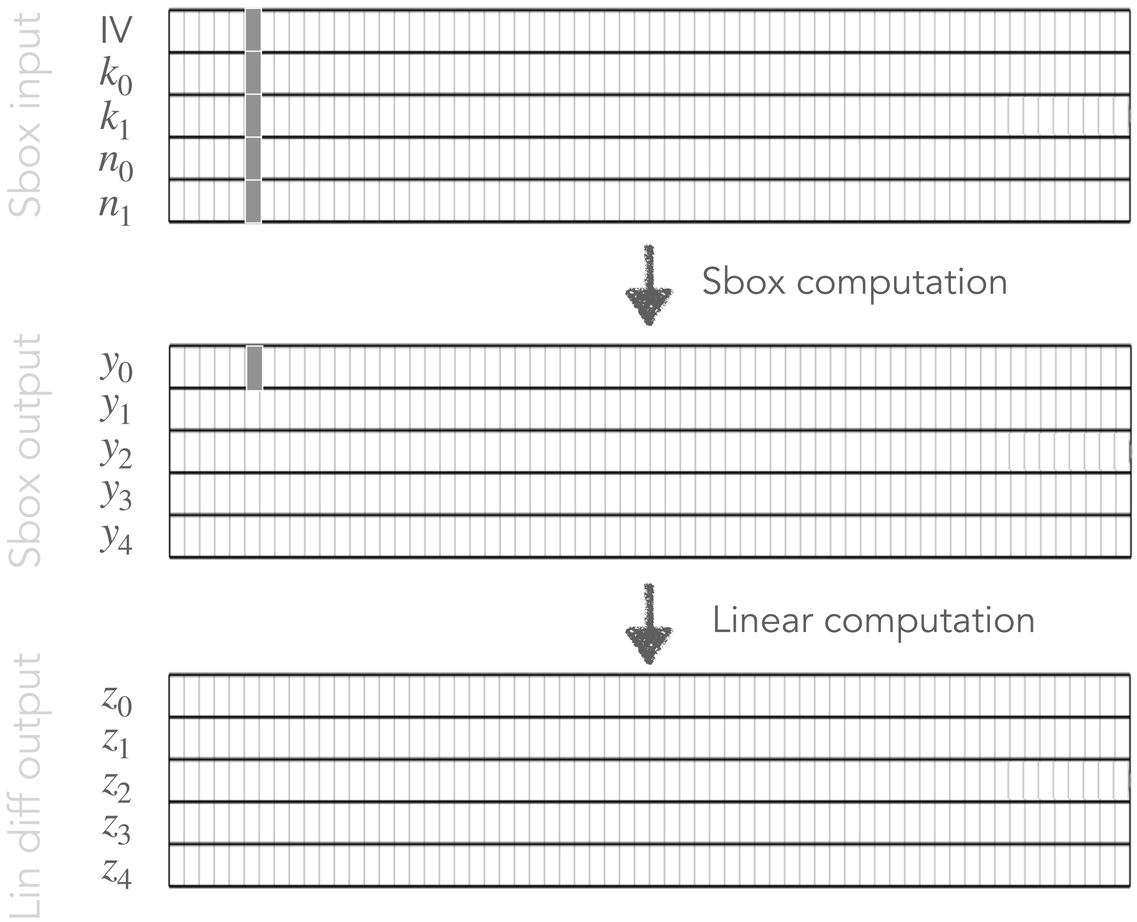
$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$



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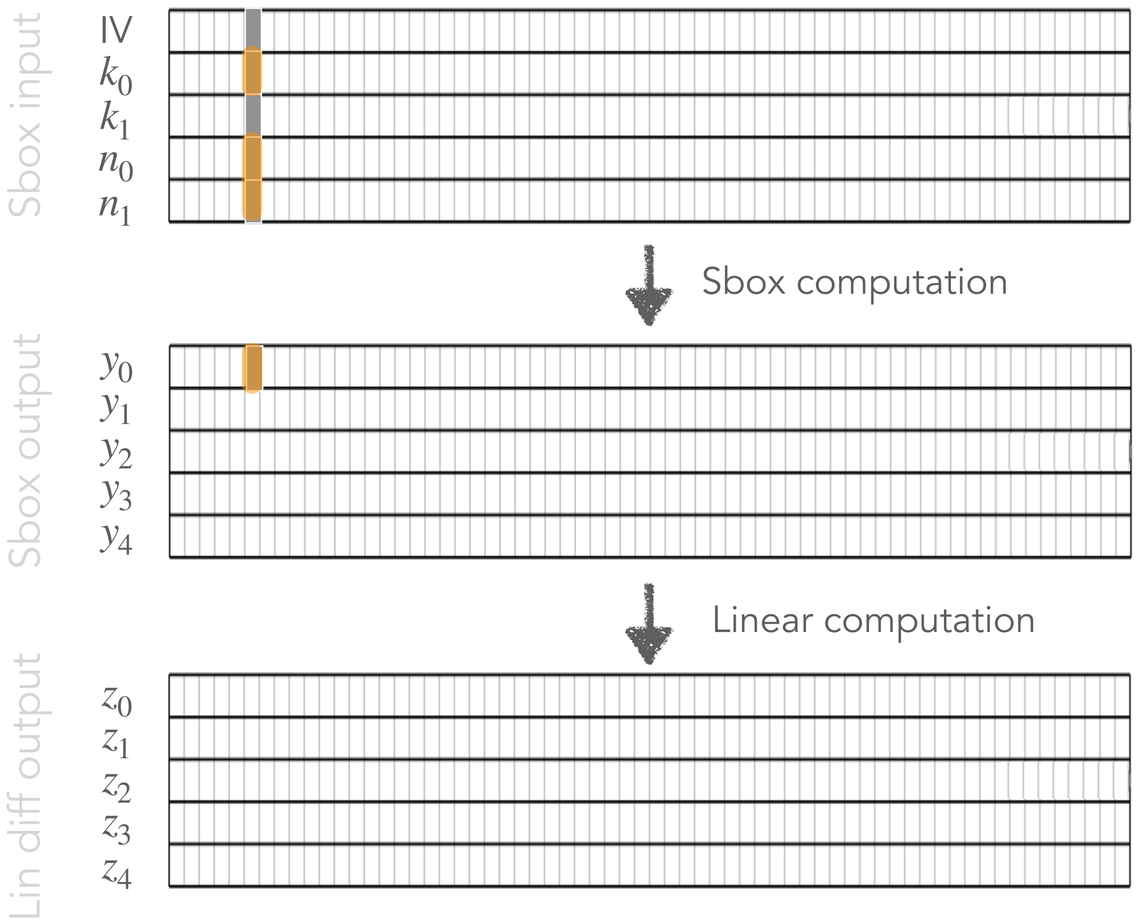

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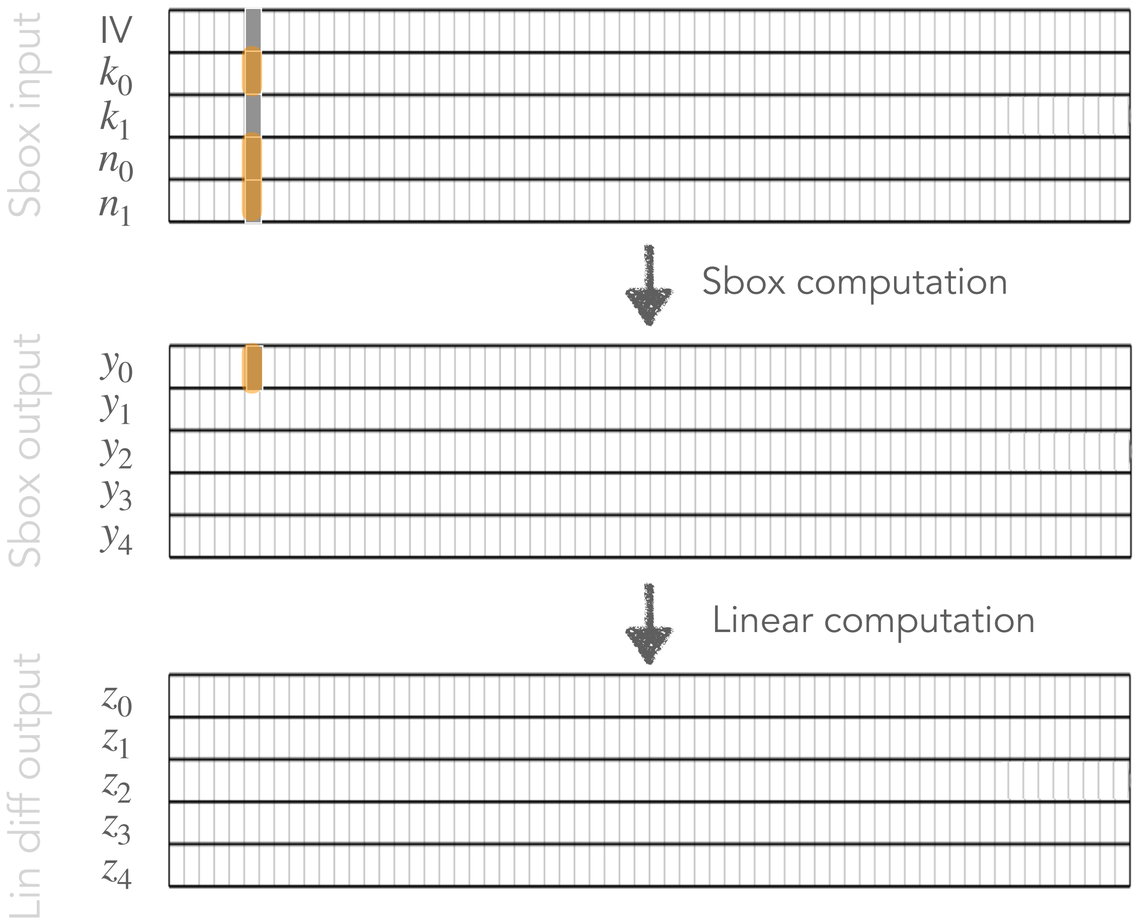
$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

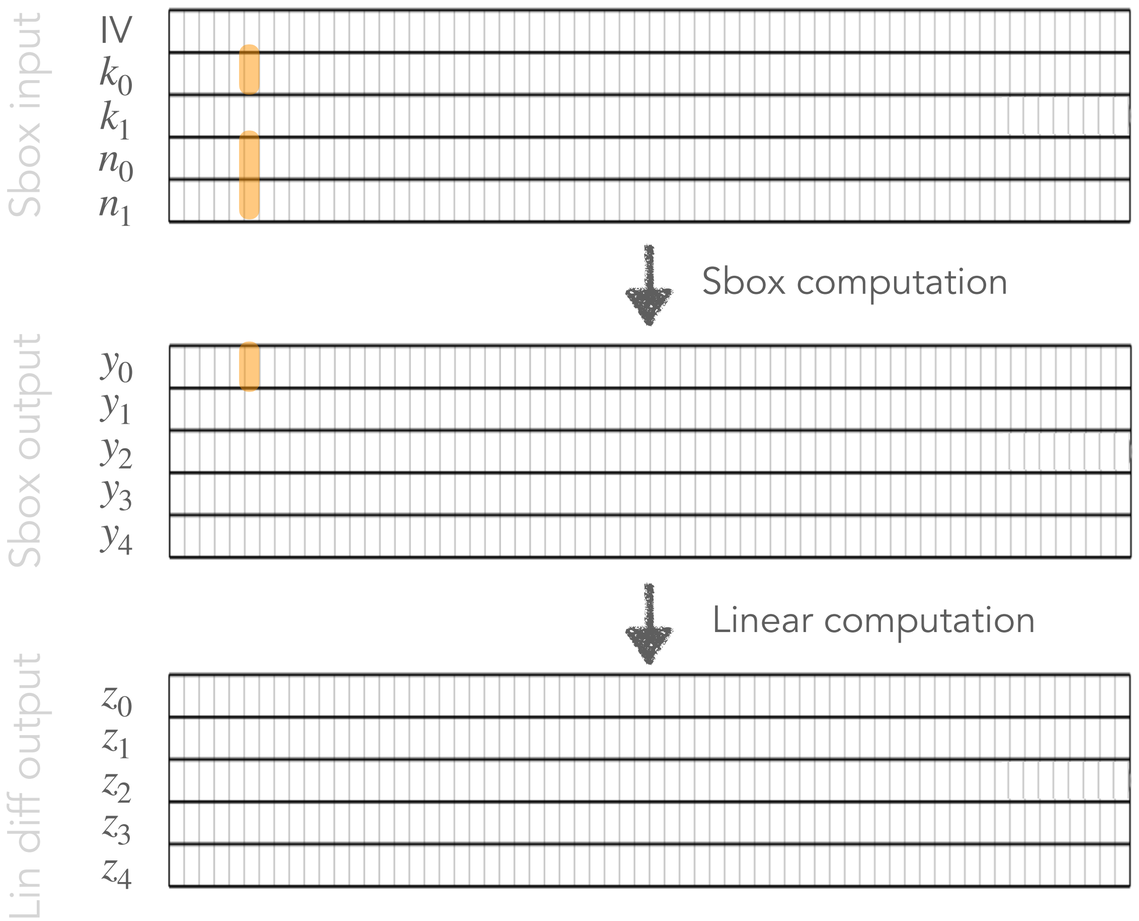
	k_0^j	
(n_0^j, n_1^j)	0	1
(0,0)	0	1
(0,1)	0	0
(1,0)	1	0
(1,1)	1	1
Correlation	0	



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$



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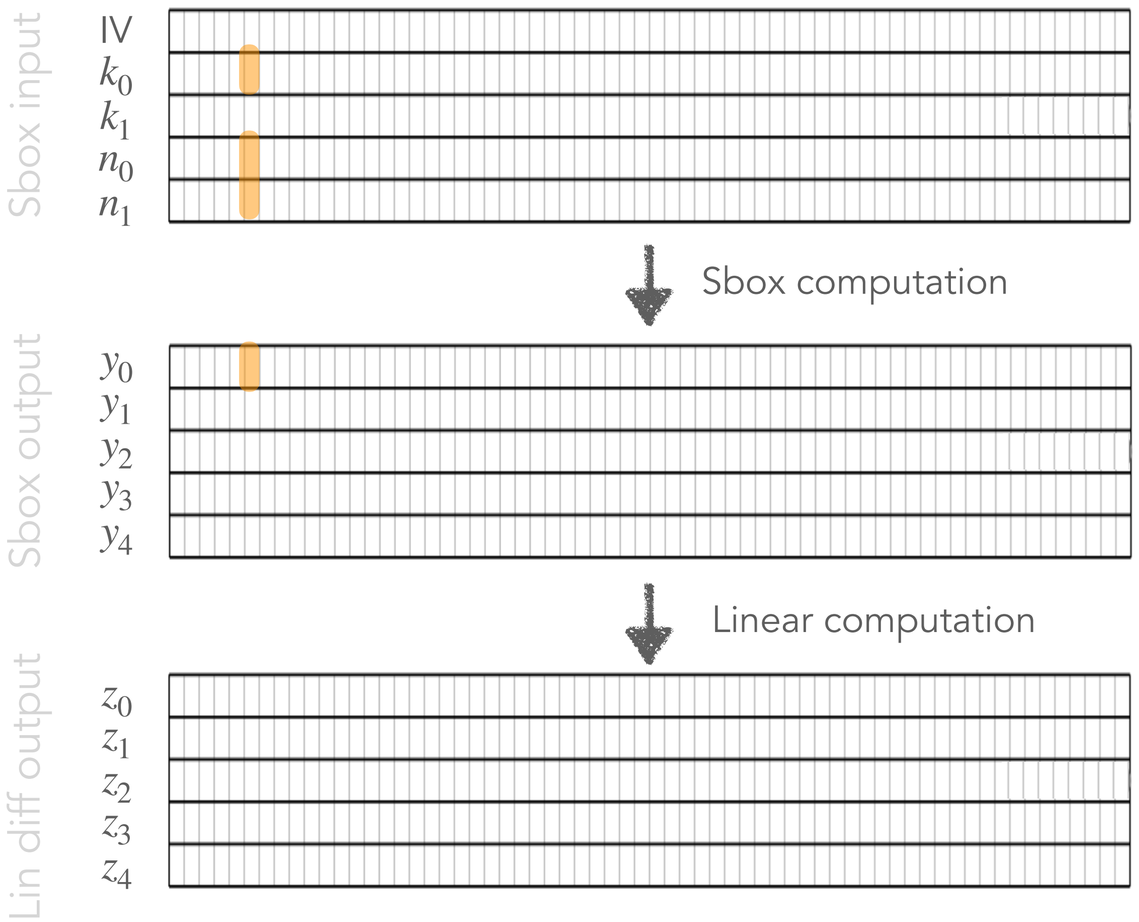
Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)



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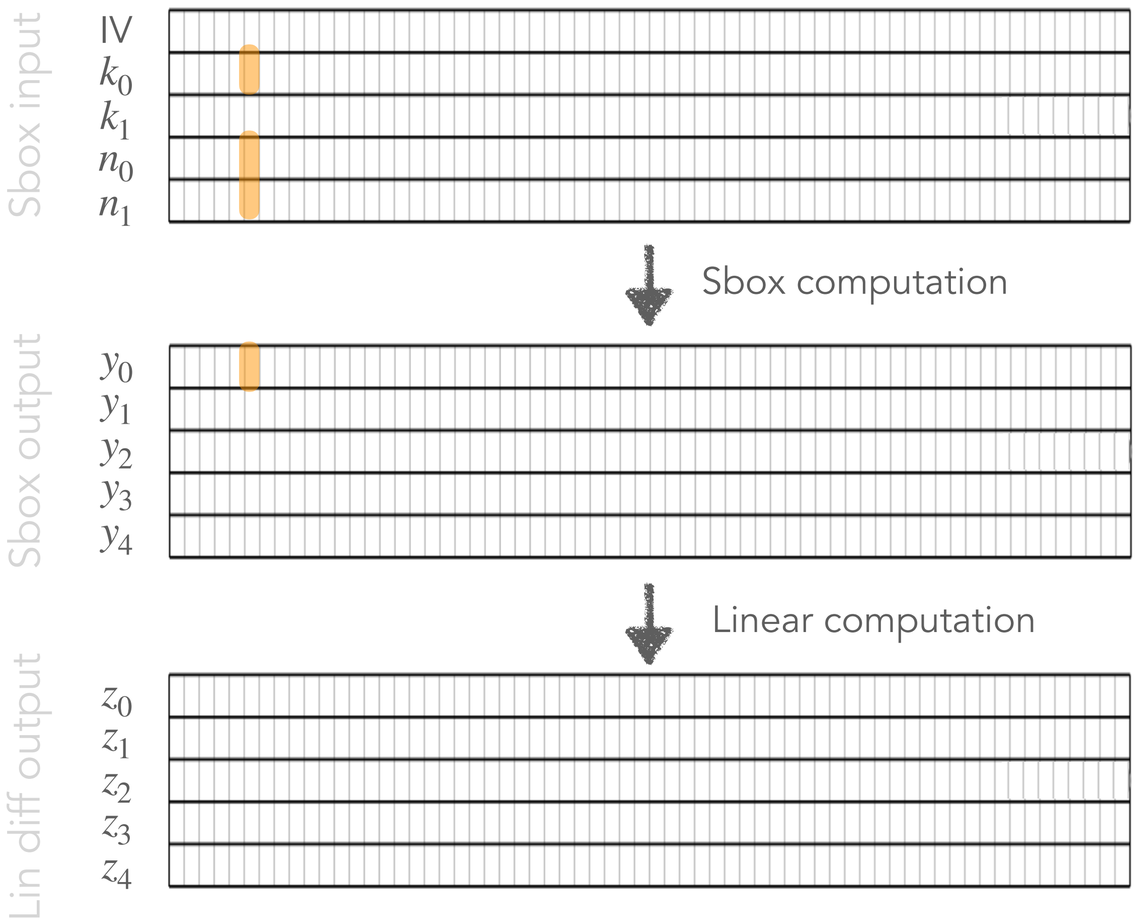
Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$



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Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

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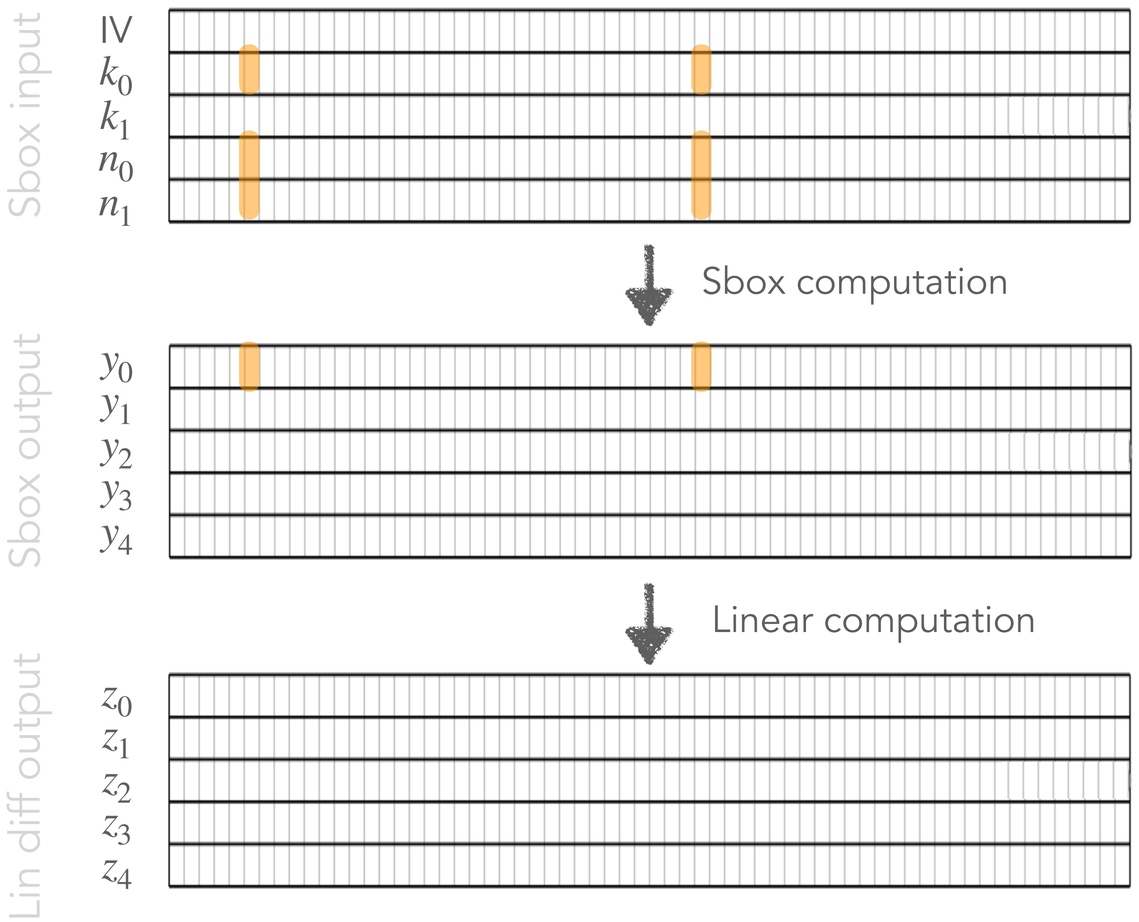

Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

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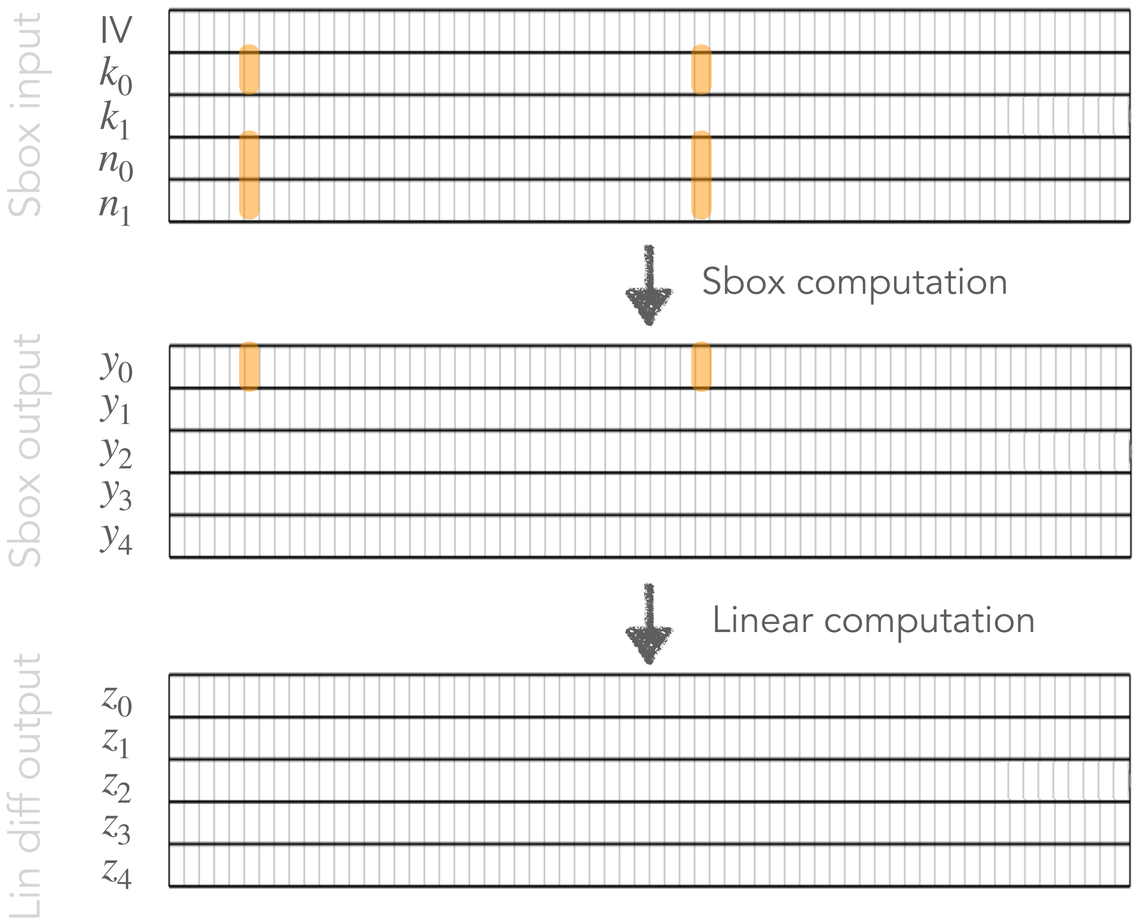
Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\tilde{y}_0^{j+45} = k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

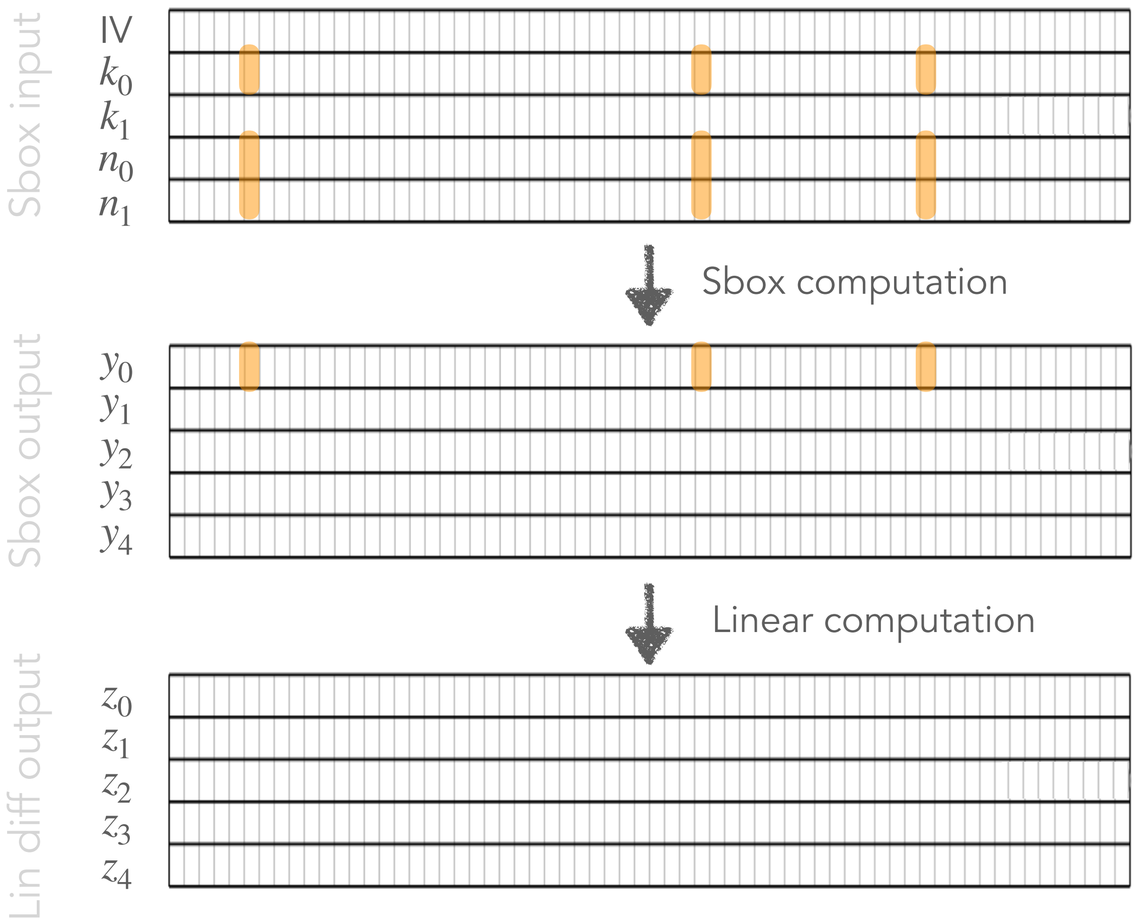
Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\tilde{y}_0^{j+45} = k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

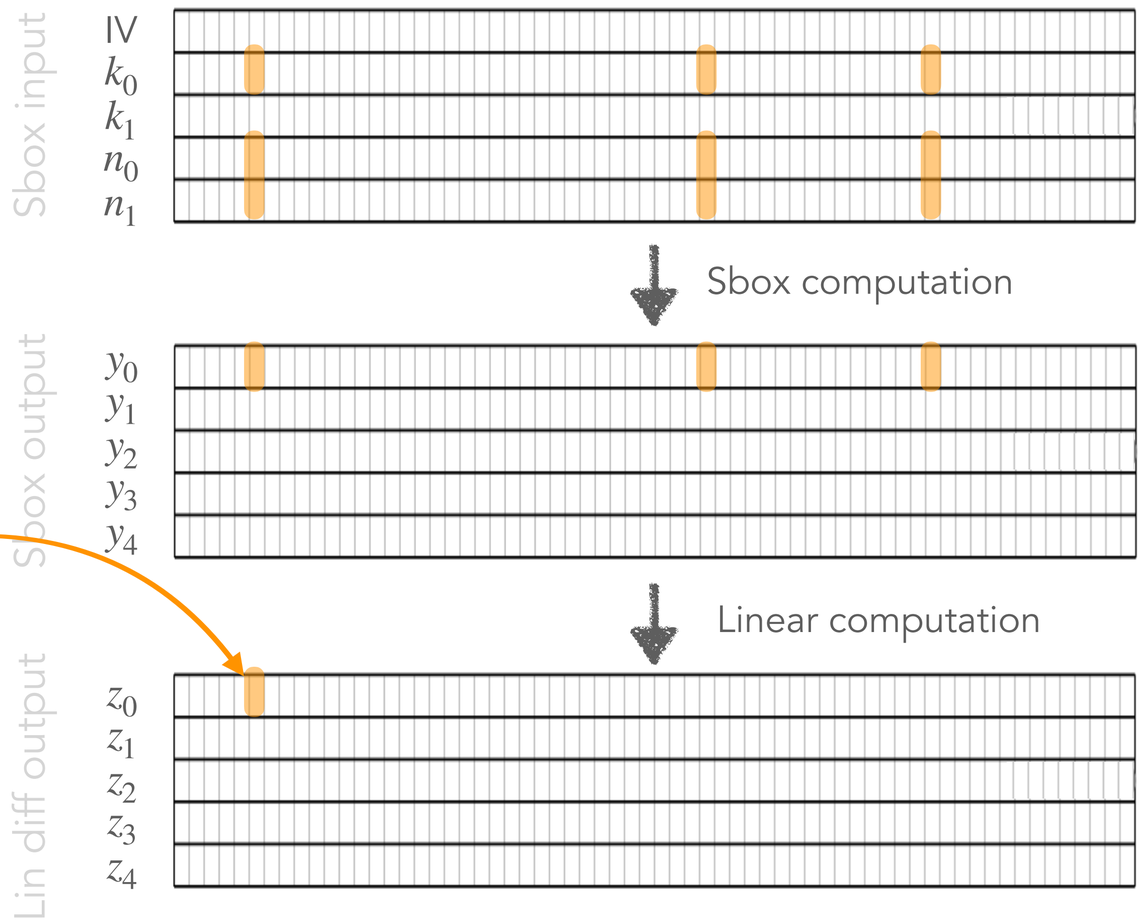
Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\tilde{y}_0^{j+45} = k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$

Linear computation:

$$\tilde{z}_0^j = \tilde{y}_0^j \oplus \tilde{y}_0^{j+36} \oplus \tilde{y}_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

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$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

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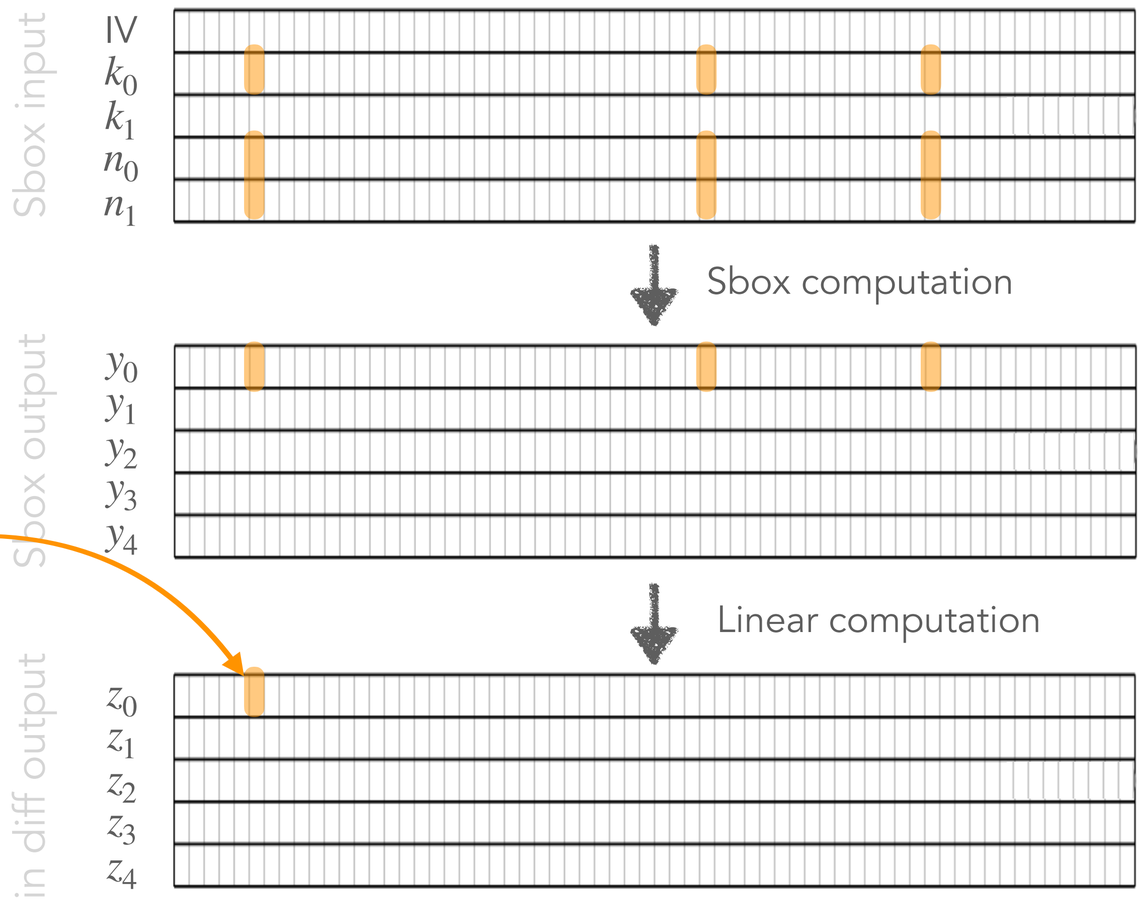
Linear computation:

$$\tilde{z}_0^j = \tilde{y}_0^j \oplus \tilde{y}_0^{j+36} \oplus \tilde{y}_0^{j+45}$$

$$\tilde{z}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36} \oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

$$\begin{aligned}\tilde{z}_0^j = {} & k_0^j(n_1^j \oplus 1) \oplus n_0^j \\ & \oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36} \\ & \oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}\end{aligned}$$

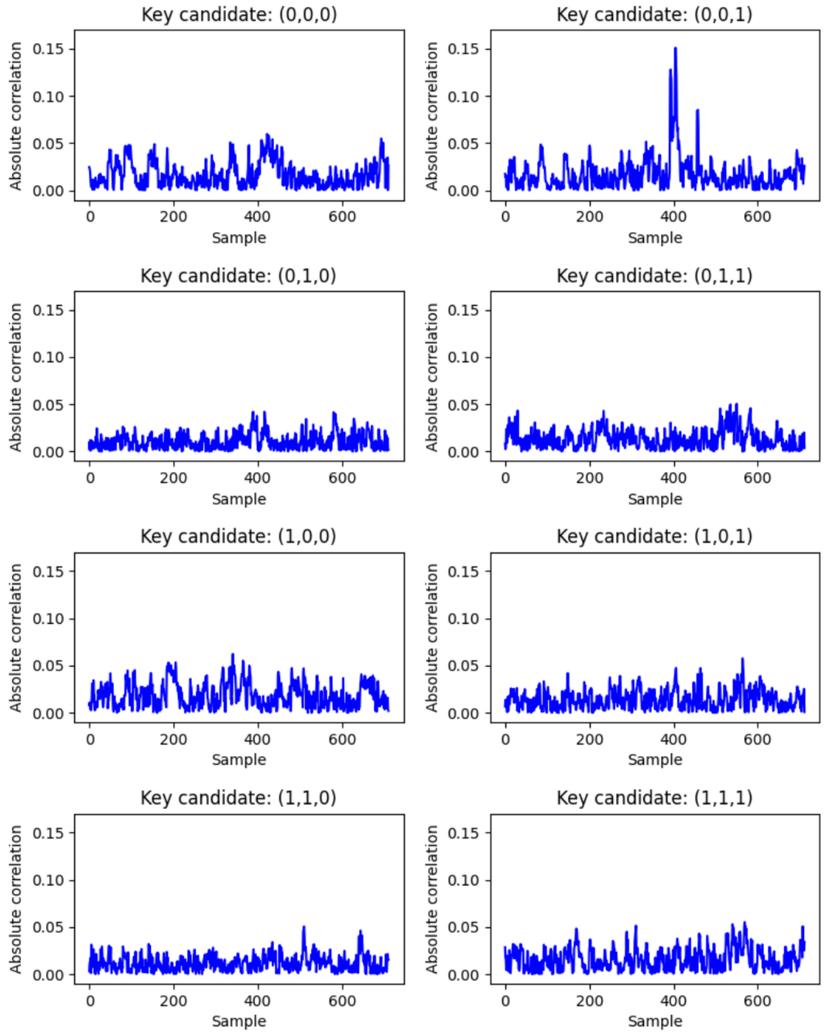
Fine-tuned linear diffusion output as attack point

Correlations of distributions associated to all key pairs

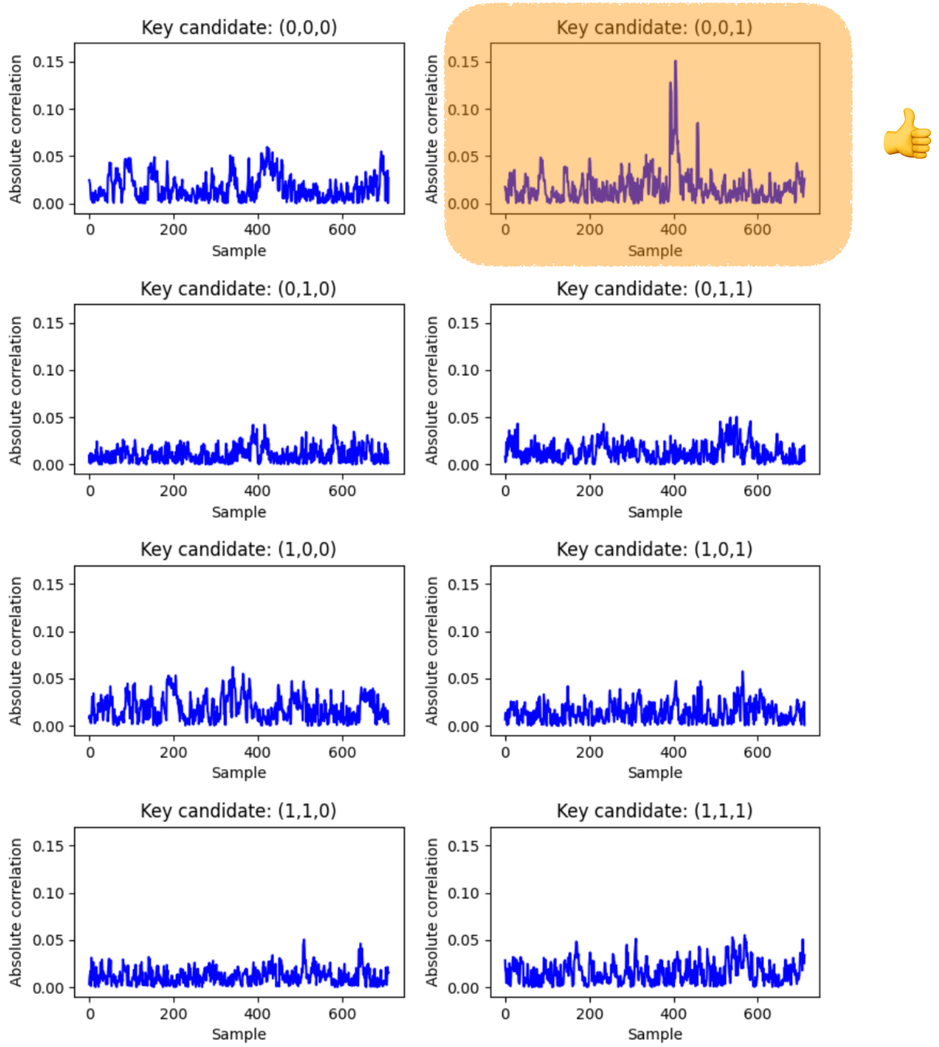
$(k_0^j, k_0^{j+36}, k_0^{j+45})$	(0,0,0)	(0,0,1)	(0,1,0)	(0,1,1)	(1,0,0)	(1,0,1)	(1,1,0)	(1,1,1)
(0,0,0)	1	-	-	-	-	-	-	-
(0,0,1)	-	1	-	-	-	-	-	-
(0,1,0)	-	-	1	-	-	-	-	-
(0,1,1)	-	-	-	1	-	-	-	-
(1,0,0)	-	-	-	-	1	-	-	-
(1,0,1)	-	-	-	-	-	1	-	-
(1,1,0)	-	-	-	-	-	-	1	-
(1,1,1)	-	-	-	-	-	-	-	1

$$\tilde{z}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \\ \oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36} \\ \oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$

Fine-tuned linear diffusion output as attack point



Fine-tuned linear diffusion output as attack point



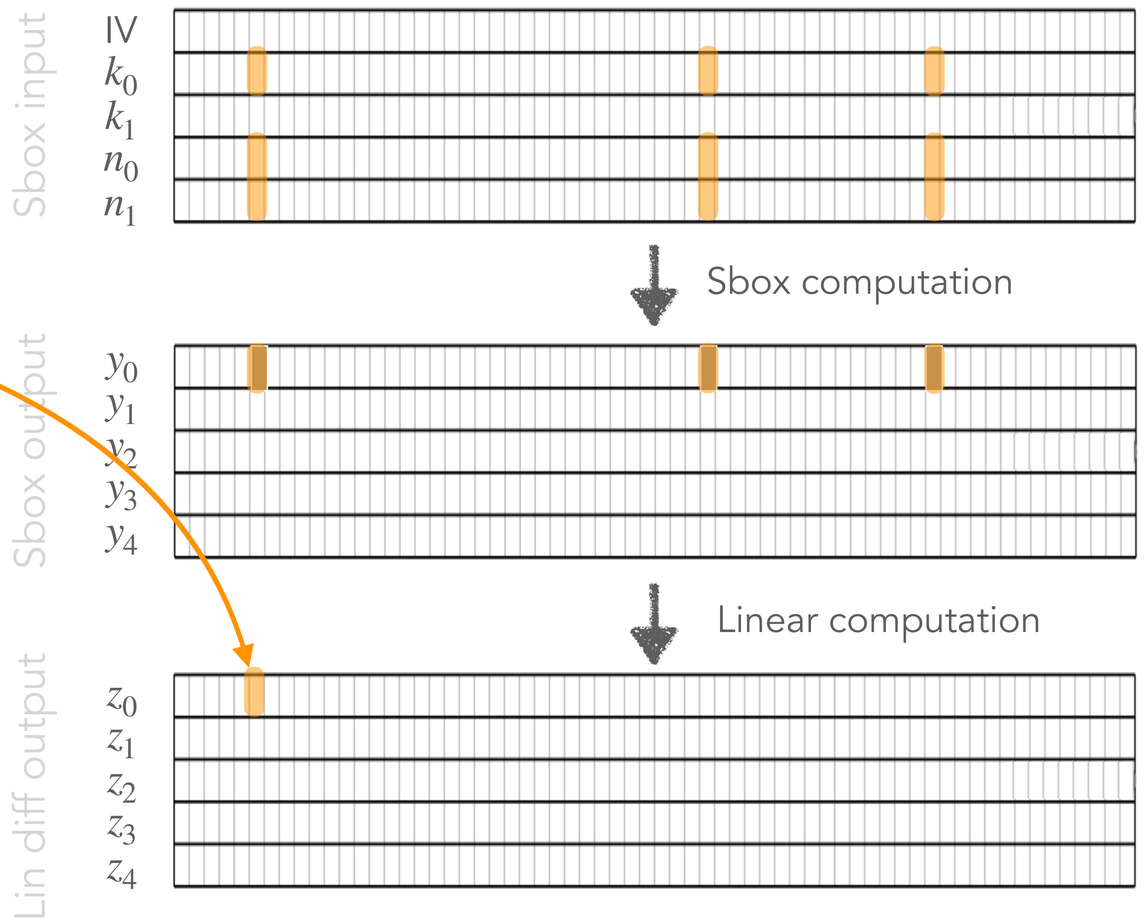
$\tilde{y}_0^j$ as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)



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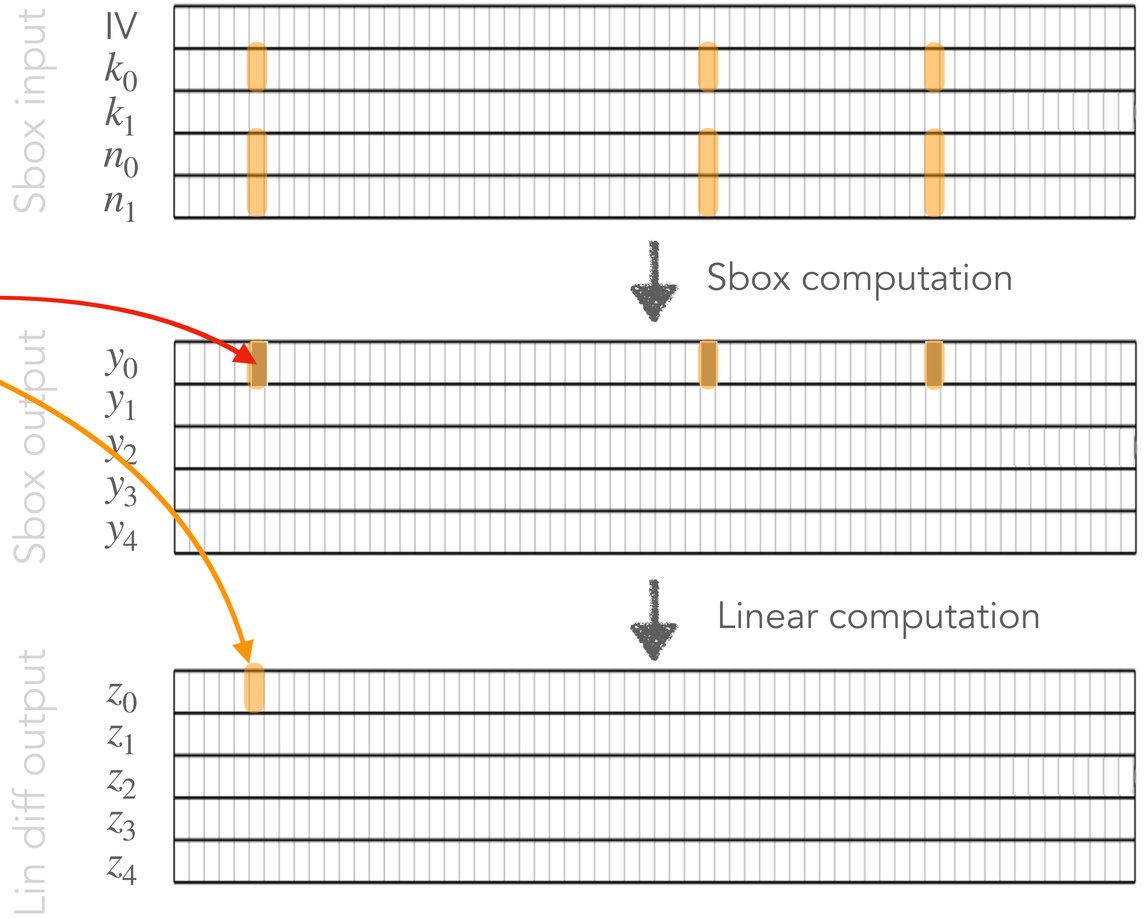
Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Software implementation:
Can we use $\tilde{y}_0^j$ as attack point ?



$\tilde{y}_0^j$ as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j V^j \oplus k_1^j \oplus V^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

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Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Software implementation:
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$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j = \begin{cases} n_0^j & \text{if } k_0^j = 0, \\ n_0^j \oplus n_1^j \oplus 1 & \text{if } k_0^j = 1. \end{cases}$$

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Samwel and Daemen, 2018

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→ Correlated with activity of n_0

$\tilde{y}_0^j$ as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j v^j \oplus k_1^j \oplus v^j$$

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Samwel and Daemen, 2018

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→ Correlated with activity of n_0

→ Correlated with activity of $n_0 \oplus n_1$

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Samwel and Daemen, 2018

Hardware implementation
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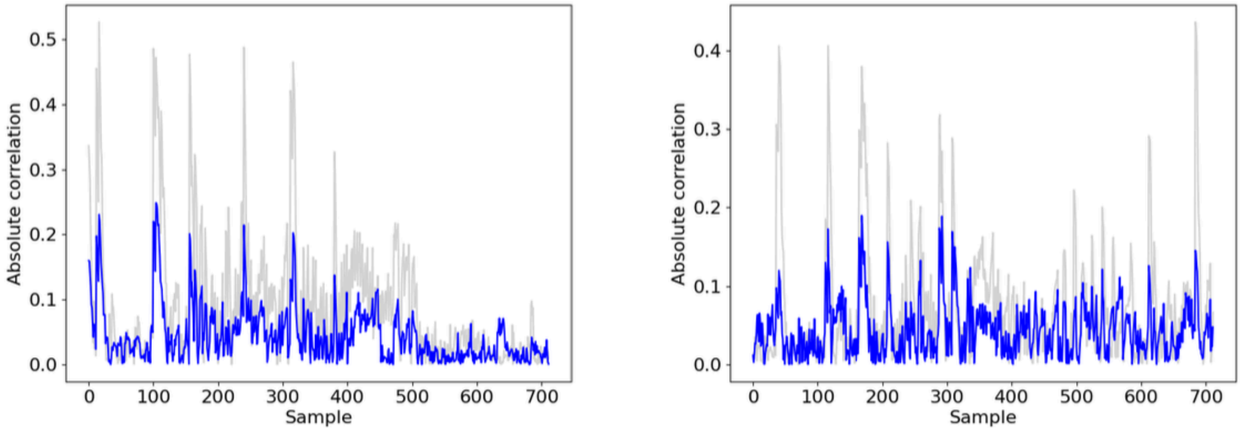
Software implementation:
Can we use $\tilde{y}_0^j$ as attack point ?



(a) $k_0^j = 0$ (b) $k_0^j = 1$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j = \begin{cases} n_0^j & \text{if } k_0^j = 0, \\ n_0^j \oplus n_1^j \oplus 1 & \text{if } k_0^j = 1. \end{cases}$$

→ Correlated with activity of n_0

→ Correlated with activity of $n_0 \oplus n_1$

$\tilde{y}_0^j$ as attack point ?

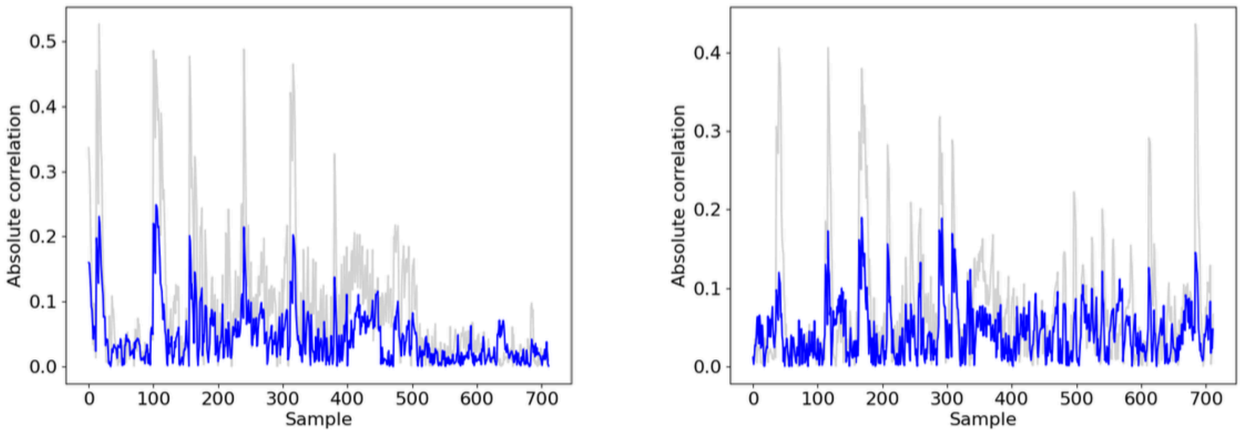
$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathrm{IV}^j \oplus k_1^j \oplus \mathrm{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Software implementation:
Can we use $\tilde{y}_0^j$ as attack point ?



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→ Correlated with activity of n_0

→ Correlated with activity of $n_0 \oplus n_1$

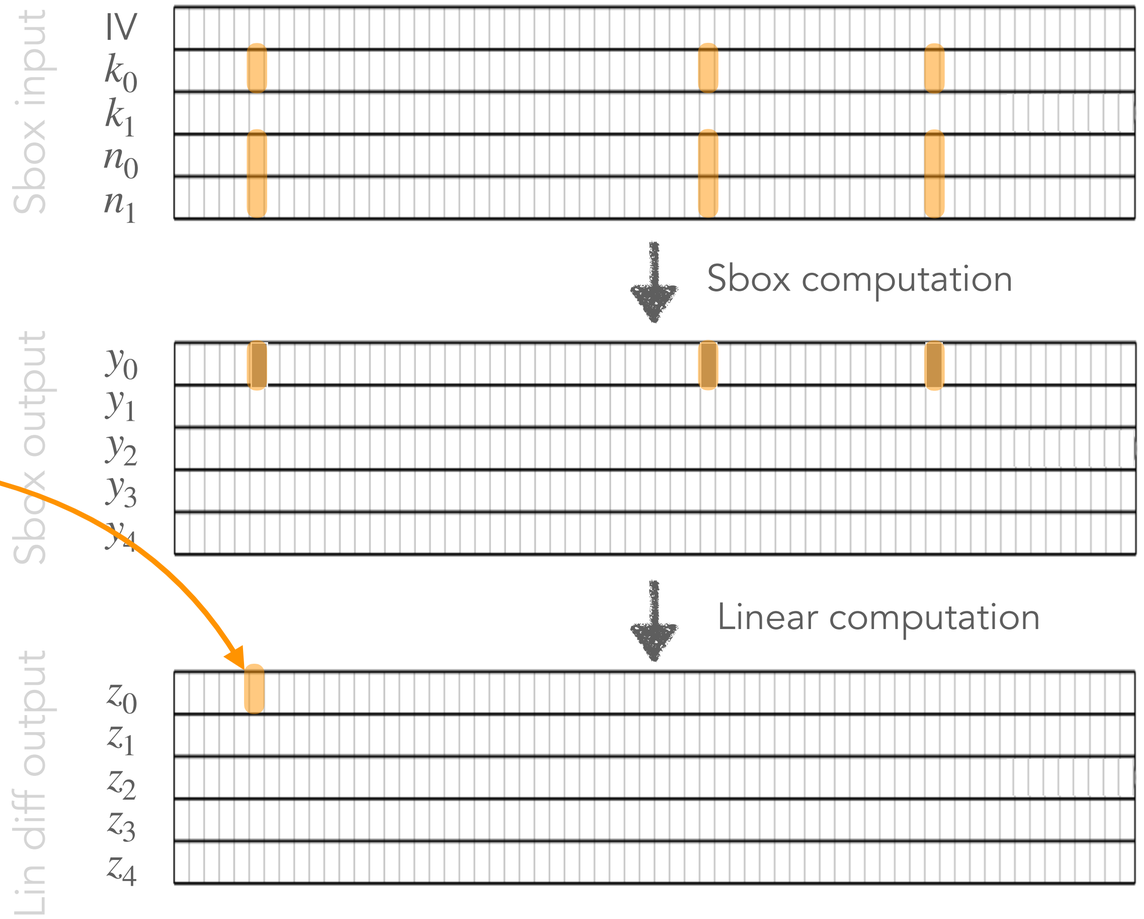
→ Not effective for CPA attacks

The best choice

for both hardware and software implementations

Samwel and Daemen, 2018

$$\tilde{z}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36} \oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$

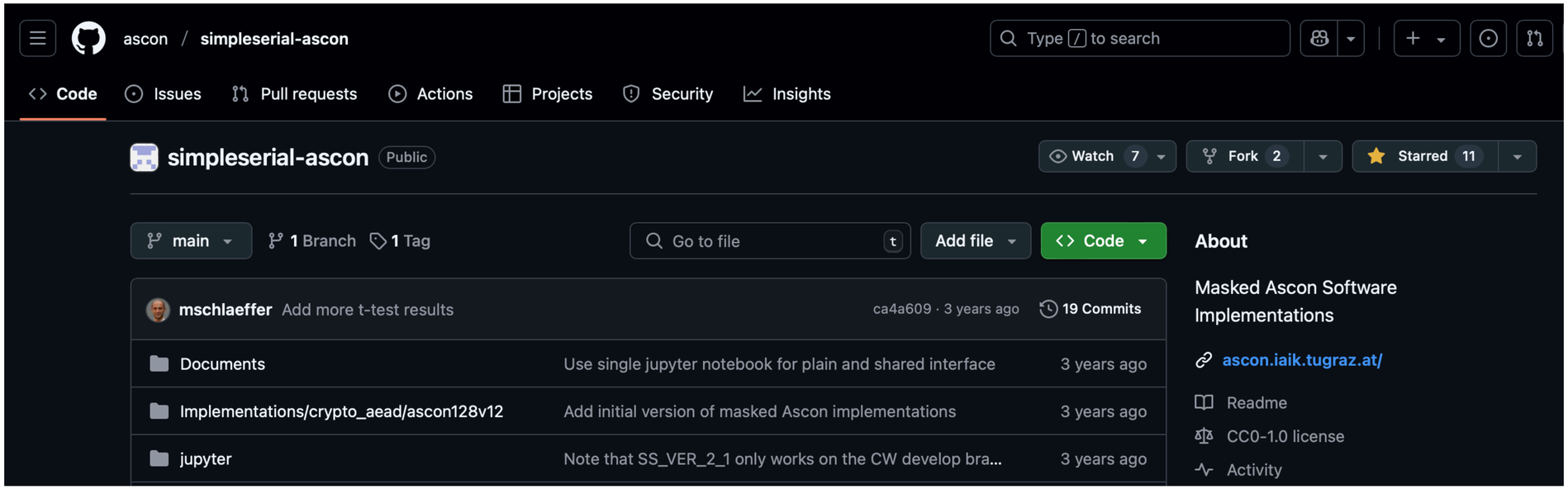


Second-Order CPA attack

Target implementation

Masked software implementations with 2 shares
by Ascon team

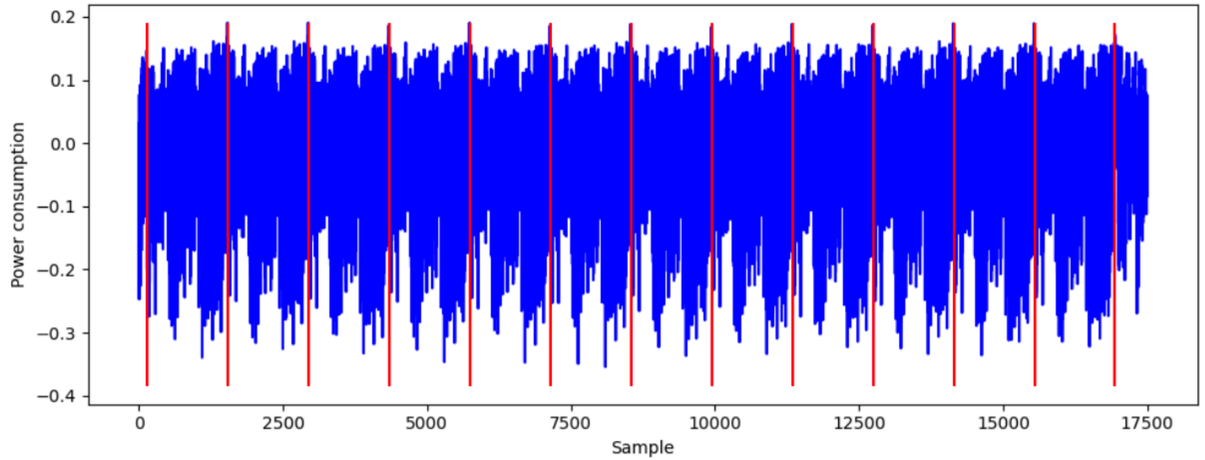
https://github.com/ascon/simpleserial-ascon



Target implementation

Target implementation

Power consumption of the first 12 rounds from ChipWhisperer ARM with Cortex-M3 core:



Target implementation

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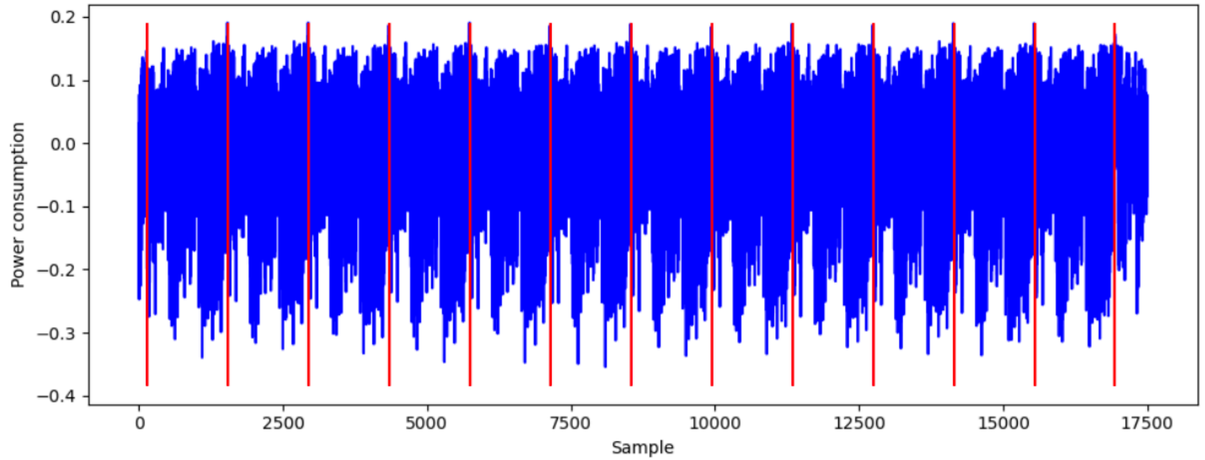
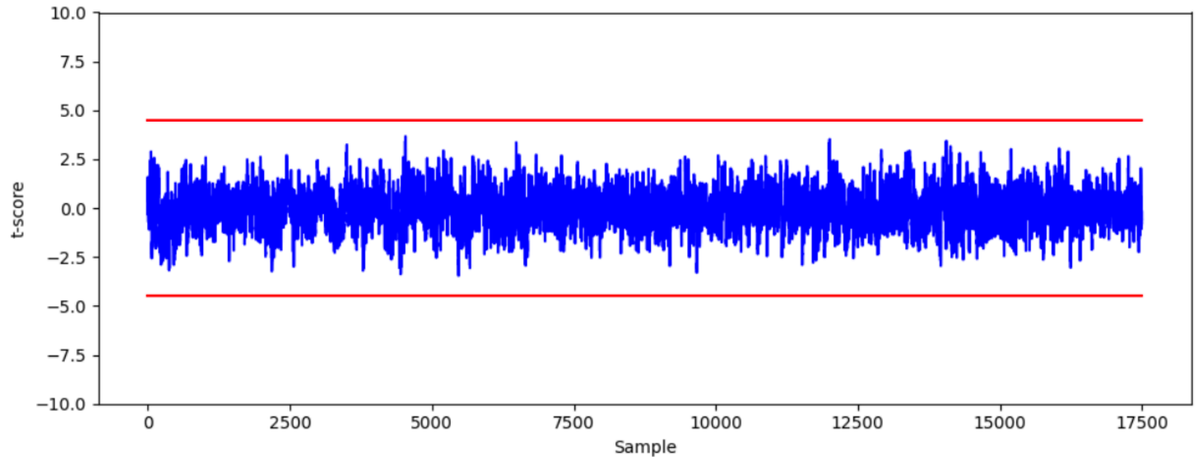
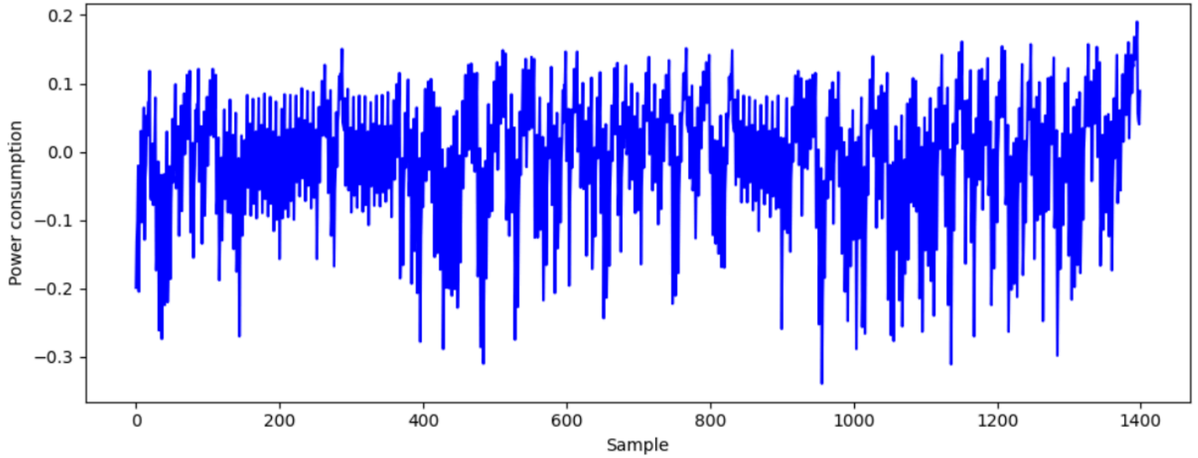


Verify first-order leakage by TVLA:

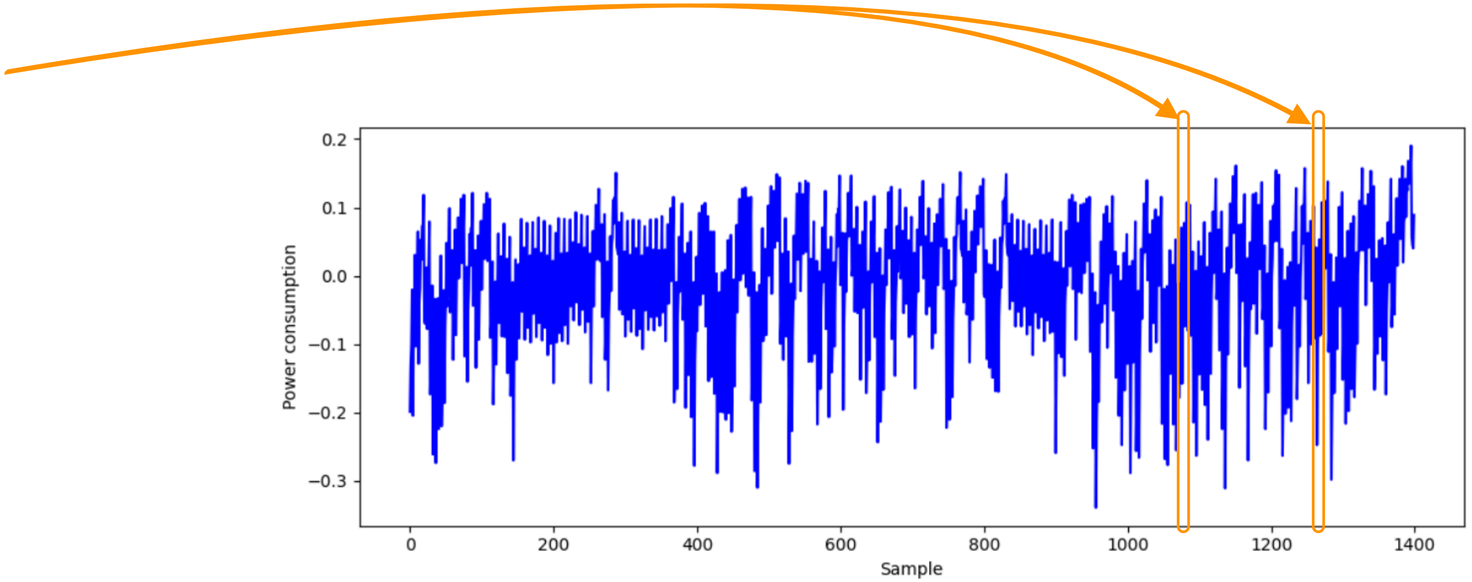


Second-Order CPA



Second-Order CPA

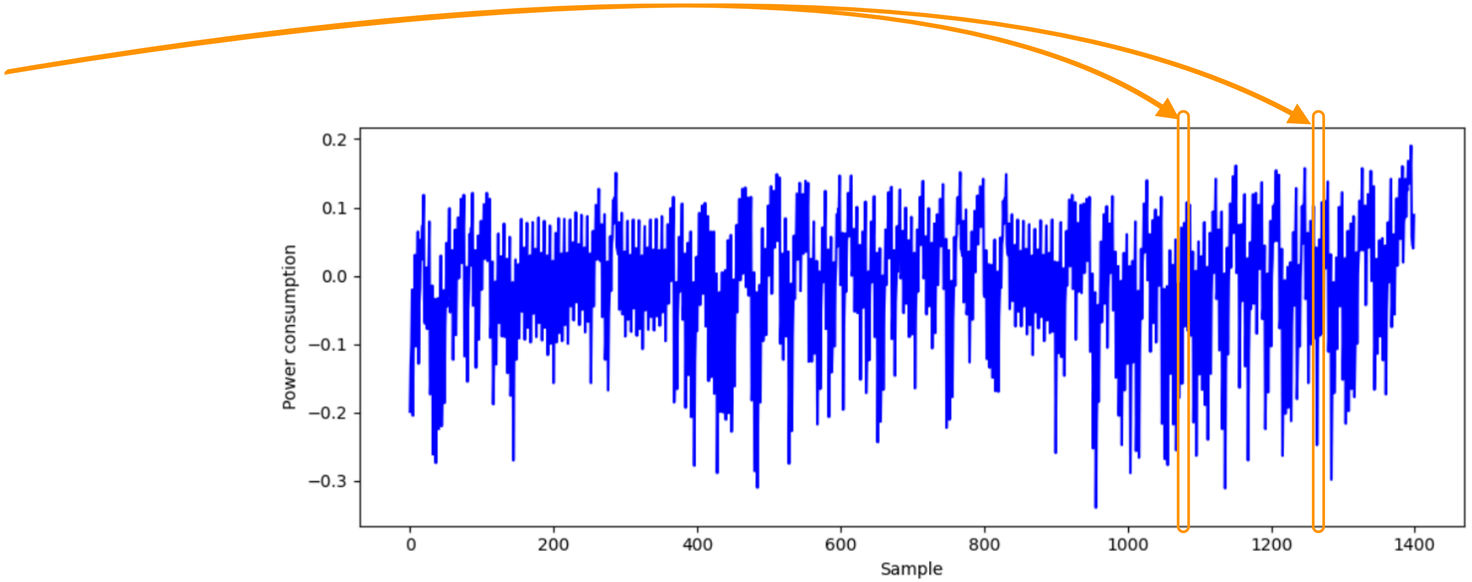
Combine two points on the trace
(by normalized product)



Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:



Second-Order CPA

Combine two points on the trace
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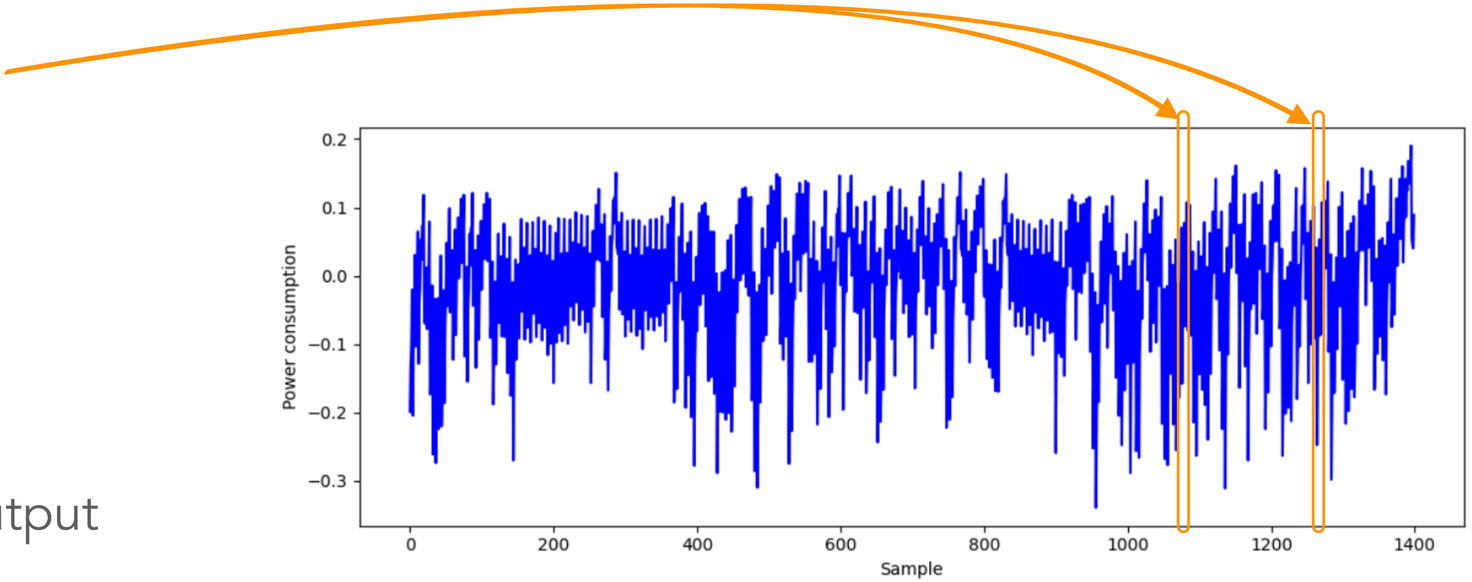
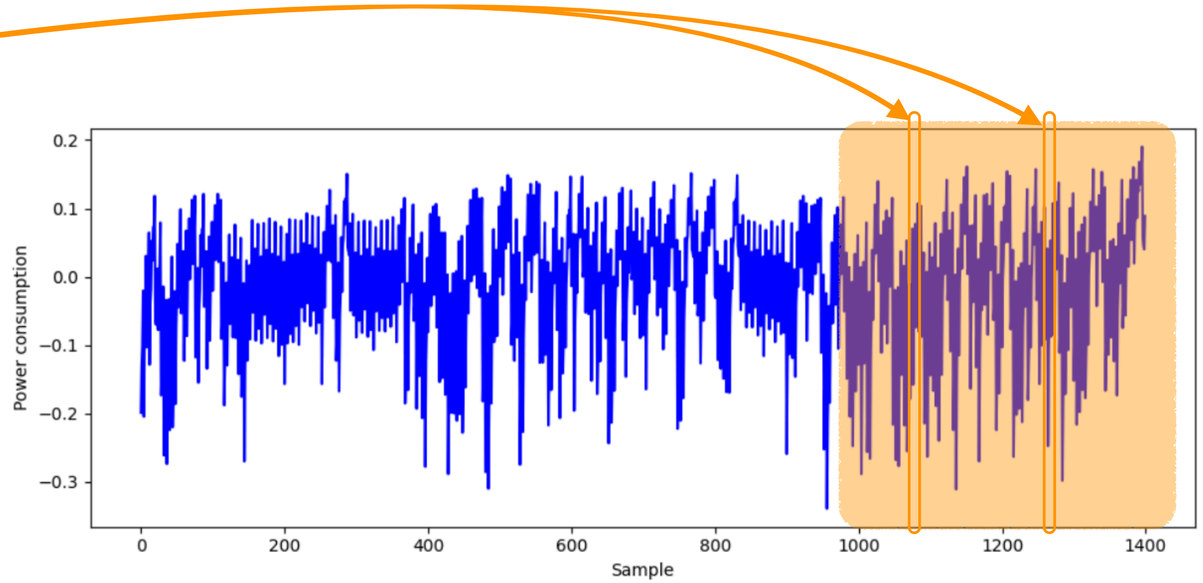


Optimizations:

- Attack point at linear layer output

Second-Order CPA

Combine two points on the trace
(by normalized product)

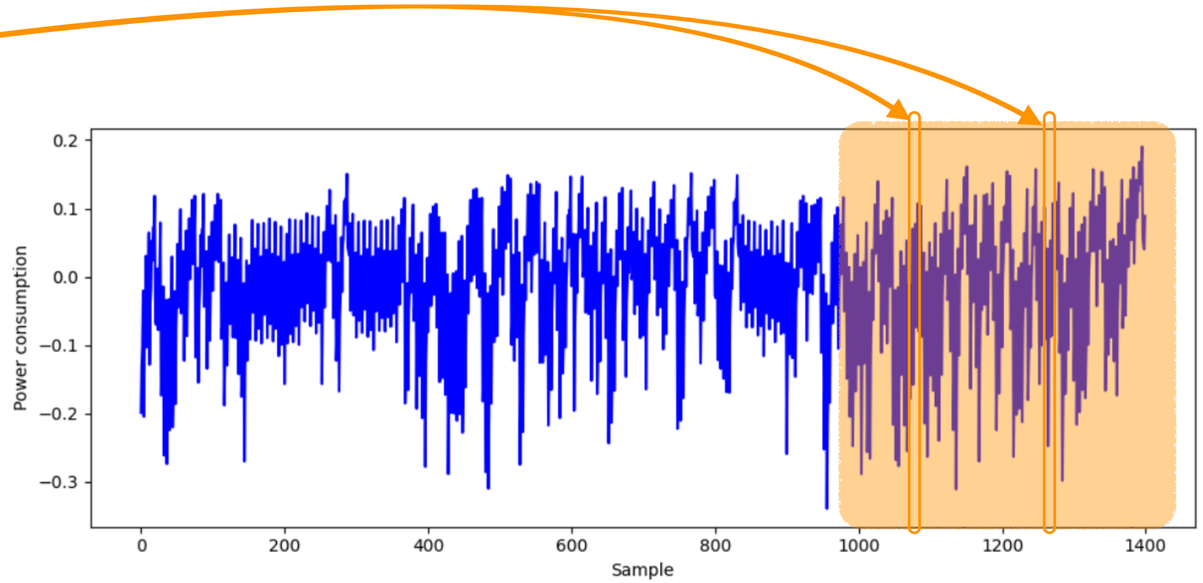


Optimizations:

- Attack point at linear layer output

Second-Order CPA

Combine two points on the trace
(by normalized product)



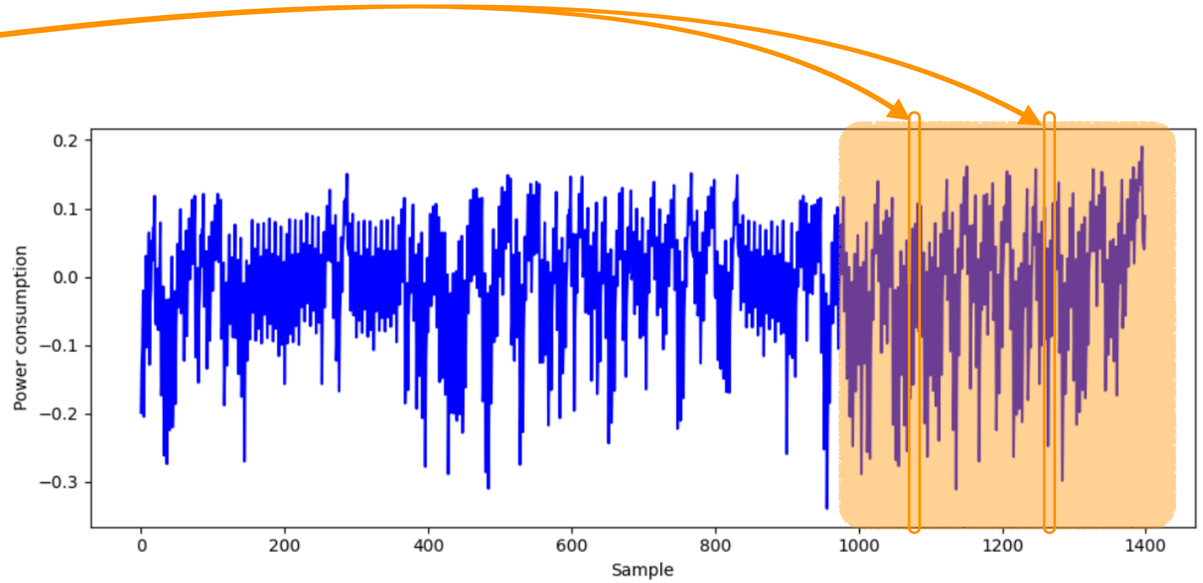
Optimizations:

- Attack point at linear layer output

  → focus on the right part of the trace

Second-Order CPA

Combine two points on the trace
(by normalized product)



Optimizations:

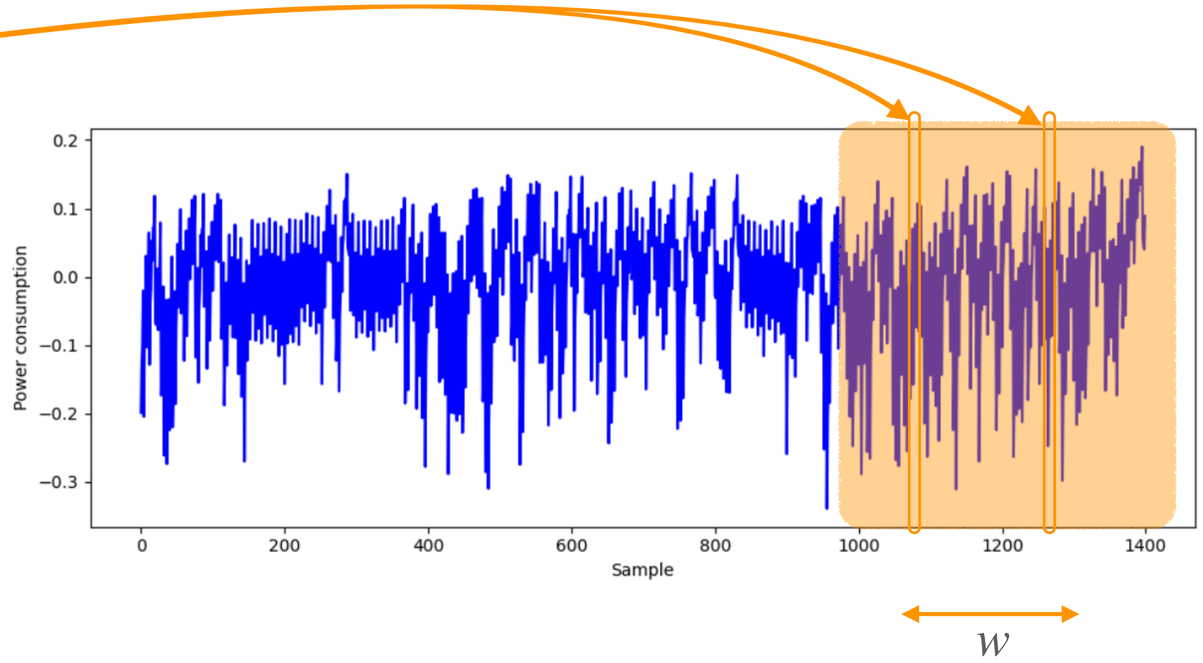
- Attack point at linear layer output

  → focus on the right part of the trace

- Two shares occur in a time span

Second-Order CPA

Combine two points on the trace
(by normalized product)



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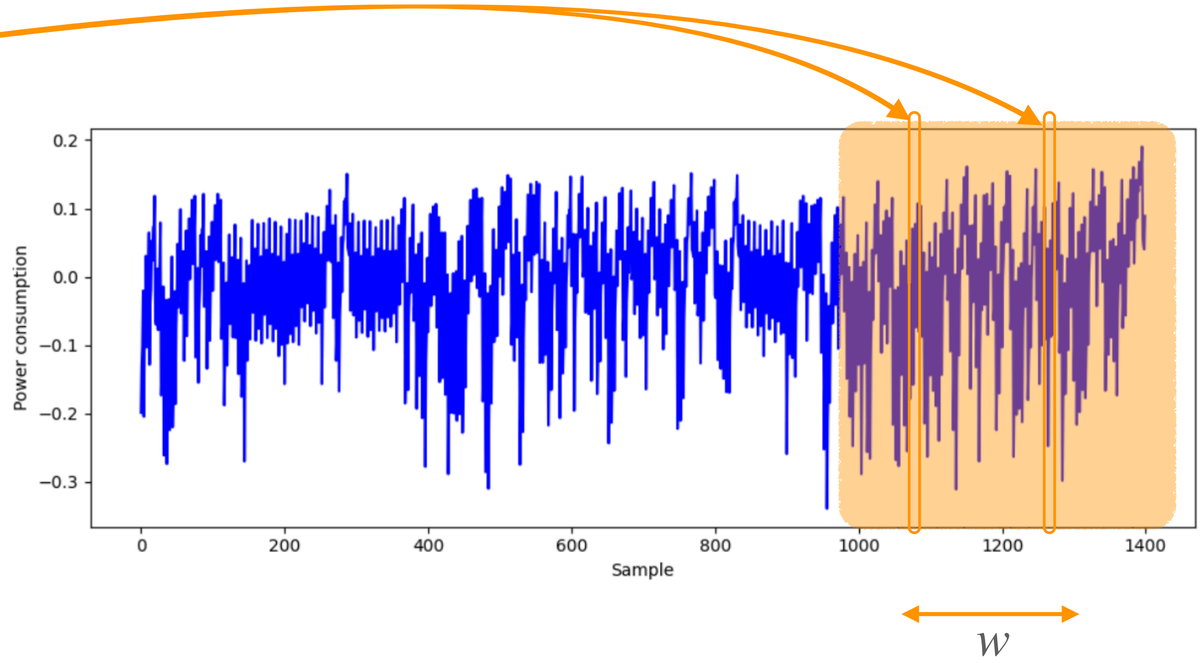

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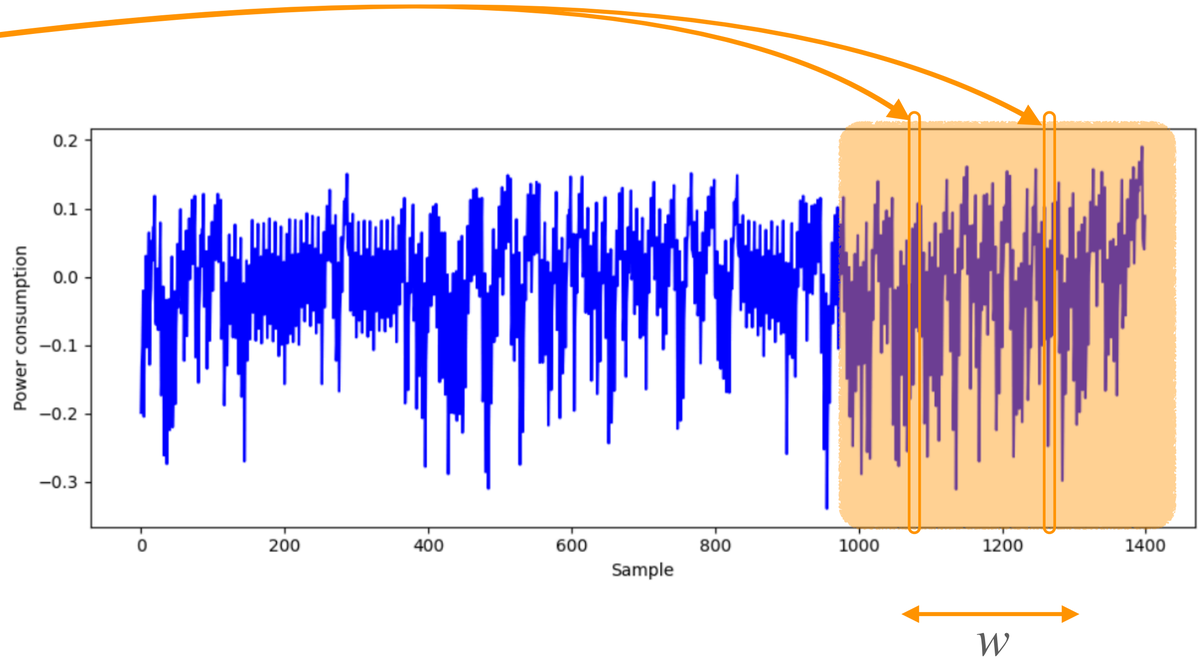

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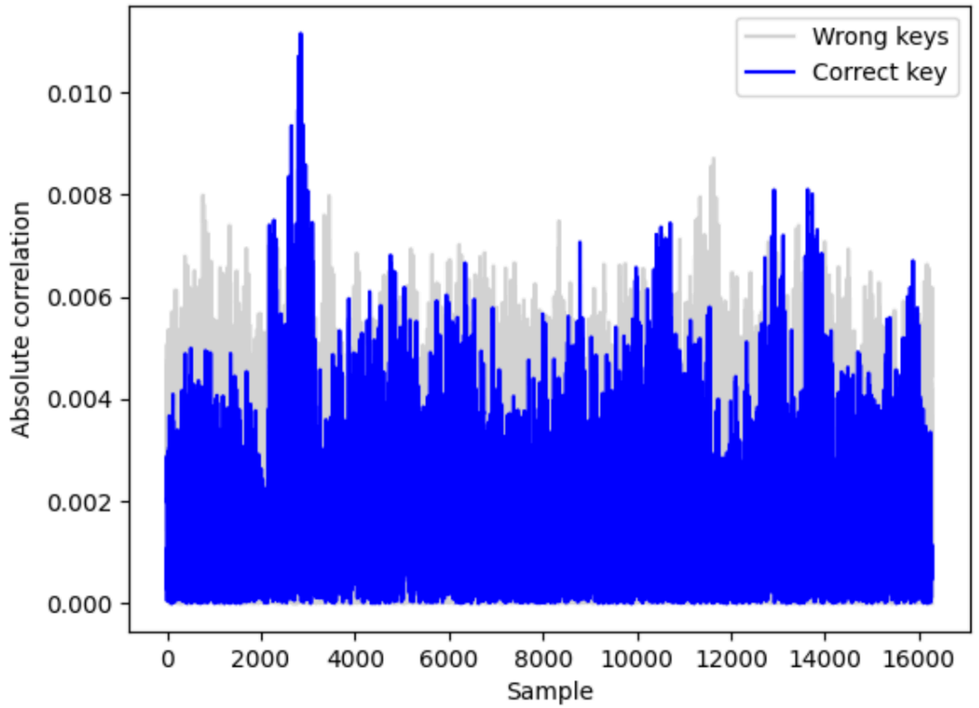
Our attack considers the last 350 samples

$w = 50$

Results

Results

Correlation traces



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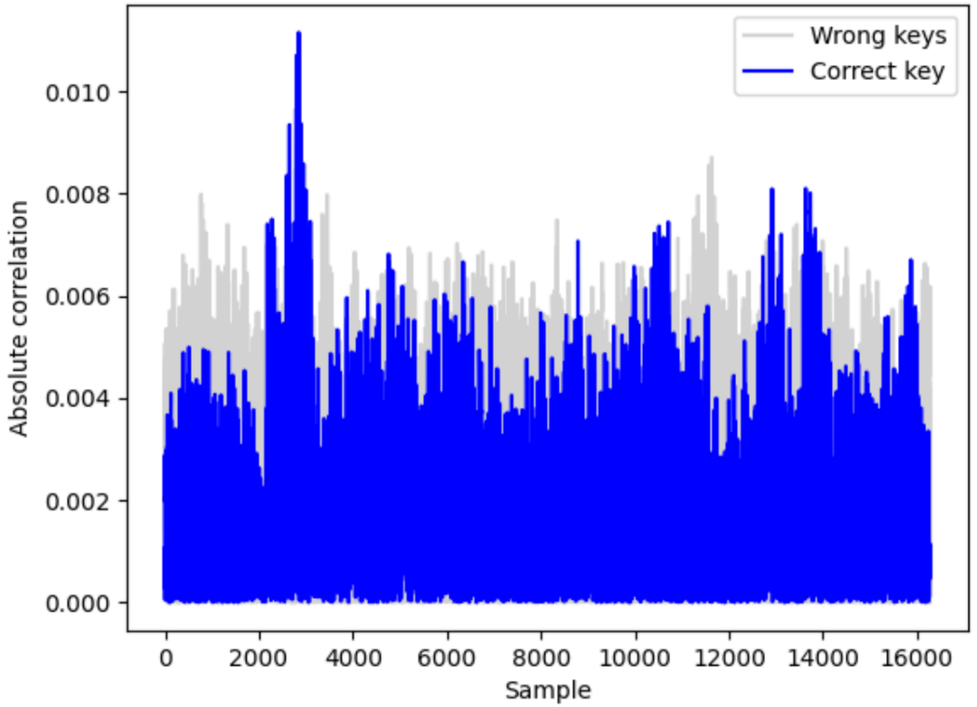
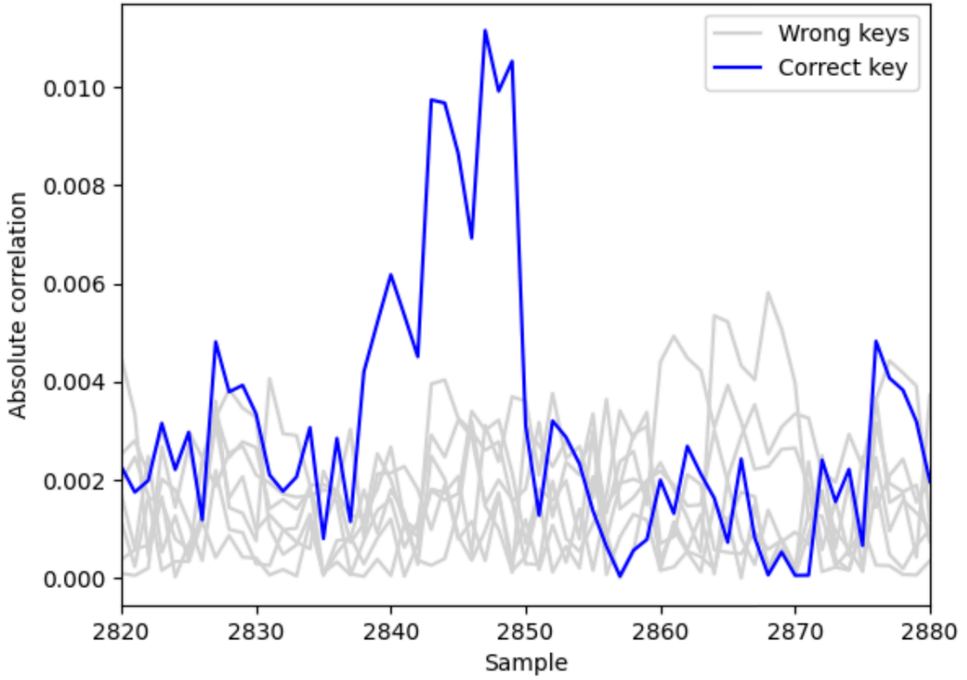


Correlation peak



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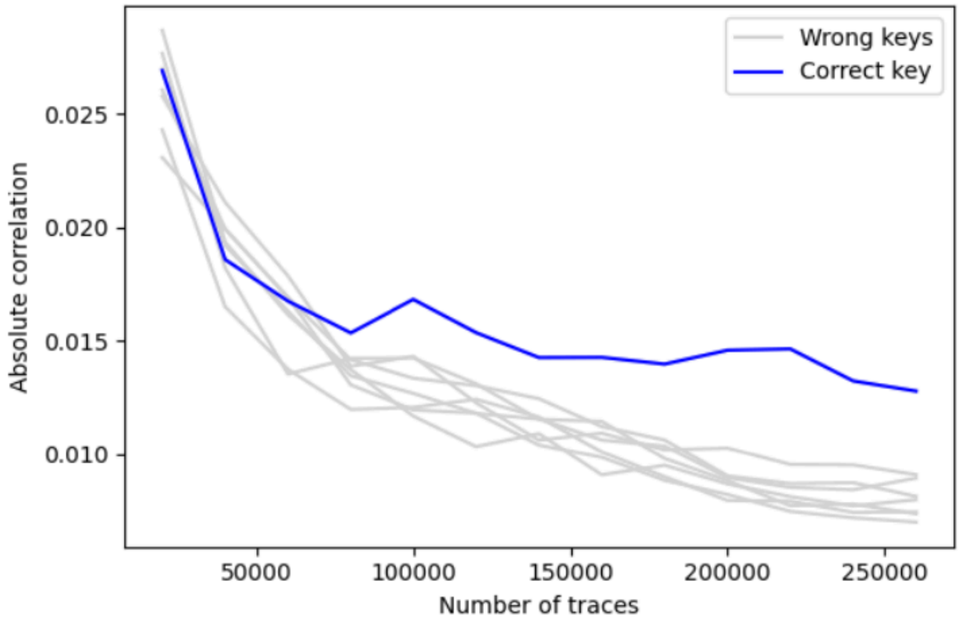
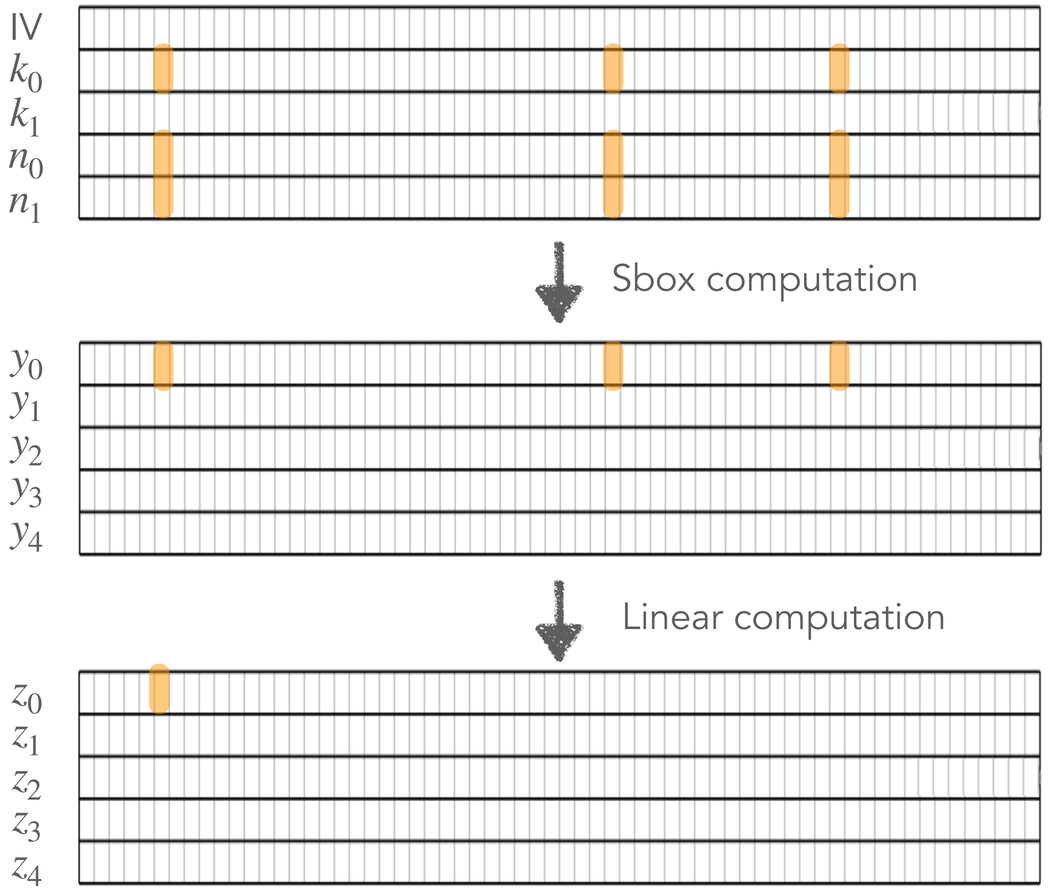
Correlation with increasing number of traces



CPA runs for full-key recovery



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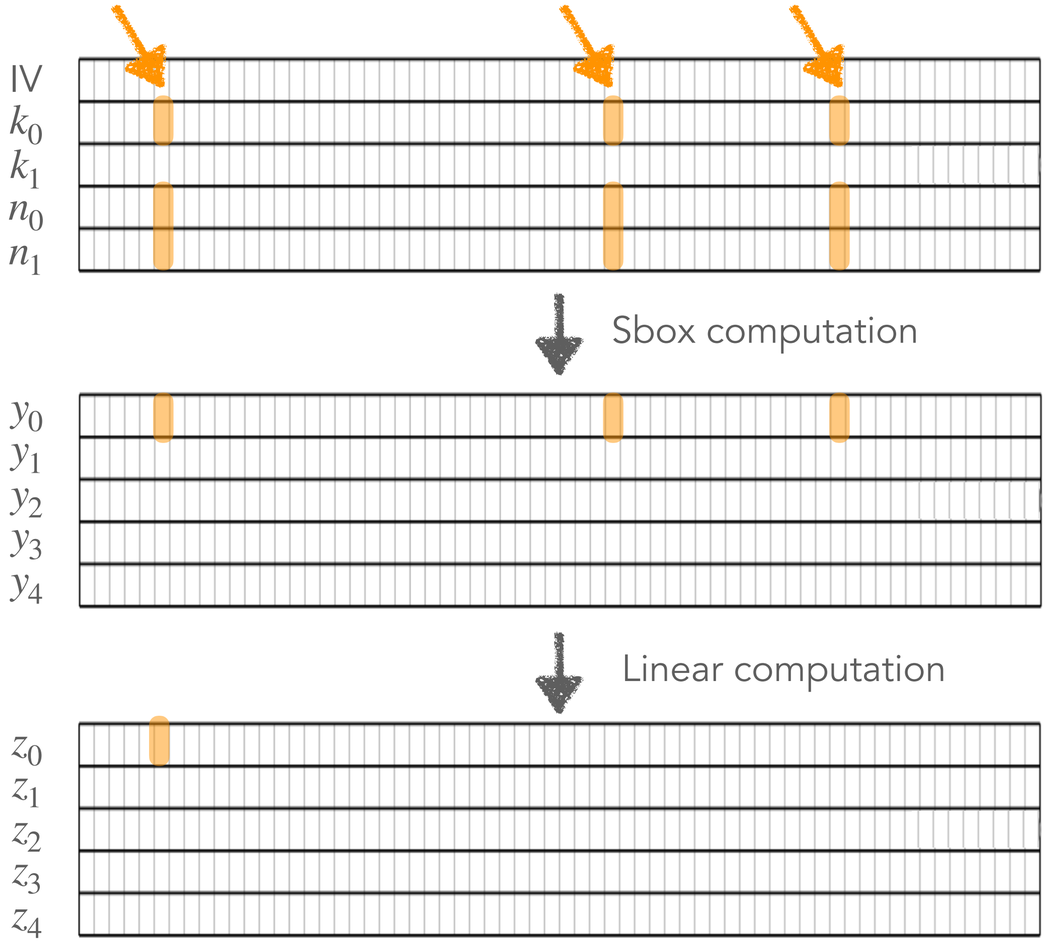
Each CPA run recovers 3 key bits



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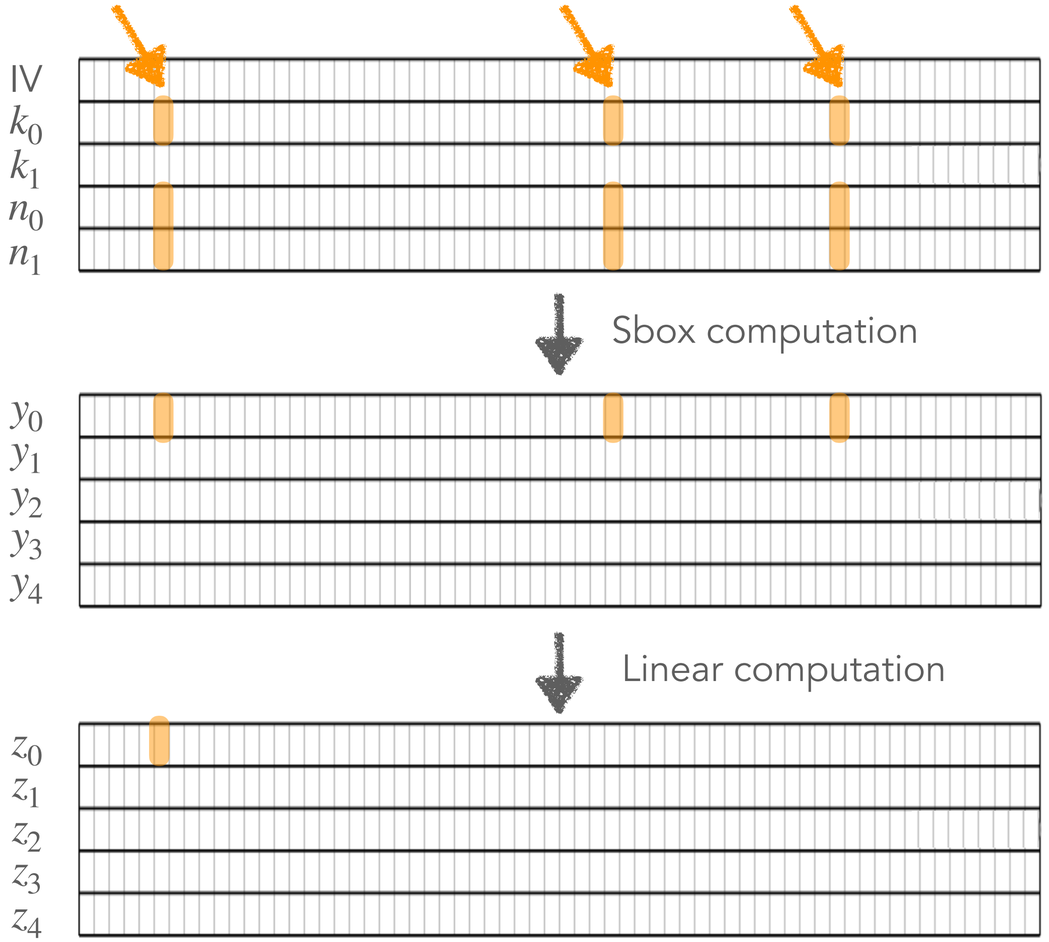
How many CPA runs to recover 128 key bits?



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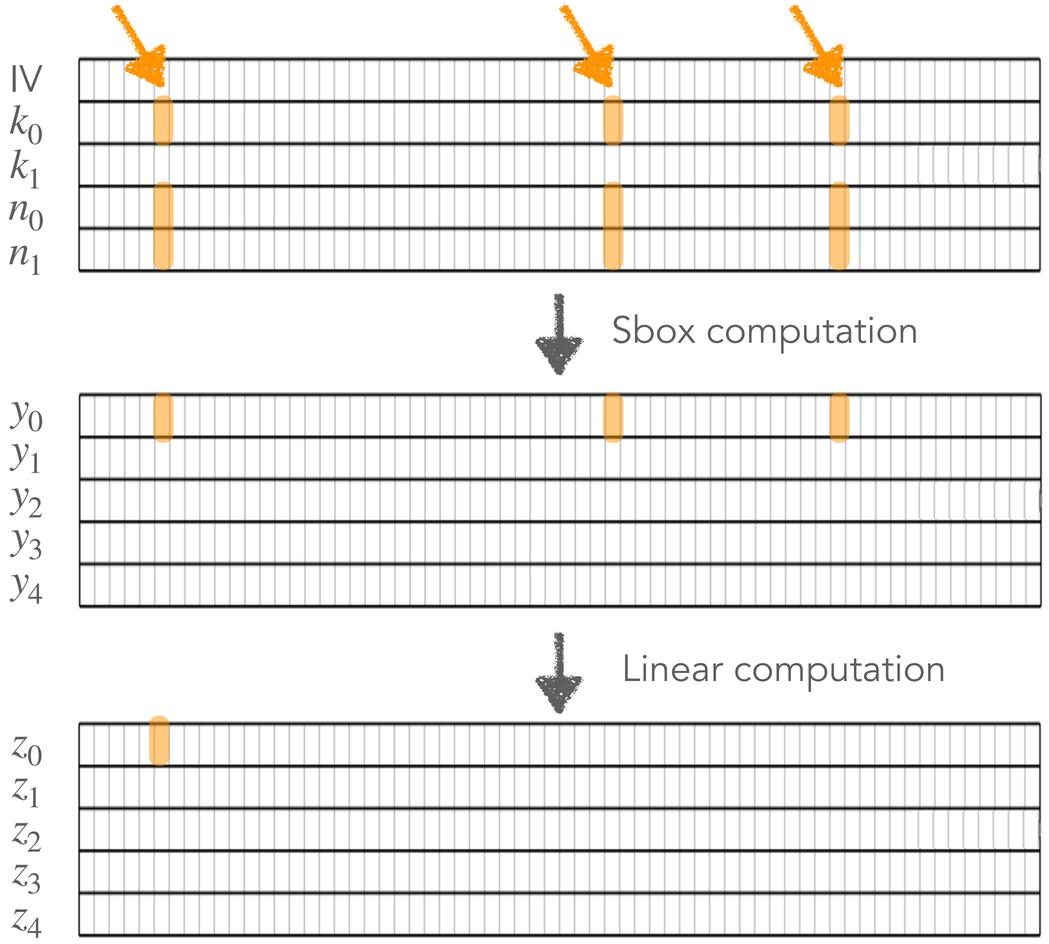

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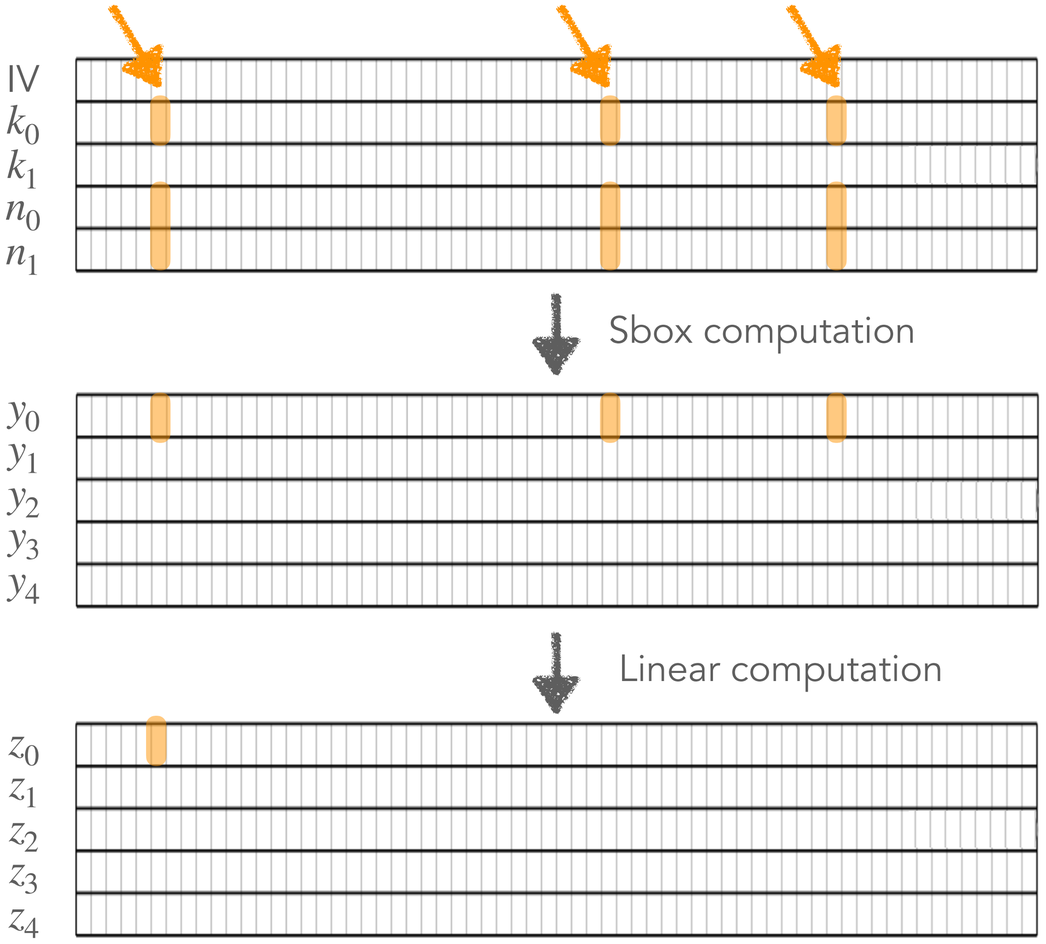

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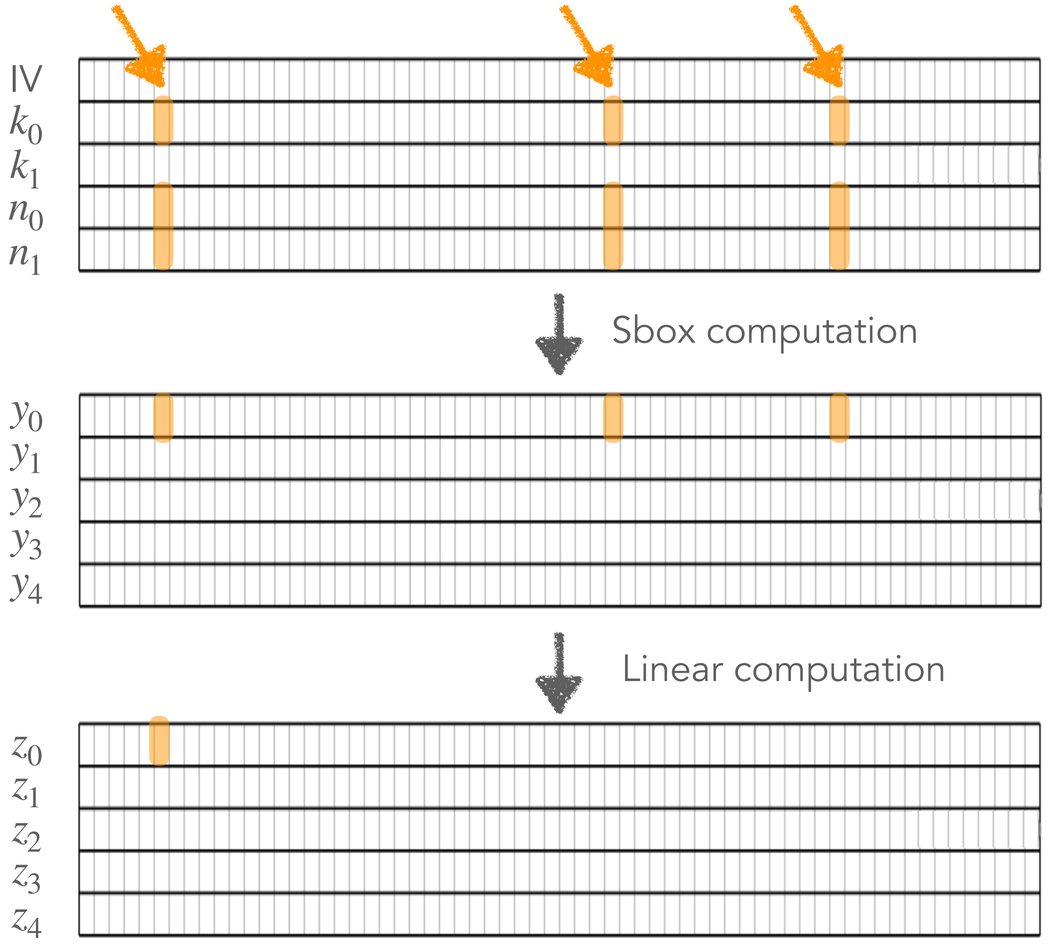
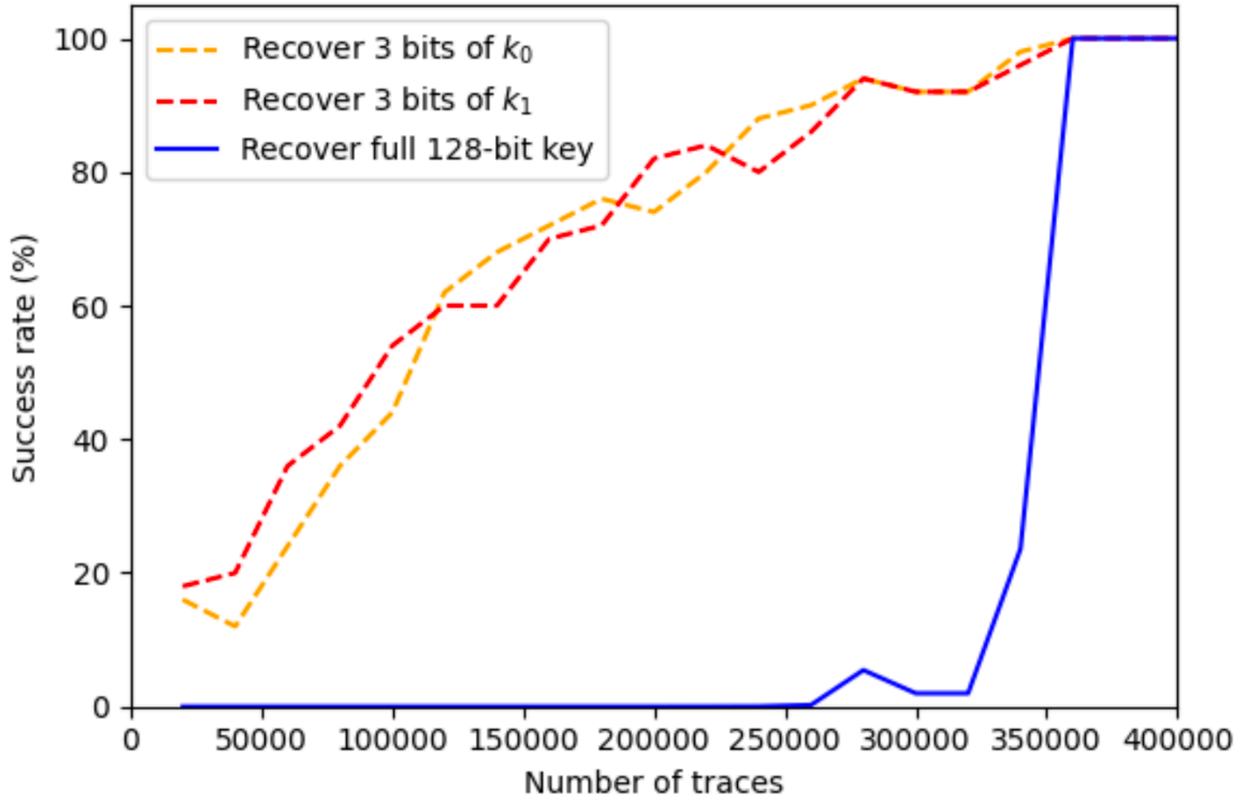
→ 47 CPA runs (optimal)



Full-key recovery

Full-key recovery

Success rates



Full-key recovery

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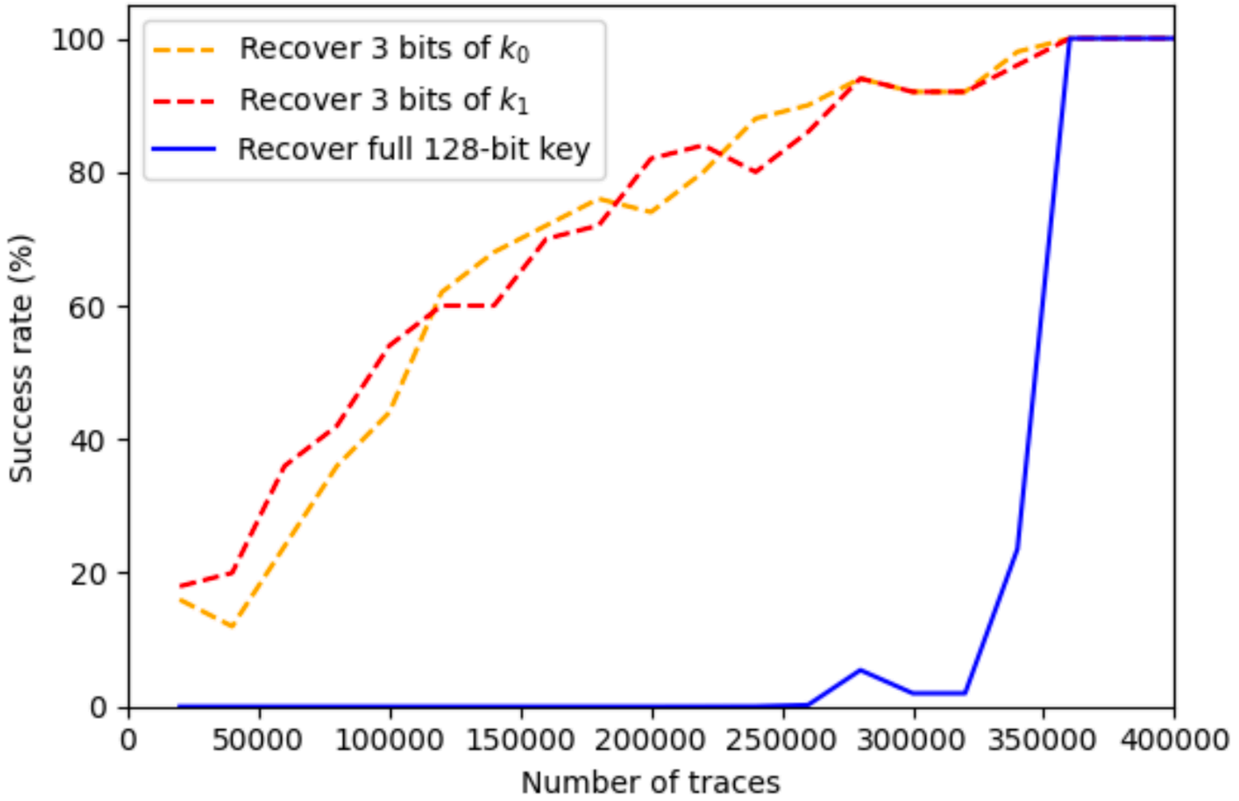

360K traces ensure 100% success rates

Recover full key in 4.7 hours

Summary

CPA attack on Ascon

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- Quadratic boolean Sbox function
  - choose the selection function carefully

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- Optimal number of CPA runs for full-key recovery: 47
- First results on practical second-order CPA

Practical Second-Order CPA Attack on Ascon with Proper Selection Function

Viet-Sang Nguyen

joint work with Vincent Grosso and Pierre-Louis Cayrel

CASCADE Conference

Saint-Etienne, 2 April, 2025

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